

D

C

- B

A

ENG APPR *Ed Knaplan*

TEST ENG
APPR

ENGINEER. Ed Knapton

VISUAL #. PS076-000

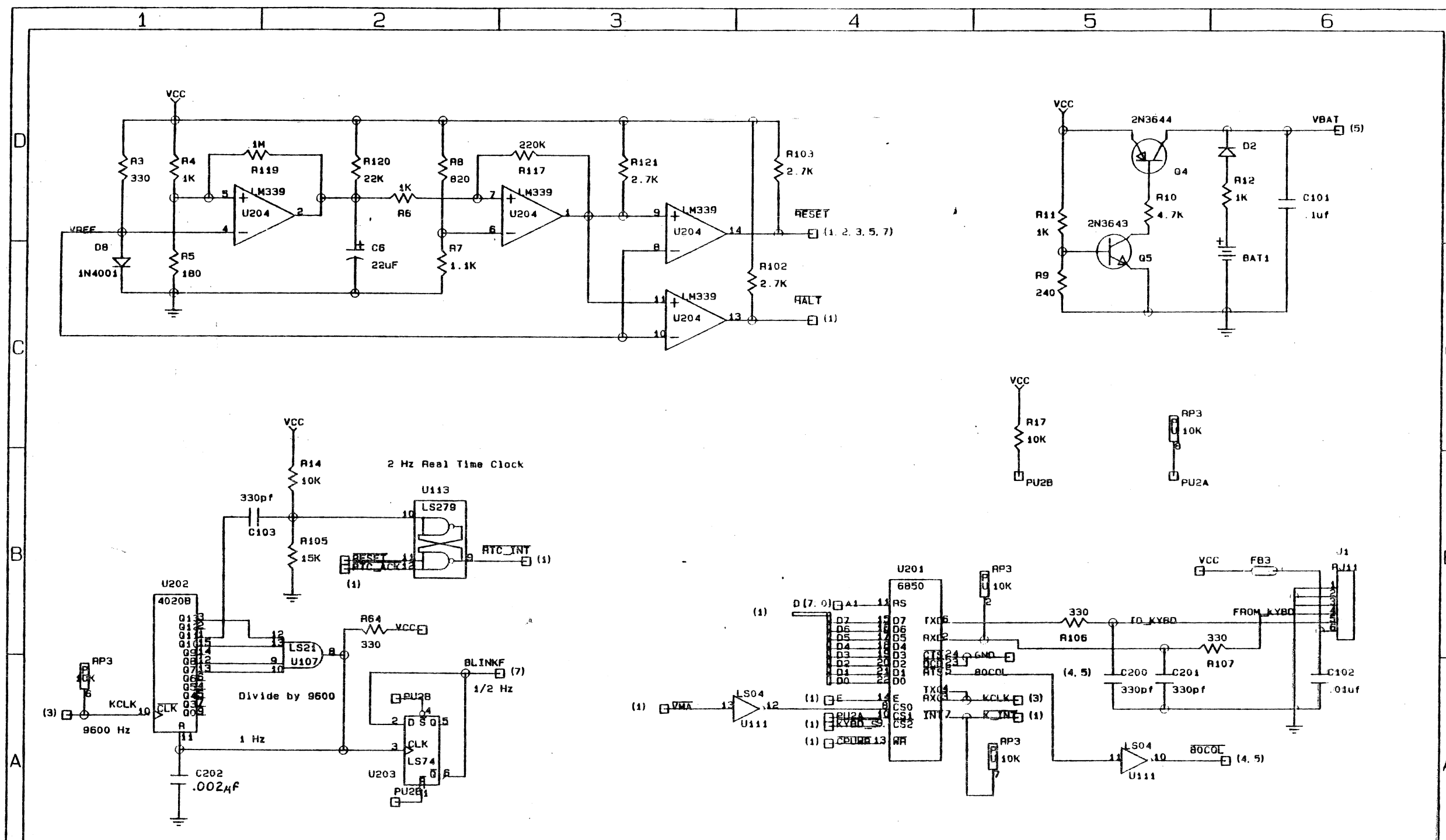
TITLE:	V6xx SCHEMATICS
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Cover Page

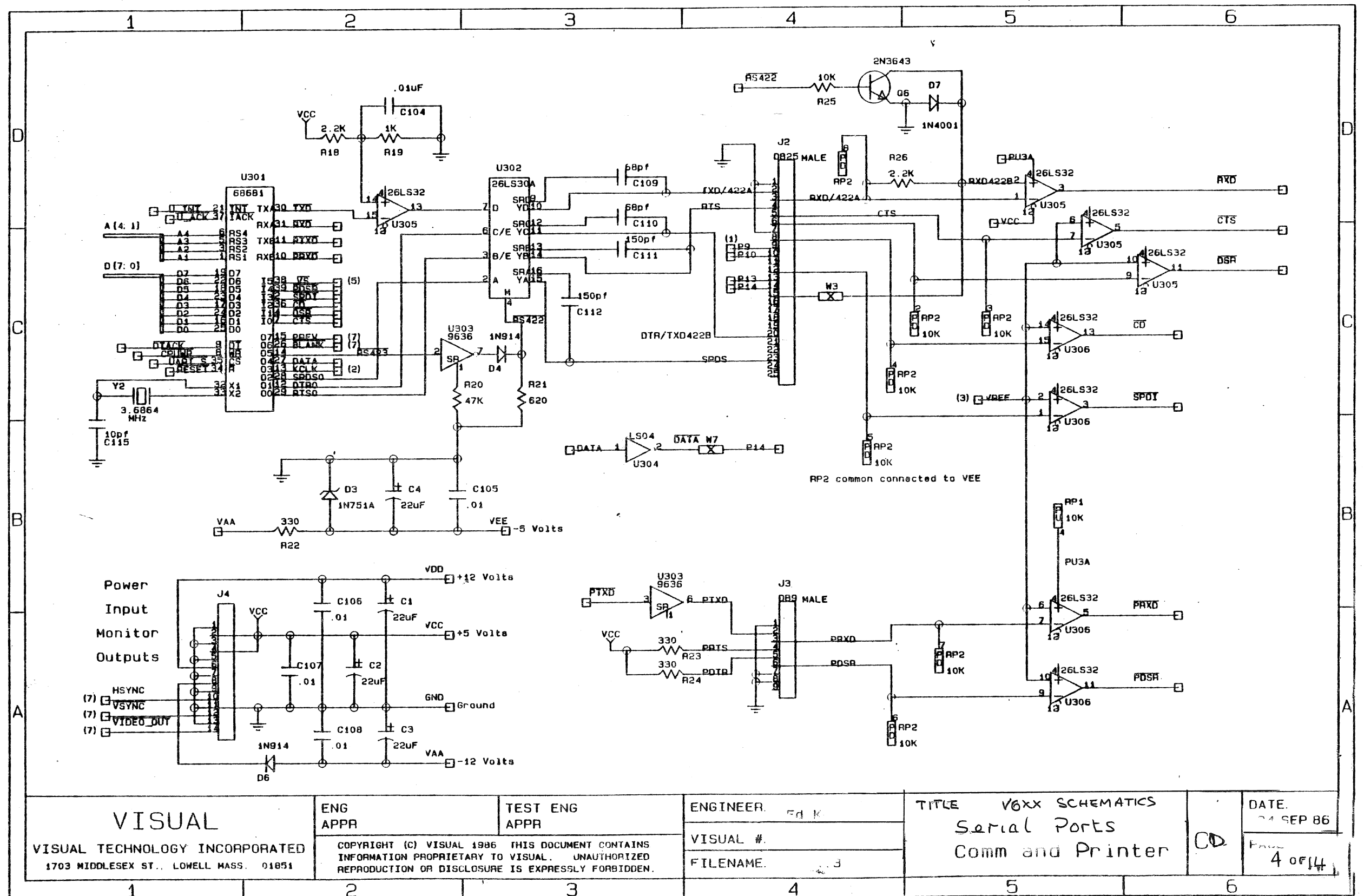
REV.	DATE.
CD	24 SEP 86
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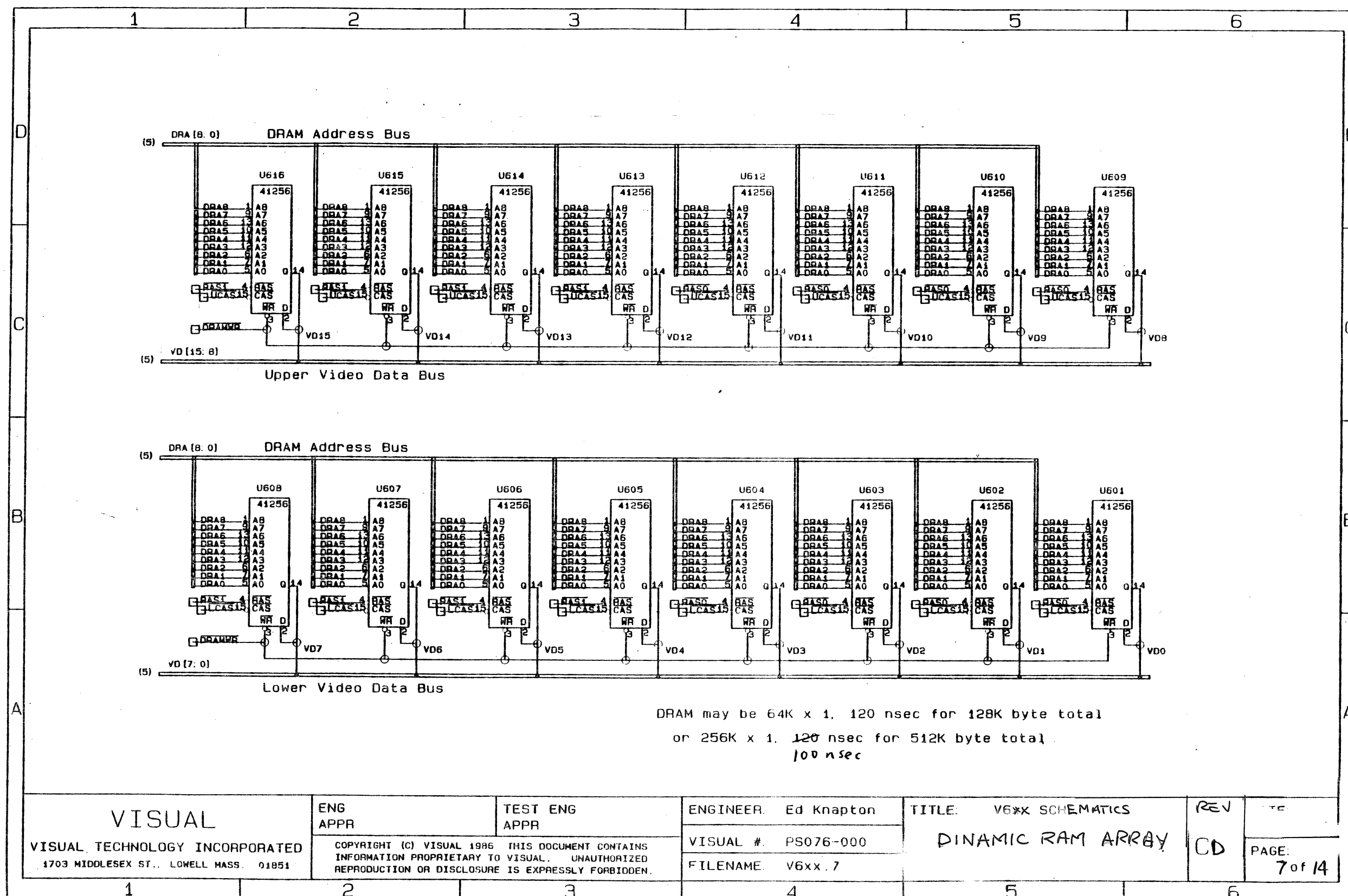
REVISION STATUS						
REV	ECO	SHEET	DESCRIPTION	DATE	APPROVAL	
1			initial design	27FEB86		
1a		all	ref. desig. updated to PCB layout	10MAR86		
2		all	results of debugging rev 1	18APR86		
2a		0 to 7	more debugging of rev 1. plus results of rev 2 PCB layout use rev 2 sheets 8 thru 13	16MAY86		
2b		1, 3, 7	sht 1. fix race condition in RAM Sw sht 3. fix rec. data input threshold sht 7. remove 2 spares sheet 14 added for rework instructions	30MAY86		
3		all	results of debugging rev 2. plus new layout for new enclosure	14JUL86		
A			Rev 3 renamed to Rev A			
B		mainly 2, 4	battery circuit modified VIOCYC* timing changed CPUACK* glitch cleanup	31JUL86		
CA	1532	3, 4, 5	various timing fixes component spacing changes	24SEP86	Ed Knapton	
CB	1592	N/A	CHANGED PA076 DWG TO -AOX; CREATED A02		Ed Knapton	
CC	1612	3	LOC. C202 .0024F WAS 470PF	16MAR87	Ed Knapton	
CD	1631	8	PCC + P/M power	5/29/87	HDS	

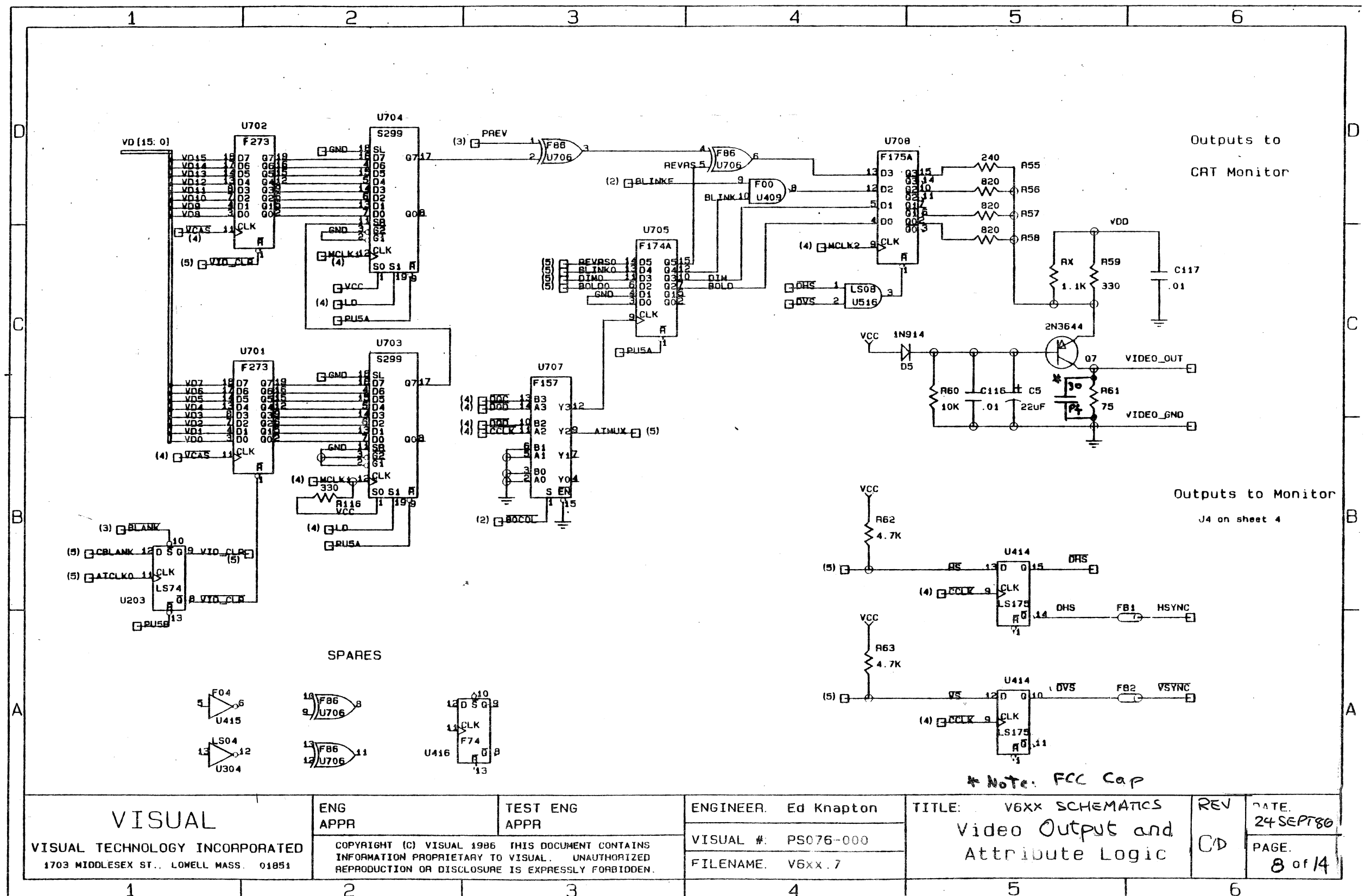
ENGINEER. Ed Knapton	TITLE: V6xx SCHEMATICS Cover Page	REV.	DATE.
VISUAL #. PS076-000		CD	24 SEP 86
FILENAME. V6xxdoc. 1			PAGE. 1 of 14

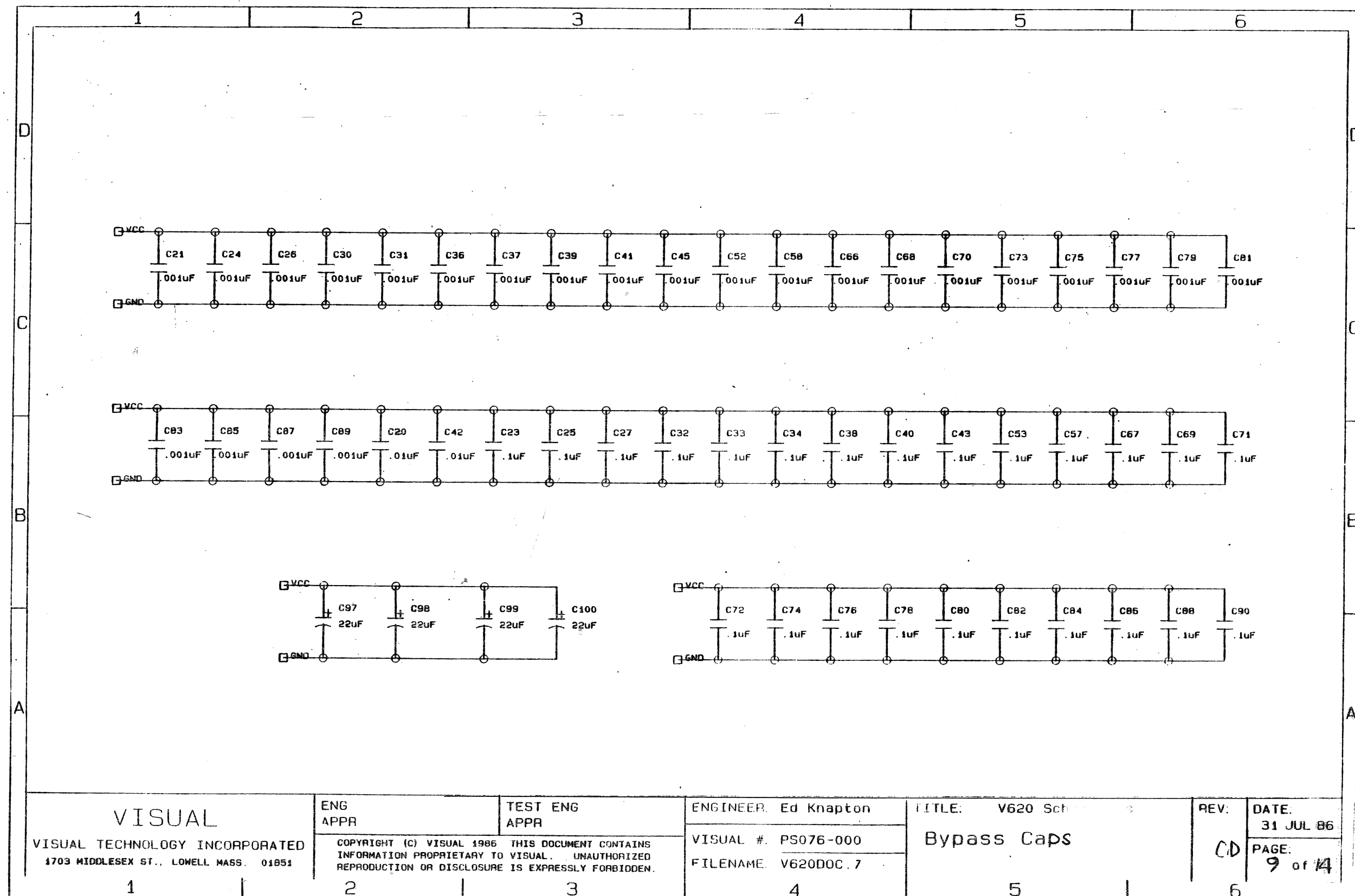


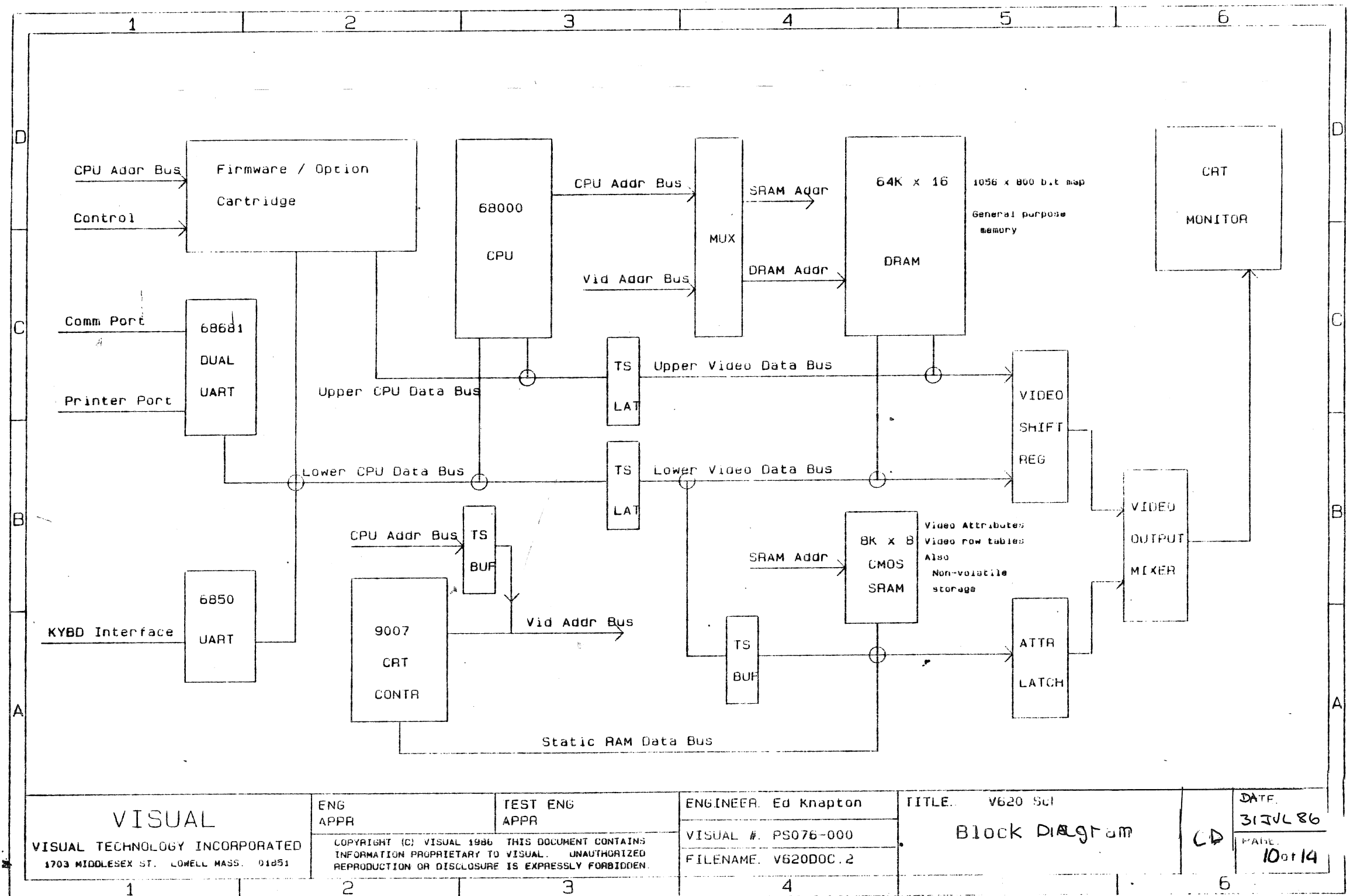
VISUAL VISUAL TECHNOLOGY INCORPORATED 1703 MIDDLESEX ST., LOWELL MASS. 01851	ENG APPR TEST ENG APPR	ENGINEER. Ed Knapton VISUAL #. PS076-000 FILENAME. V6xx.2	TITLE. V6xx SCHEMATICS Keyboard Interface RT Clock and Reset	REV. Cb DATE. 24 SEP 86 PAGE. 3 of 4
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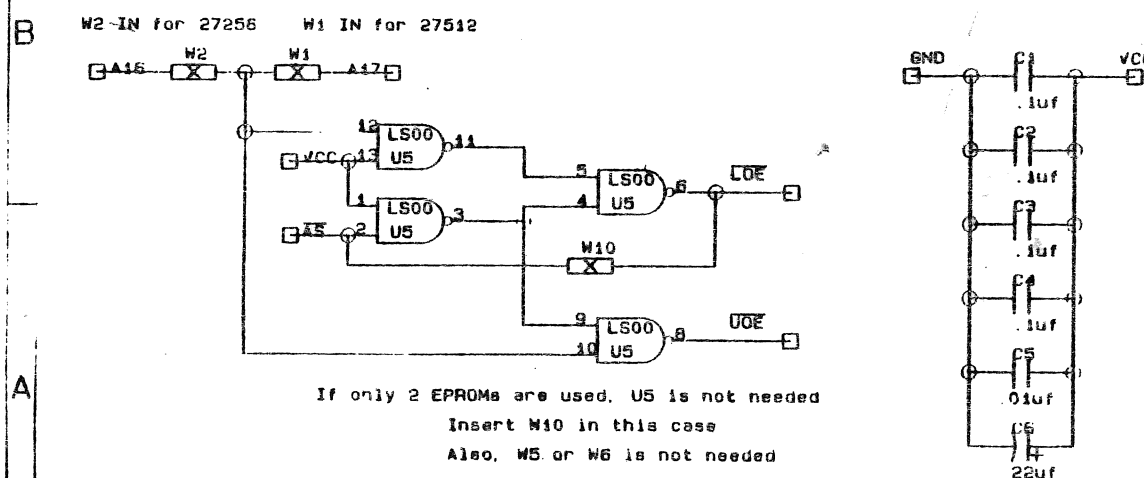
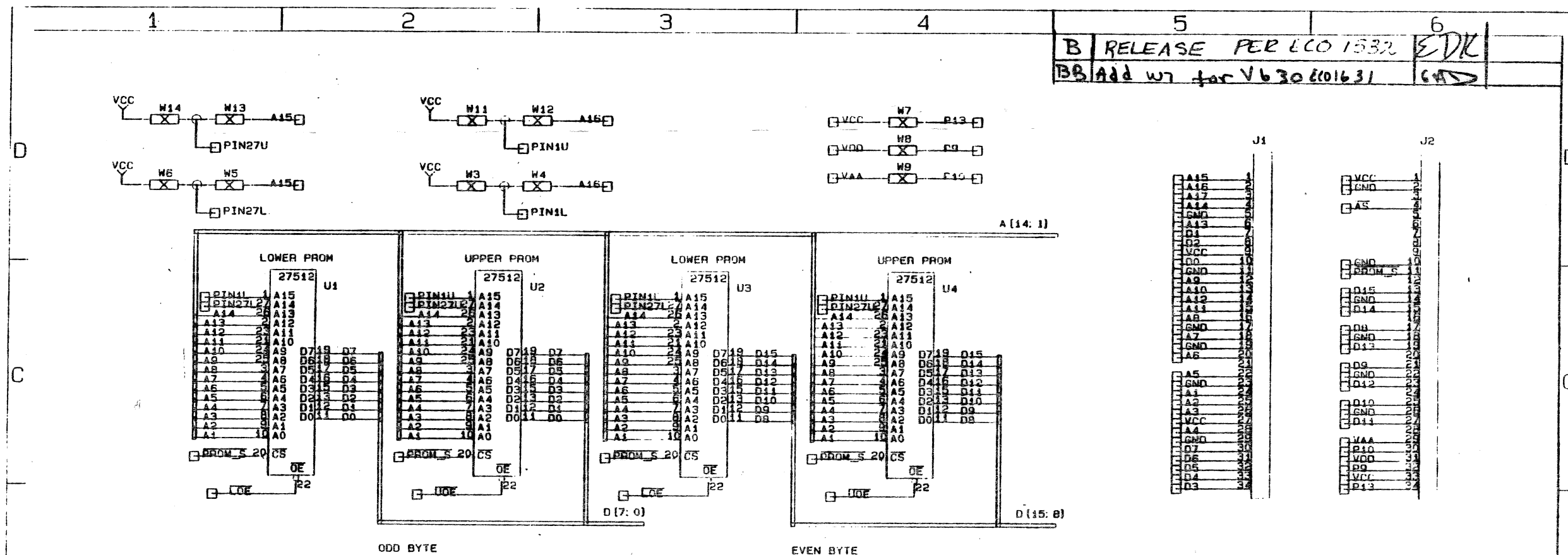








B RELEASE PER LCO 1532 EDK
BB Add w7 for V630 601631 CAD



Note: W7 must be installed on ALL V630 units

Configurations

Firmware size	U1	U2	U3	U4	U5	W2	W1	W10	W6	W5	W3	W4	W14	W13	W11	W12
16K	2764		2764						IN	IN	IN					
32K	27128		27128						IN	IN	IN					
64K	27256		27256						IN	IN	IN					
80K	27256	2764	27256	2764	74LS00	IN				IN	IN		IN		IN	
96K	27256	27128	27256	27128	74LS00	IN				IN	IN		IN		IN	
128K	27256	27256	27256	27256	74LS00	IN				IN	IN			IN	IN	
128K	27512		27512						IN	IN		IN				
144K	27512	2764	27512	2764	74LS00	IN				IN		IN	IN		IN	
160K	27512	27128	27512	27128	74LS00	IN				IN		IN	IN		IN	
196K	27512	27256	27512	27256	74LS00	IN				IN		IN		IN	IN	
256K	27512	27512	27512	27512	74LS00	IN				IN		IN		IN		IN

All EPROMs must be 250 nsec or faster

VISUAL

VISUAL TECHNOLOGY INCORPORATED
1703 MIDDLESEX ST., LOWELL MASS. 01851

ENG APPR EDKnapton
TEST ENG APPR

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ENGINEER. Ed Knapton

TITLE.

VISUAL #. PS078-000

FILENAME. EP

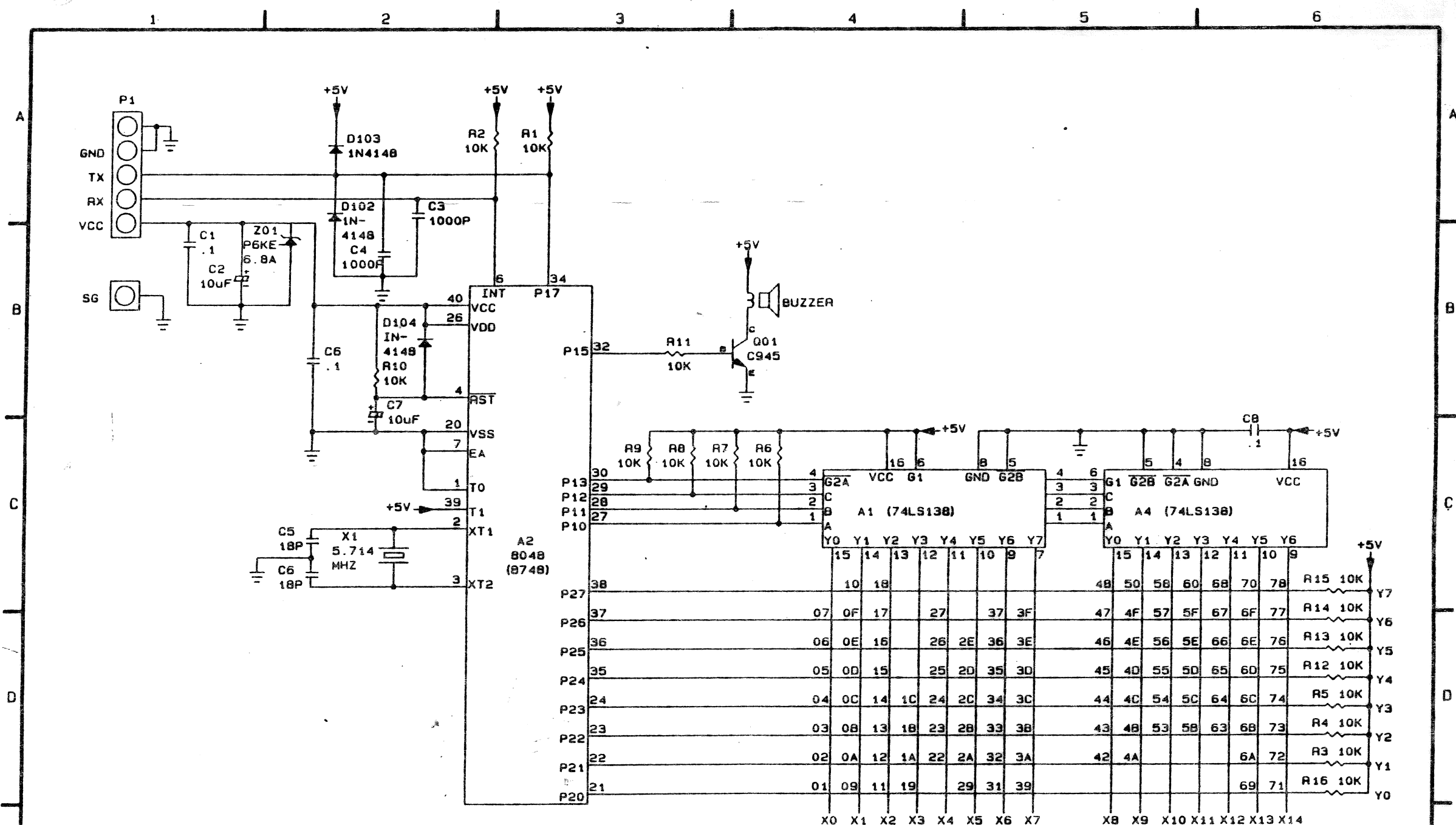
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EPROMS

REV.

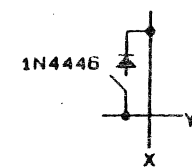
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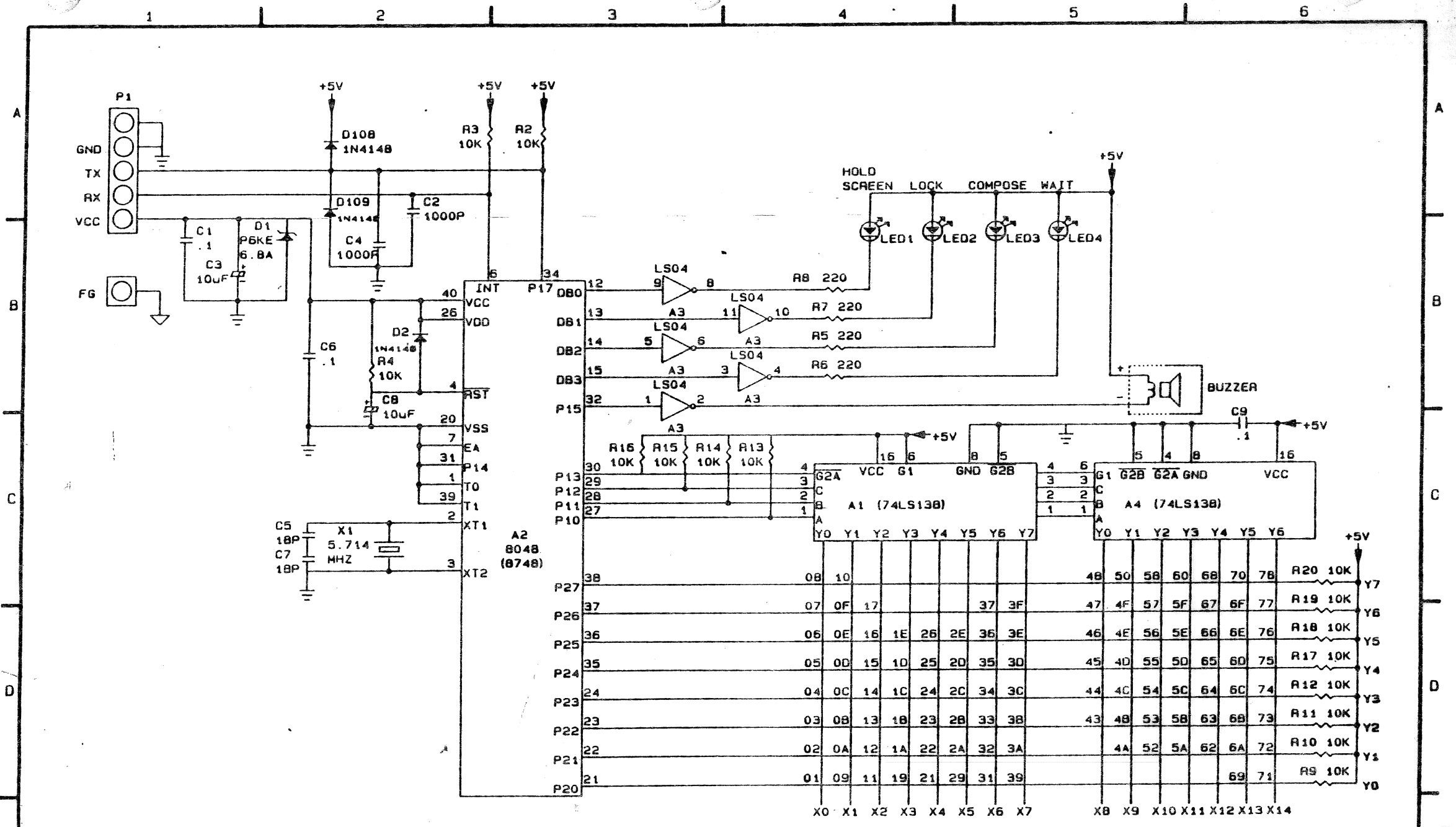
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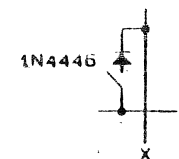
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REVISIONS 1985.5.21---- (1)	APPROVED	B. W. H.	Dec. 12, '86	SCALE (1)	VA620 KB021 CIRCUIT		
	CHECKED						
	DESIGN						
	DRAWN	M. Y. LIN					
	REF. NO.						
					ELECT. & MECH. CO., LTD.	DWG. NO.	79VA6S0A24307



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KB019 & KB020 = V602, 603, 630
KB022 = V604

REVISIONS

1985.5.21----(1)

APPROVED	B. Wen	MAR. 24. '86	SCALE (MM)	DWG.	019
CHECKED			/	TITLE	KB020 KEY BOARD CIRCUIT
DESIGN	T.L. LIN	MAR. 24. '86	TECO ELECTRIC &	DWG.	
DRAWN	M.Y. LIN	MAR. 24. '86	MACHINERY CO., LTD	NO.	
REF. NO.					

