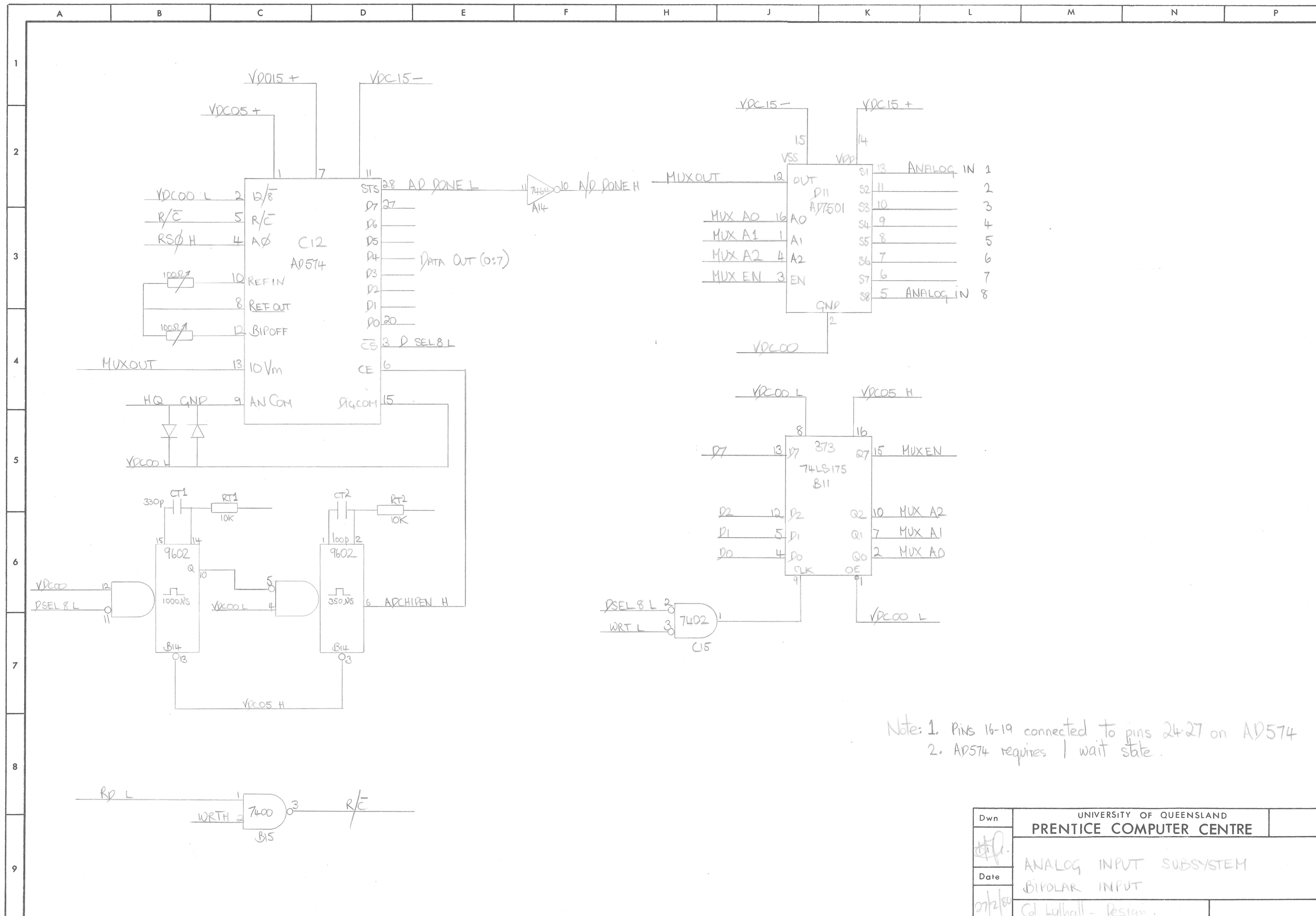
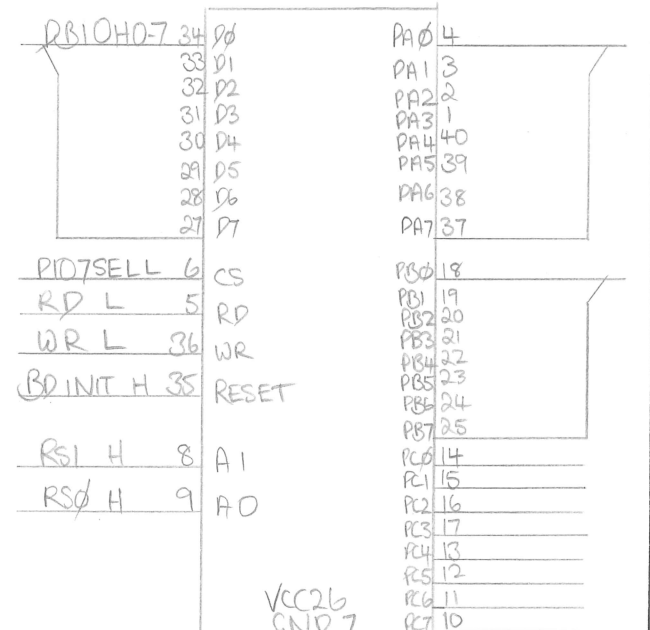
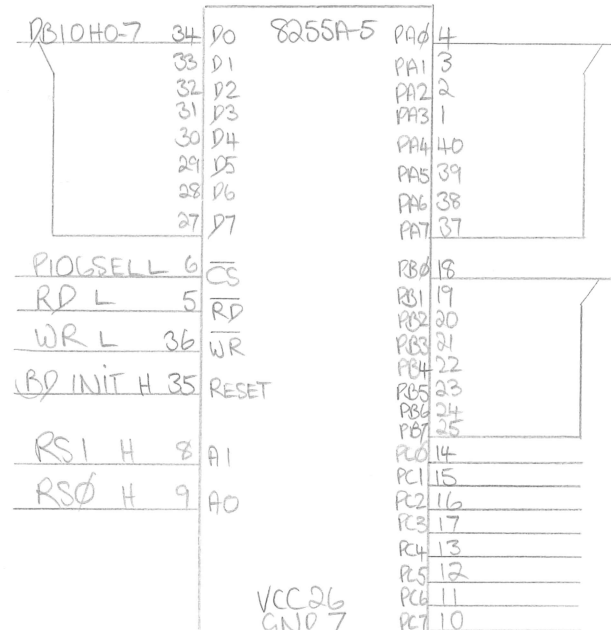
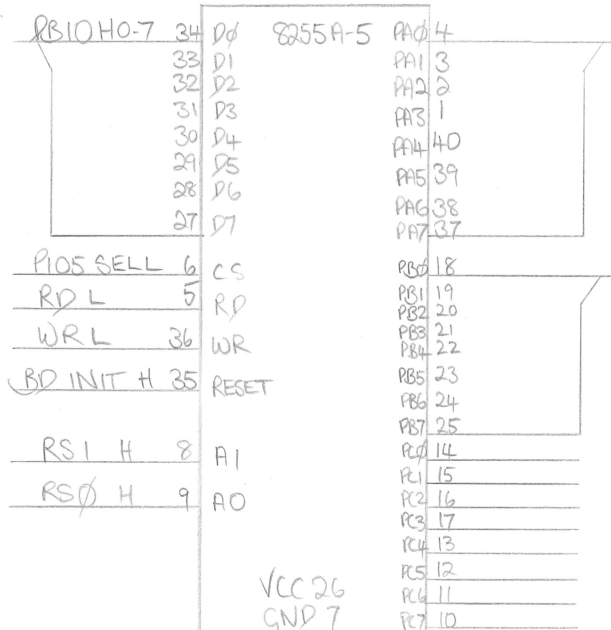
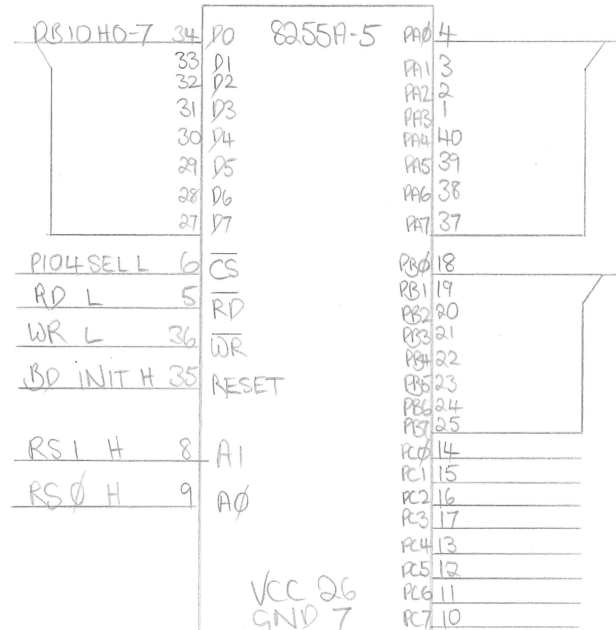
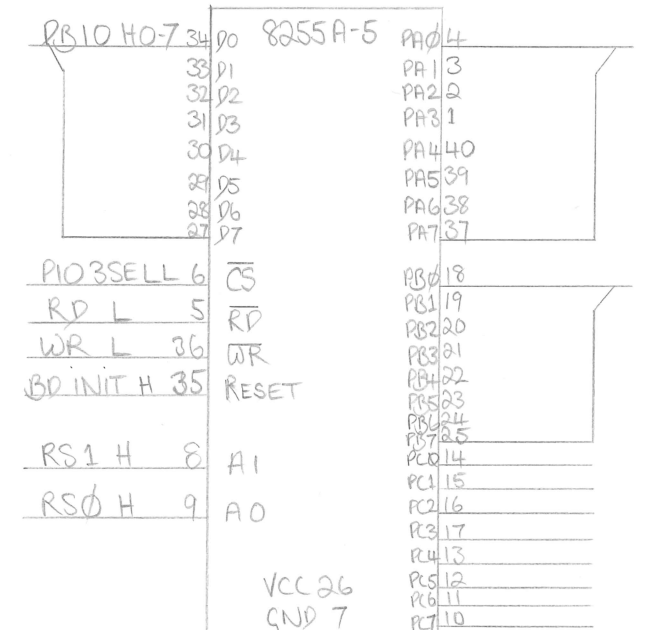
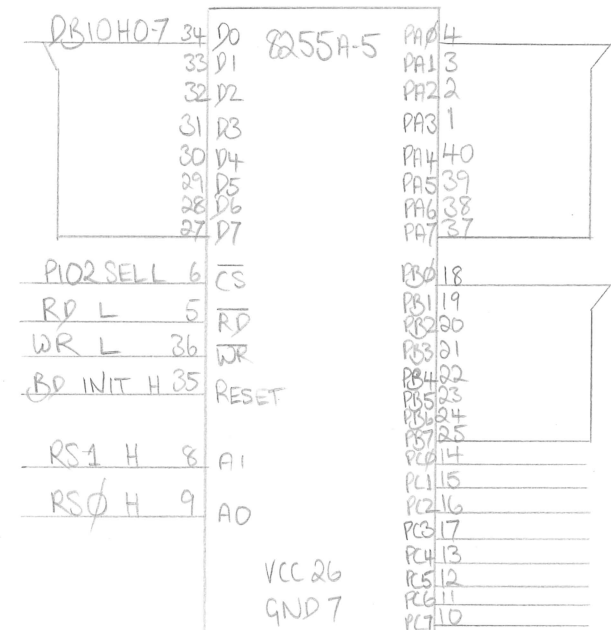
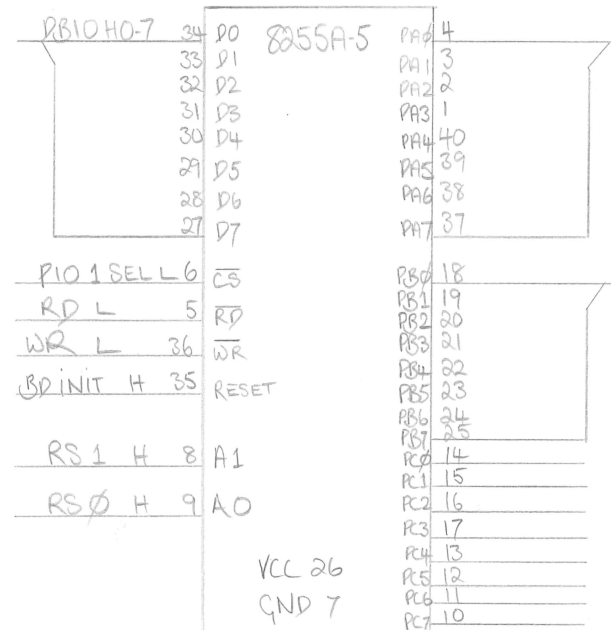
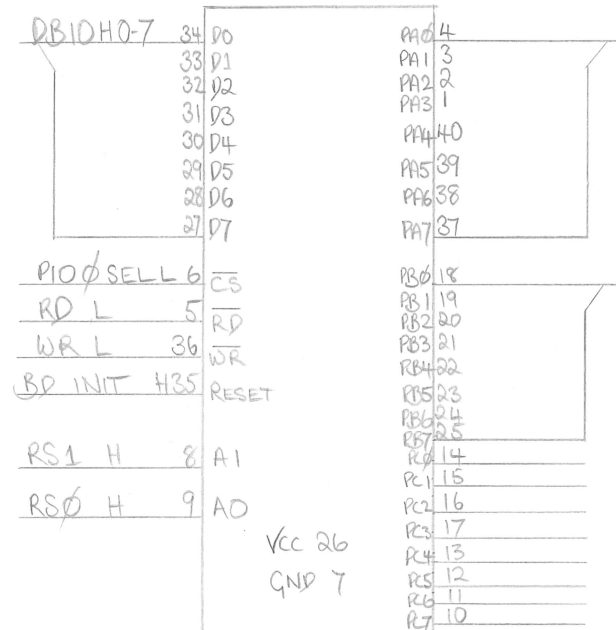




Note: 1. Pins 16-19 connected to pins 24-27 on AD574
 2. AD574 requires 1 wait state.

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Date	ANALOG INPUT SUBSYSTEM BIPOLAR INPUT	
27/2/80	Col Lythall - Design.	

	A	B	C	D	E	F	H	J	K	L	M	N	P
1													
2													
3													
4													
5													
6													
7													
8													
9													



Dwn	UNIVERSITY OF QUEENSLAND
	PRENTICE COMPUTER CENTRE
Micro Processor DATA	
Acquisition SYSTEM	
4/79	