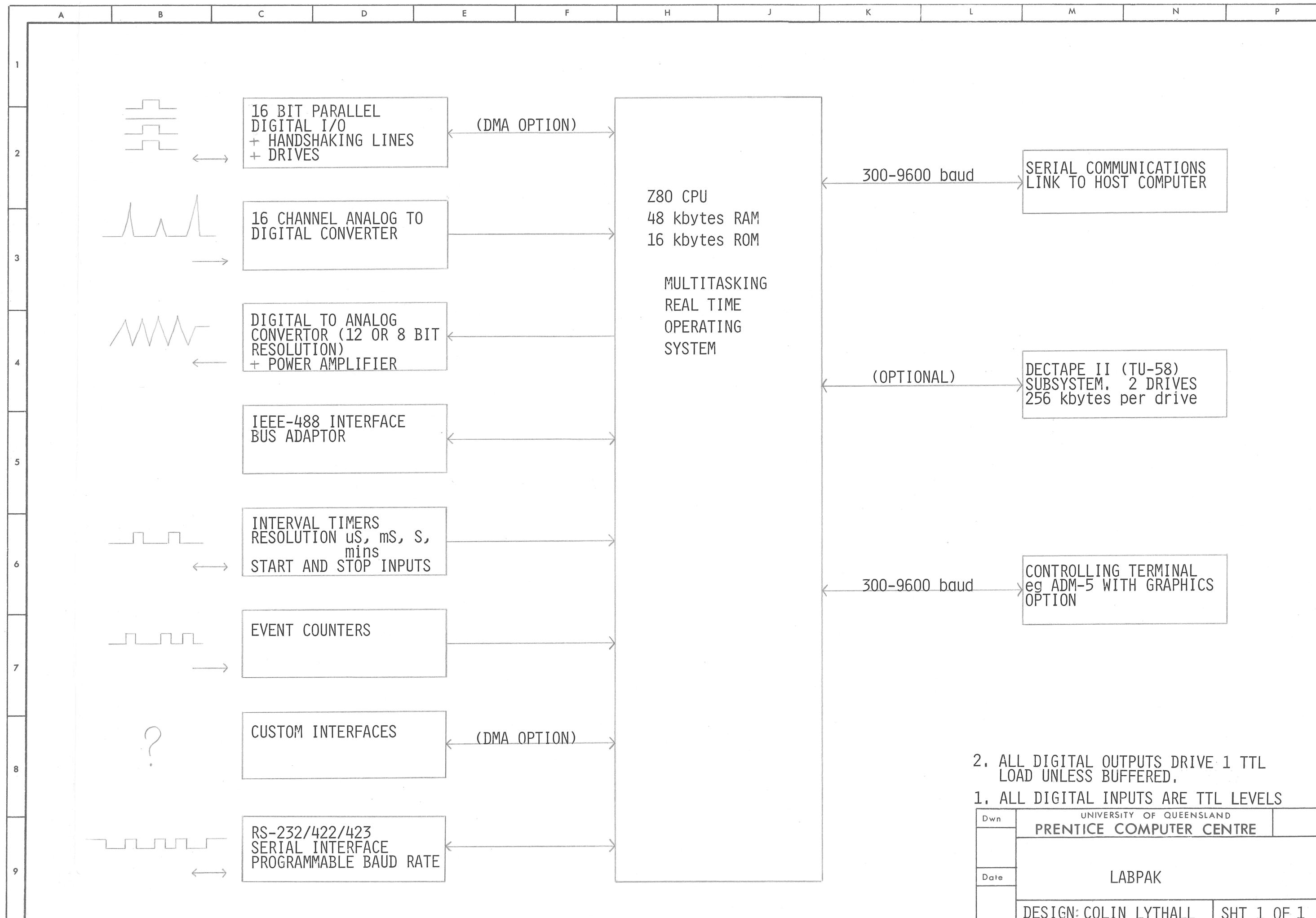
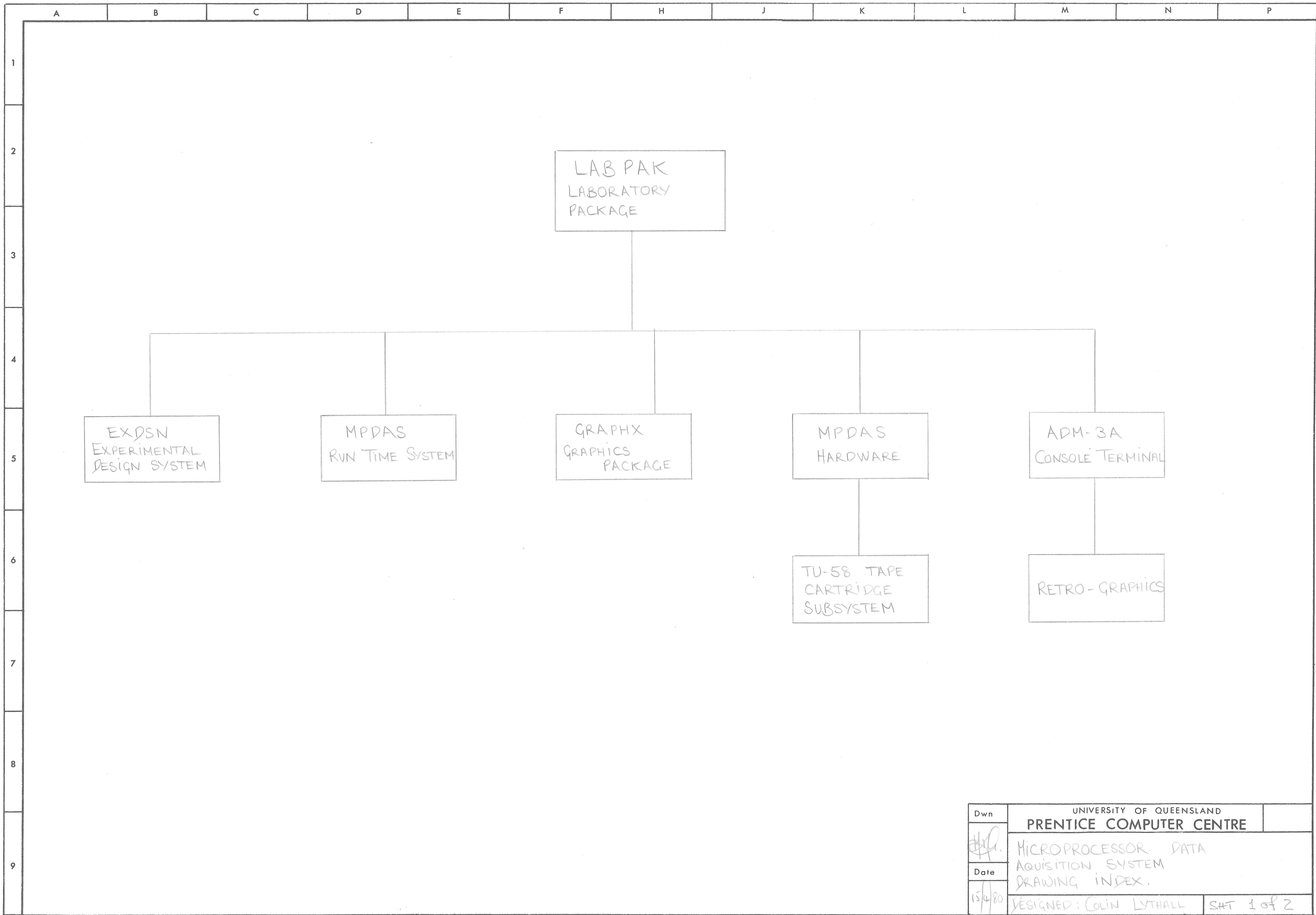
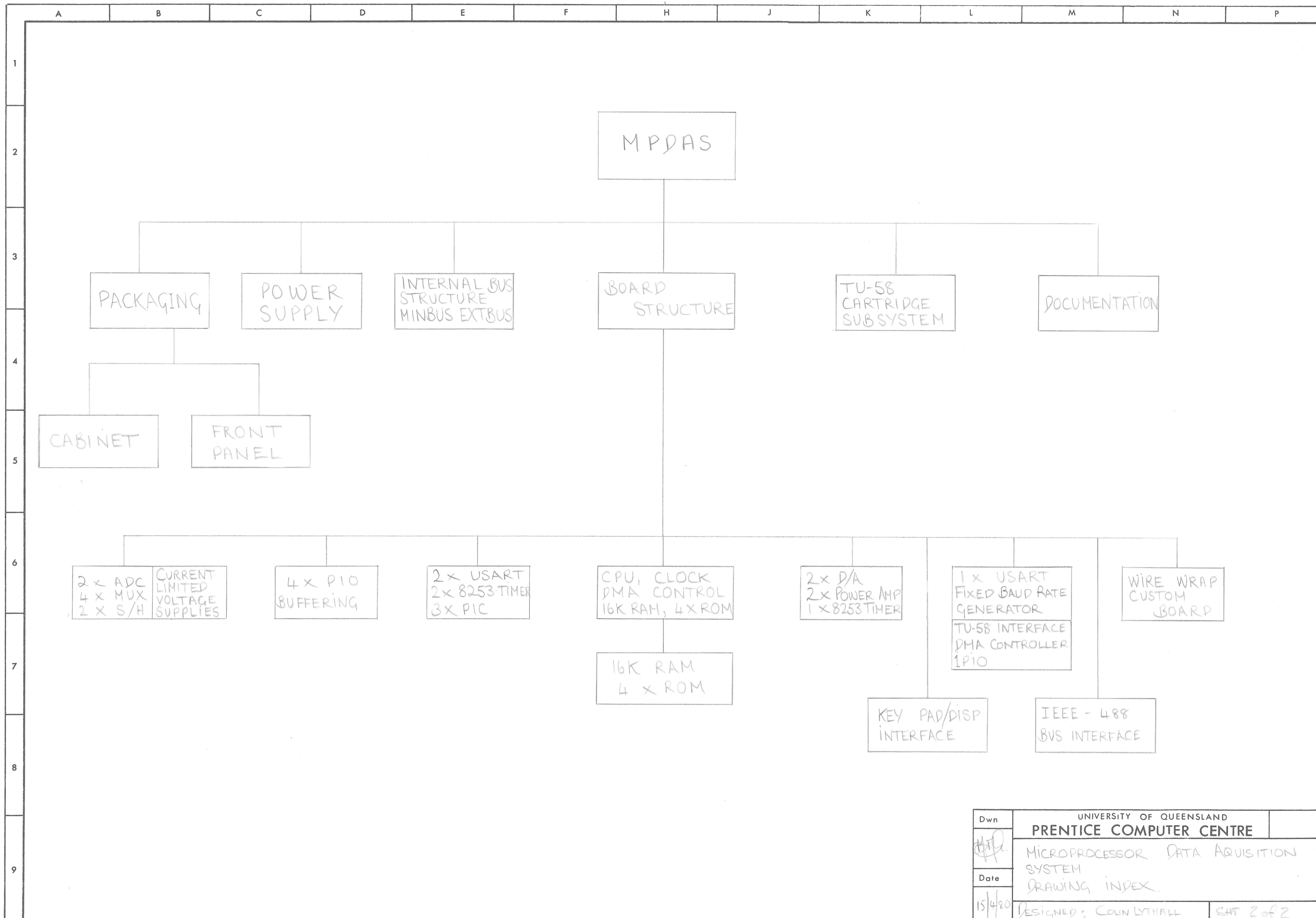
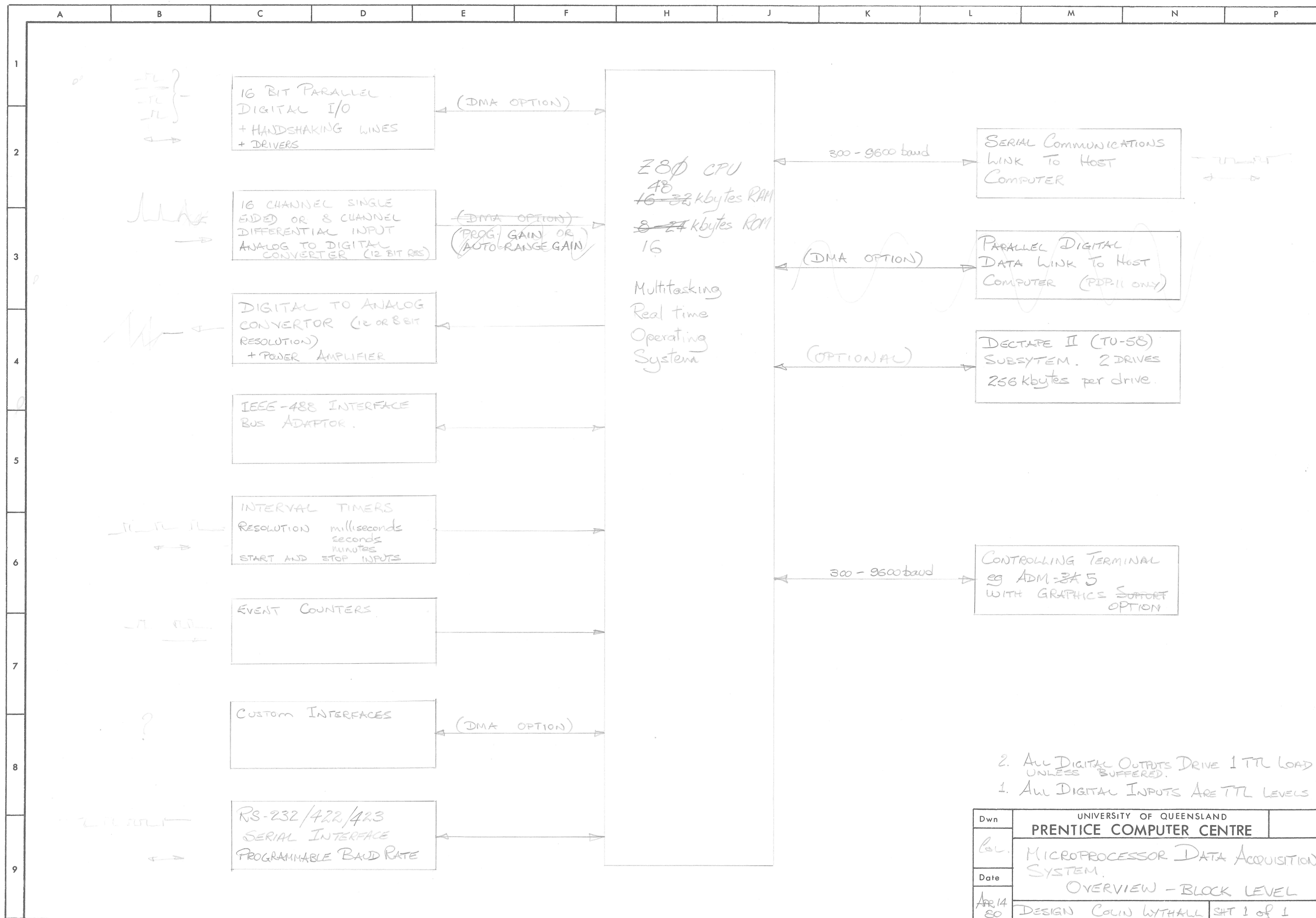
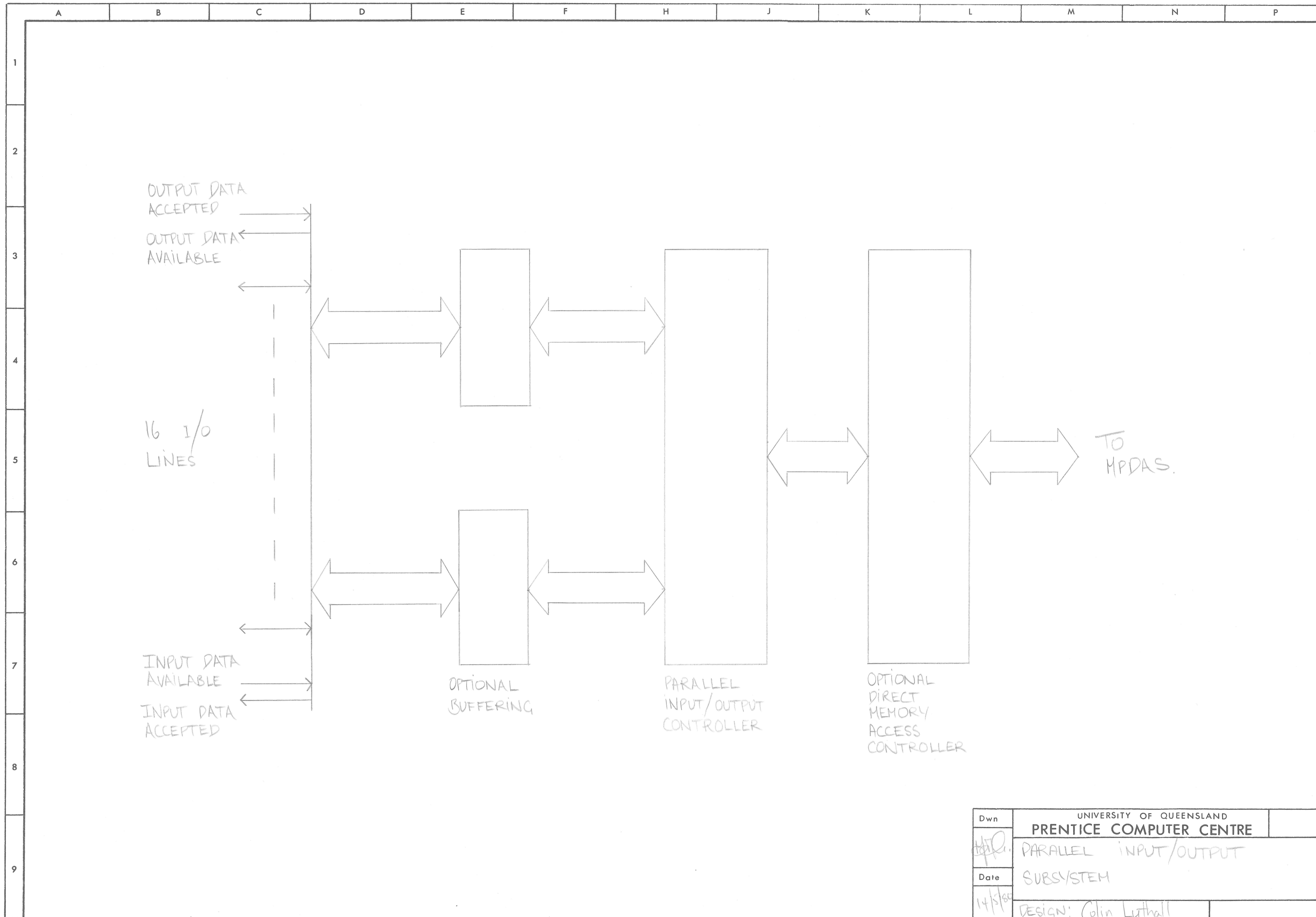




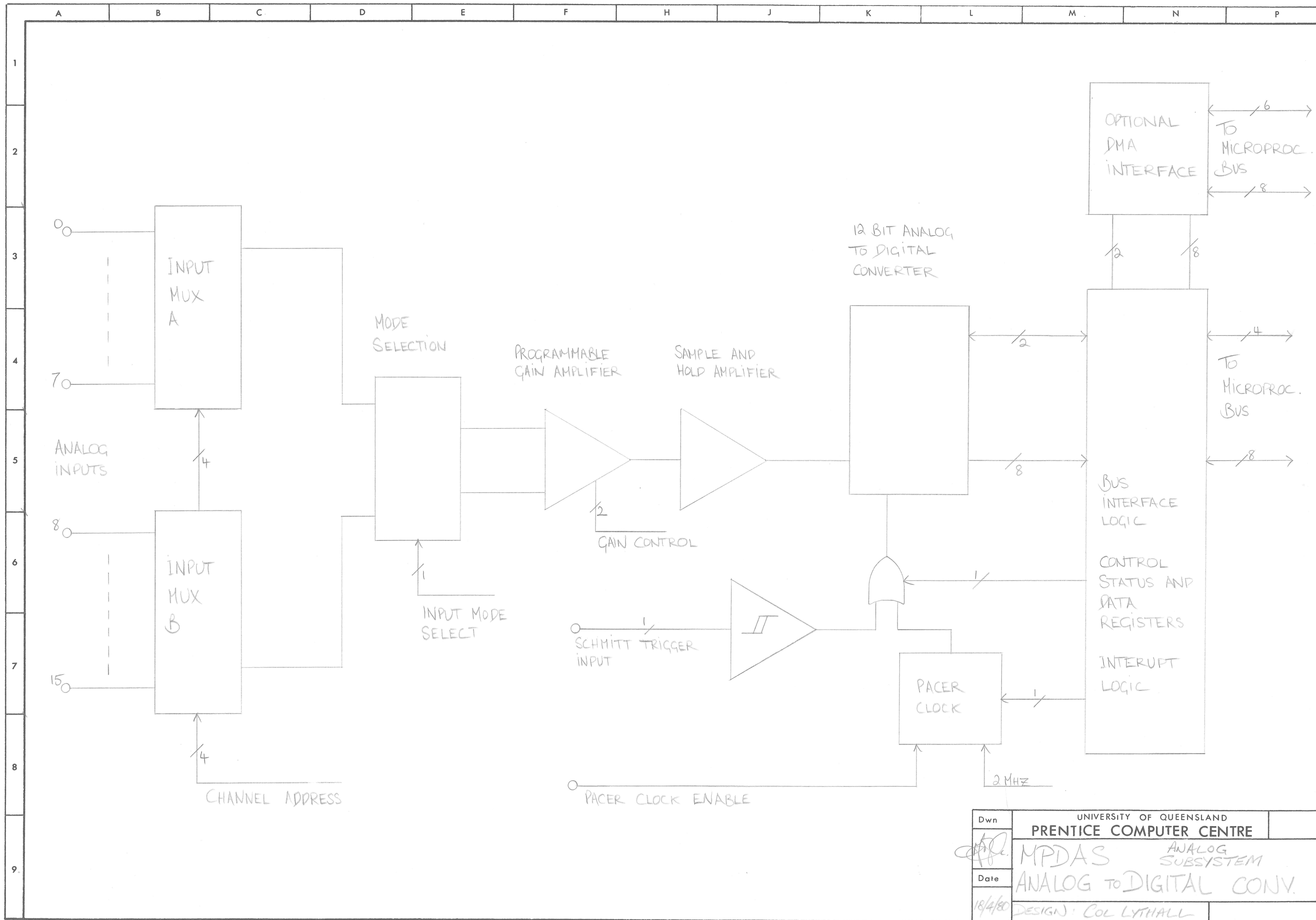




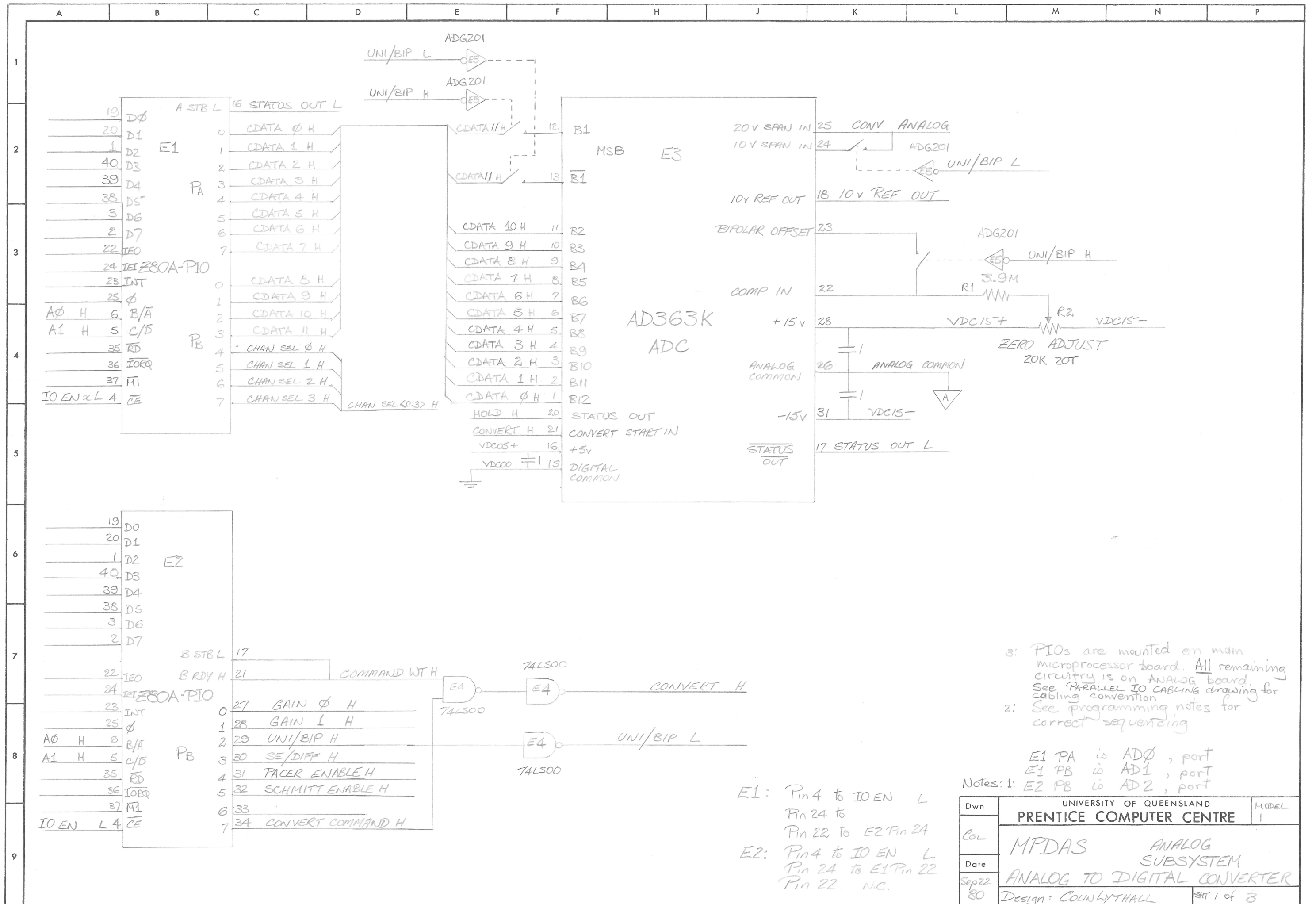


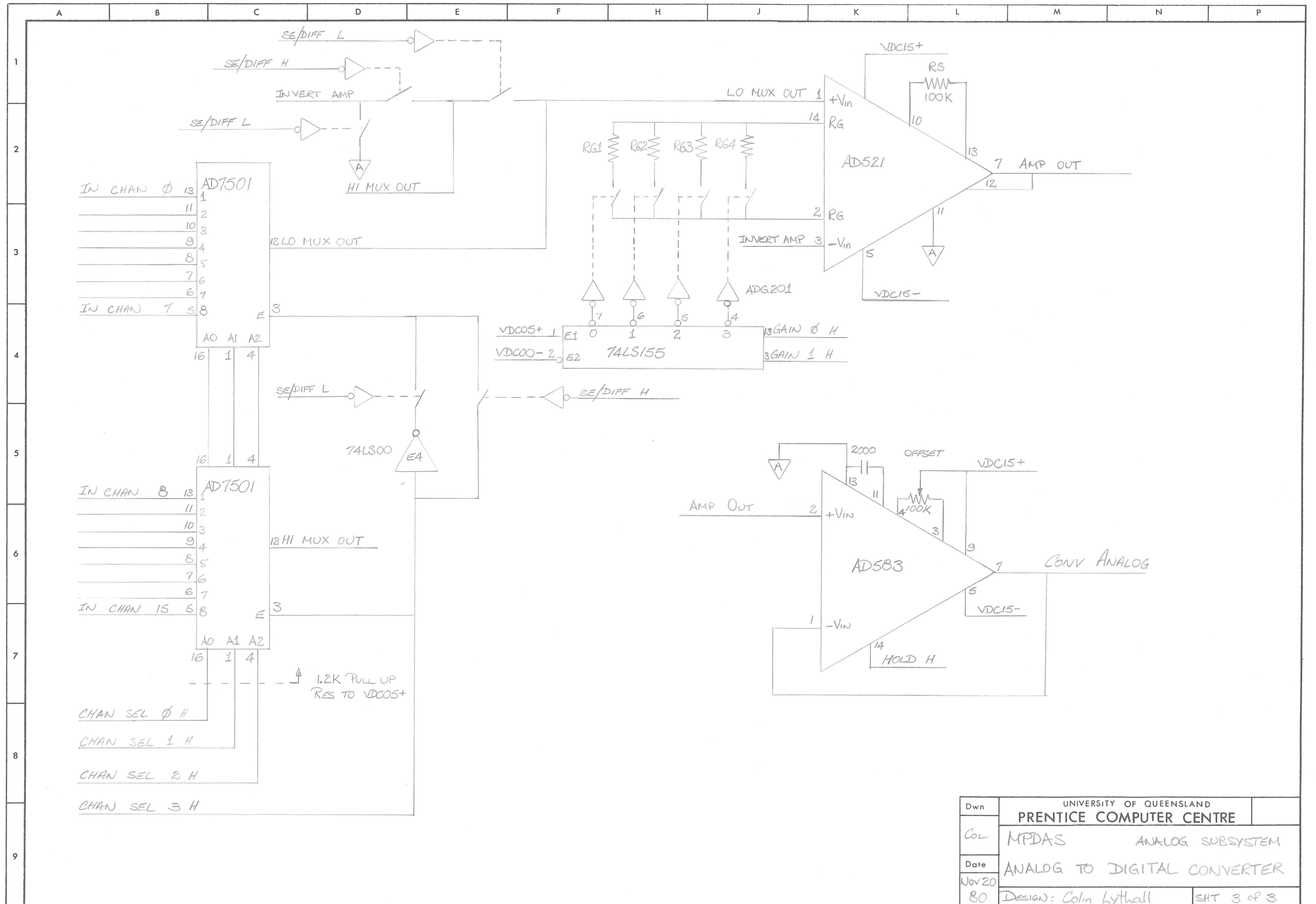


Dwn	UNIVERSITY OF QUEENSLAND	
	PRENTICE COMPUTER CENTRE	
Date	PARALLEL INPUT/OUTPUT	
	SUBSYSTEM	
14/5/80	DESIGN: Colin Lythall	



Dwn	UNIVERSITY OF QUEENSLAND	
	PRENTICE COMPUTER CENTRE	
Date	MPDAS ANALOG SUBSYSTEM	
18/4/80	ANALOG TO DIGITAL CONV.	
	DESIGN: COL LYTHALL	





Dwn	UNIVERSITY OF QUEENSLAND	
Col	PRENTICE COMPUTER CENTRE	
Date	MPDAS ANALOG SUBSYSTEM	
Nov20	ANALOG TO DIGITAL CONVERTER	
80	DESIGN: Colin Lythall	SHT 3 of 3

