

DIGITAL EQUIPMENT CORPORATION

MAYNARD, MASSACHUSETTS

PARTS LIST

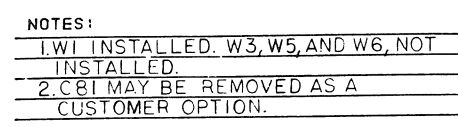
MADE BY <i>E. Wilson</i>	CHECKED <i>R. W. Cramer</i>	SECTION
DATE <i>22 SEPT 77</i>	DATE <i>23 Sept 77</i>	1
ENG <i>Robert Fitter</i>	PROD <i>[Signature]</i>	ISSUED SECT.
DATE <i>1 Dec 77</i>	DATE <i>[Signature] 12-2-77</i>	1

[illegible]

TITLE	ASSY NO.	SIZE	CODE	NUMBER	REV	ECO NO.
LSI 11 CPU WITH NO MEMORY	B-DD-KD11-HA	A	PL	KD11-HA-0		
SHEET 1 OF 1	DIST					

DEC FORM
DRA 110

COMPONENT SIDE VIEW

[illegible][illegible]

SIGNATURES		DATE	digital			
DRN.E.WILSON		4-19-77				
CHK'D.R.W. CAUNTER		19SEP77				
ENG. R. FITCH		20SEP77	TITLE			
PROJ.ENG. R. FITCH		20SEP77	LSI-11 CPU			
PROD. K. MERCADO		9-20-77				
SCALE 2:1		SIZE CODE	NUMBER		REV J	
SHT. 2 OF 3		D	M 1210-20			
NEXT HIGHER ASSY. B-DD-M 7210-0						

d	i	g	i	t	a	l
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TITLE LSI-11 CPU

SIZE	CODE	NUMBER
D	1	M1210-0-0

8

6

5

4

3

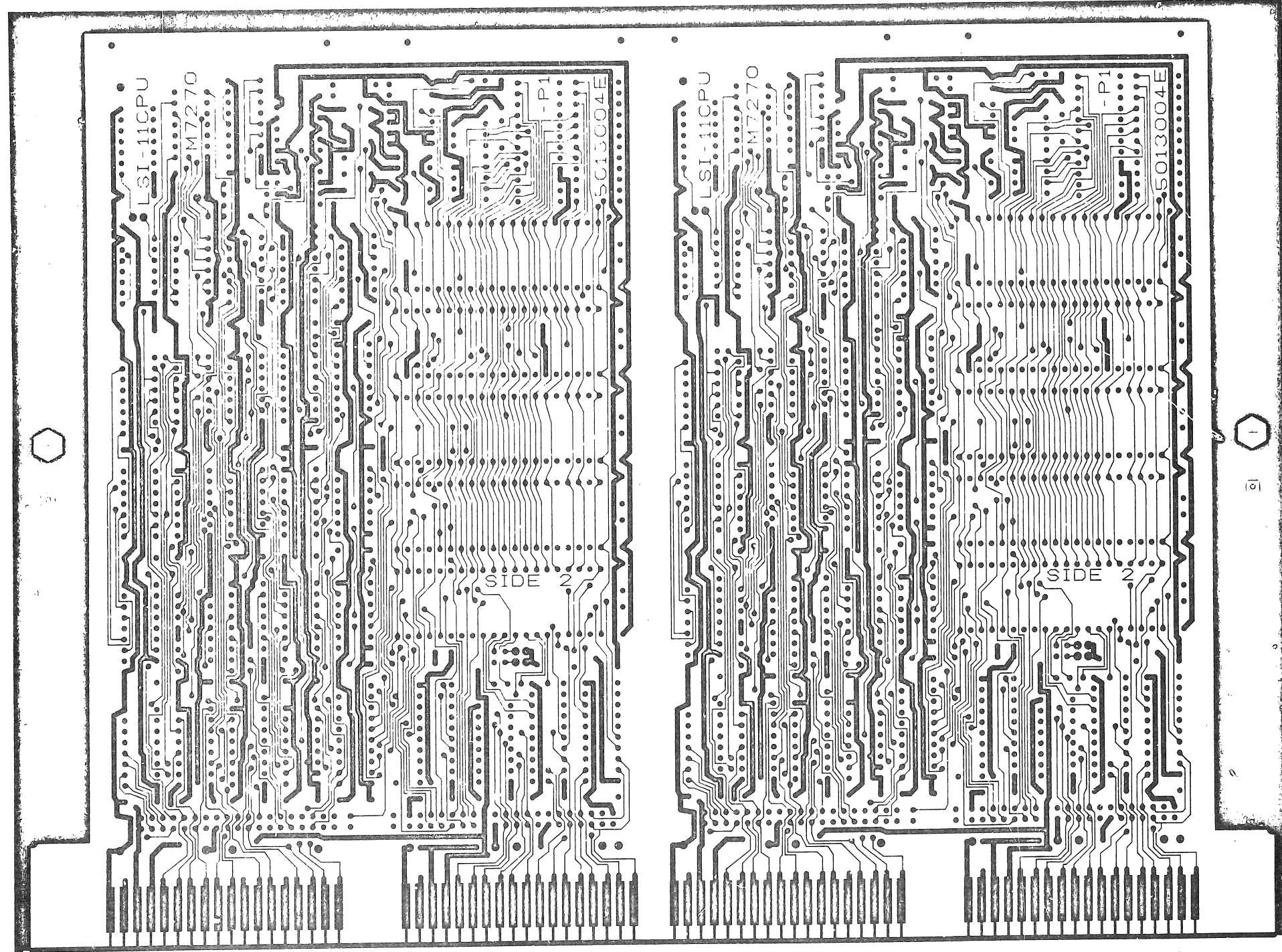
P 0 5-672214 M D 2 ml

1

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L2

L2



D

C

B

A

J

DUA M7270-0-0

TITLE		LSI-11 CPU	REV.		J
SCALE		2/1	SHEET		3 OF 3
DIST.			NUMBER		M7270-0-0
			DUA		

8

7

6

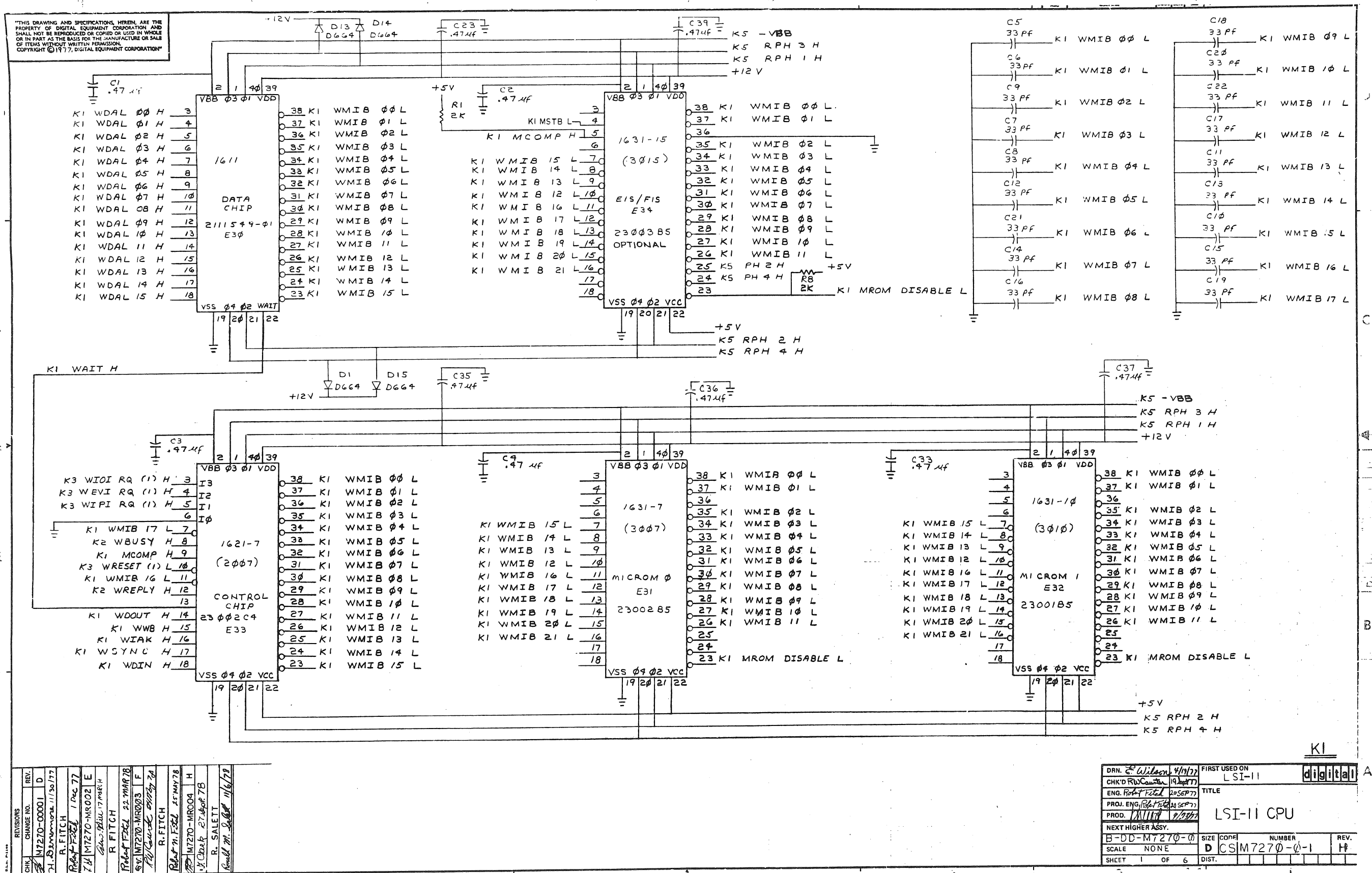
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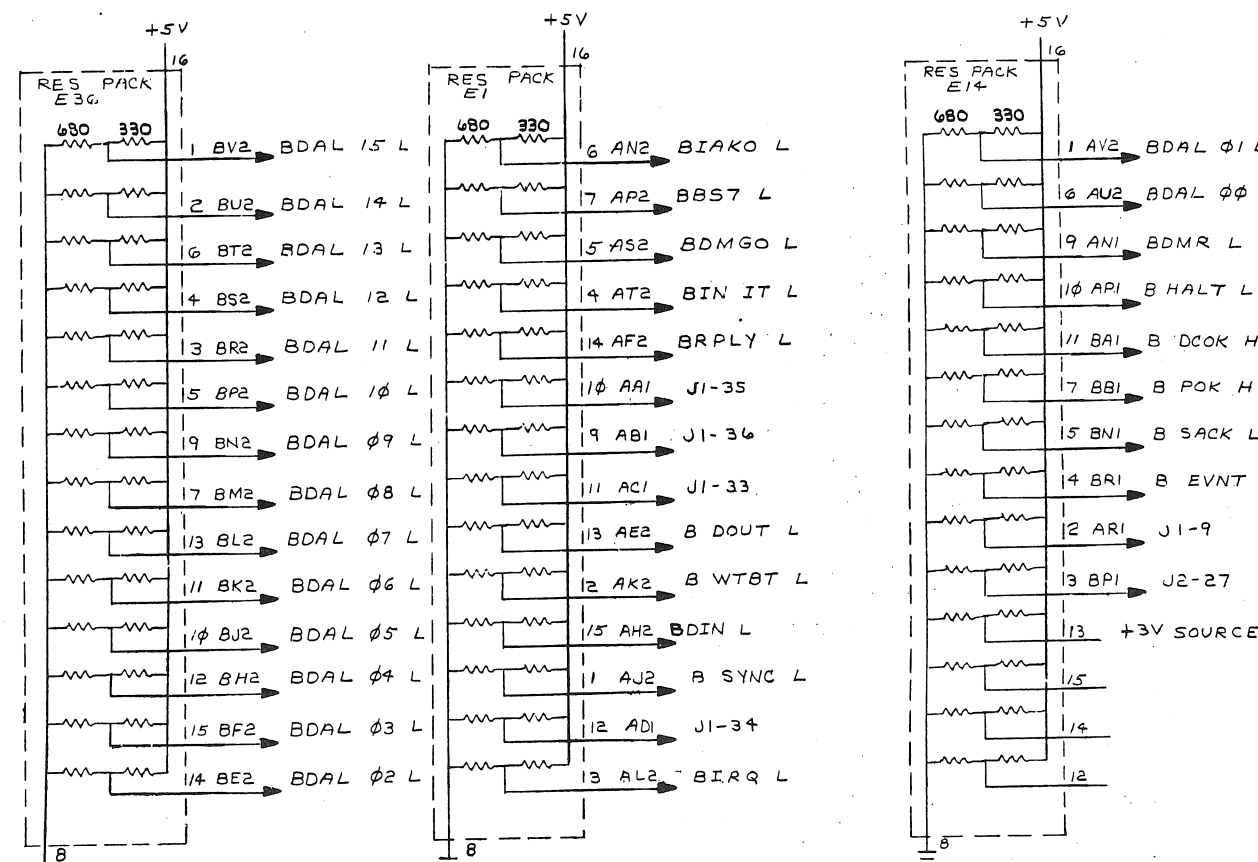
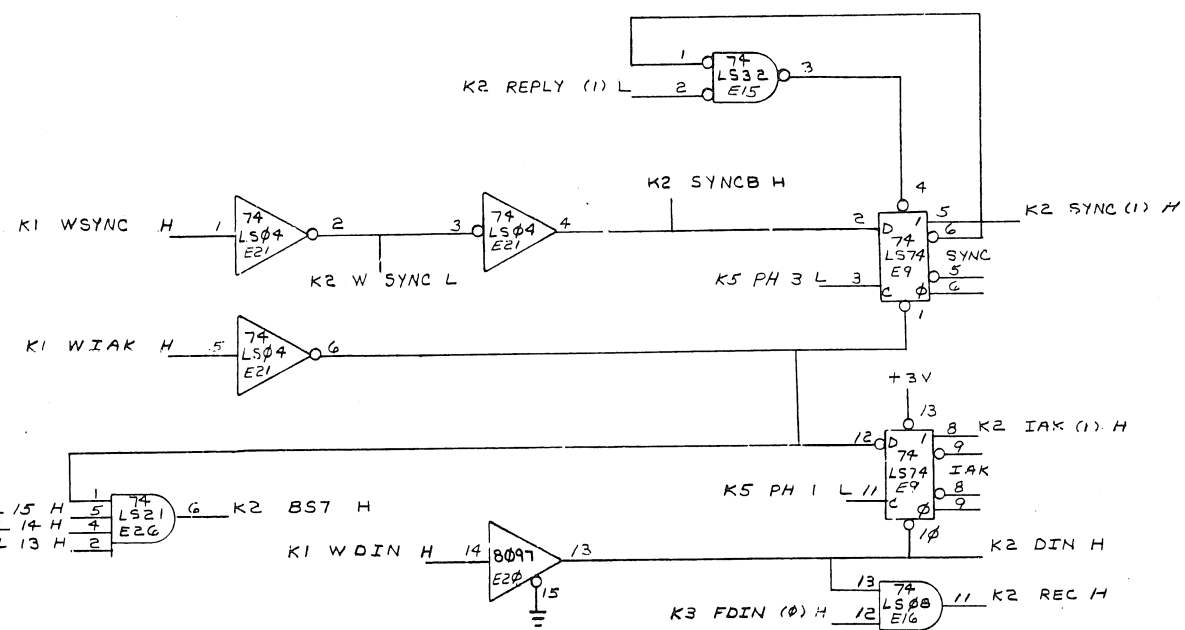
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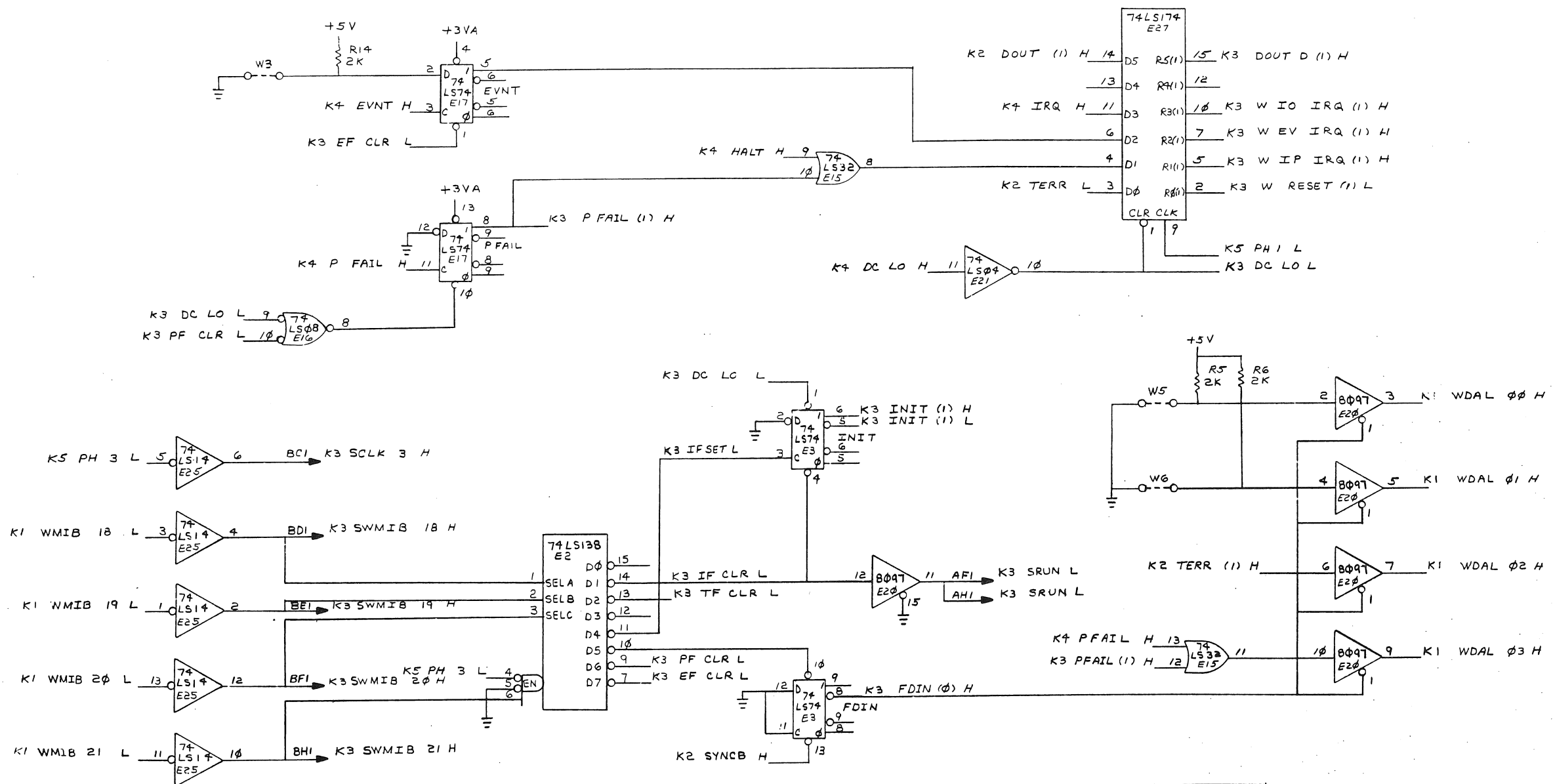


REVISIONS		
CHK	CHANGE NO.	REV.

K2

TITLE		SIZE CODE		NUMBER	
LSI-11 CPU		D CSM		M7270-0-1	
SCALE	1/2	SHEET	2	OF	6
		DIST.			

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JUMPER TABLE		
JUMPER	FUNCTION WHEN INSTALLED	FACTORY CONFIGURATION
W1	CRYSTAL CLOCK	I
W3	DISABLE EVENT LINE INTERRUPT	R
W5	POWER UP SELECT	R
W6	POWER UP SELECT	R

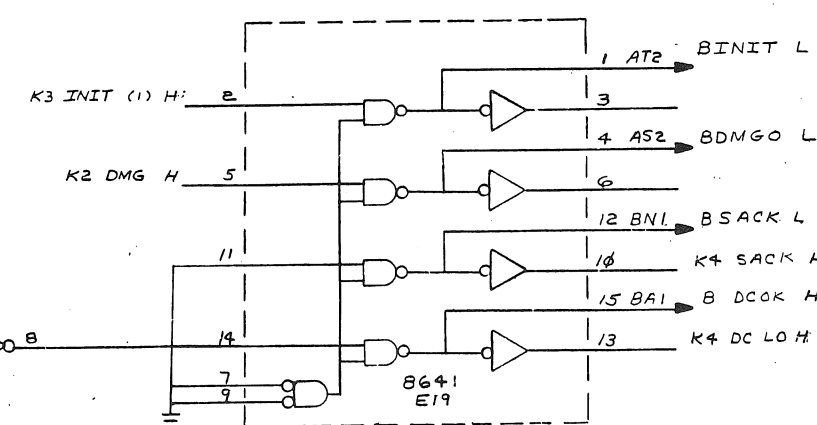
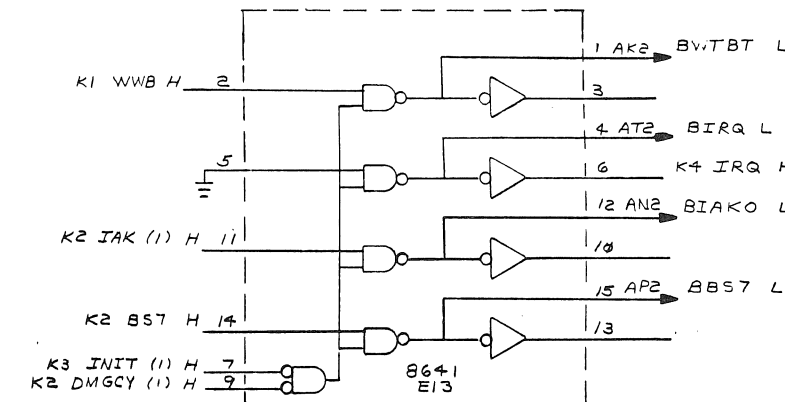
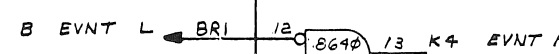
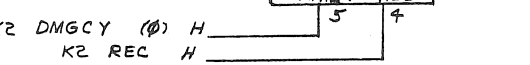
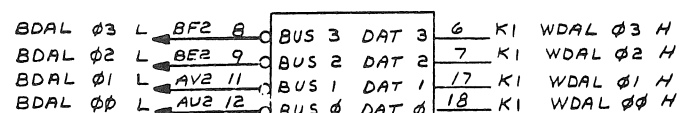
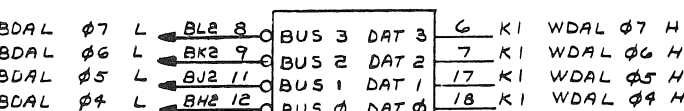
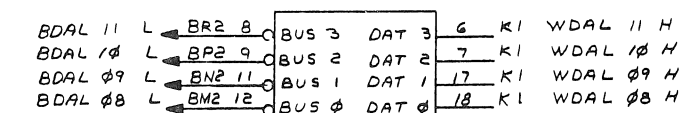
POWER-UP MODES			
MODE	JUMPERS		MODE SELECT
	W6	W5	
0	R	R	PC AT 24 AND PS AT 26, OR HALT MODE
1	R	I	ODT MICROCODE
2	I	R	PC AT 173000 FOR USER BOOTSTRAP
3	I	I	SPECIAL PROCESSOR MICROCODE (NOT IMPLEMENTED)

NOTE: R = JUMPER REMOVED, I = JUMPER INSTALLED

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE LSI-11 CPU		SIZE CODE DCSM7270-0-1	NUMBER H	REV. H
SCALE	SHEET 3 OF 6	DIST.		

BDAL 15	L	BUS 3	8	6	K1	WDAL 15 H
BDAL 14	L	BUS 2	9	7	K1	WDAL 14 H
BDAL 13	L	BUS 1	11	17	K1	WDAL 13 H
BDAL 12	L	BUS 0	12	18	K1	WDAL 12 H

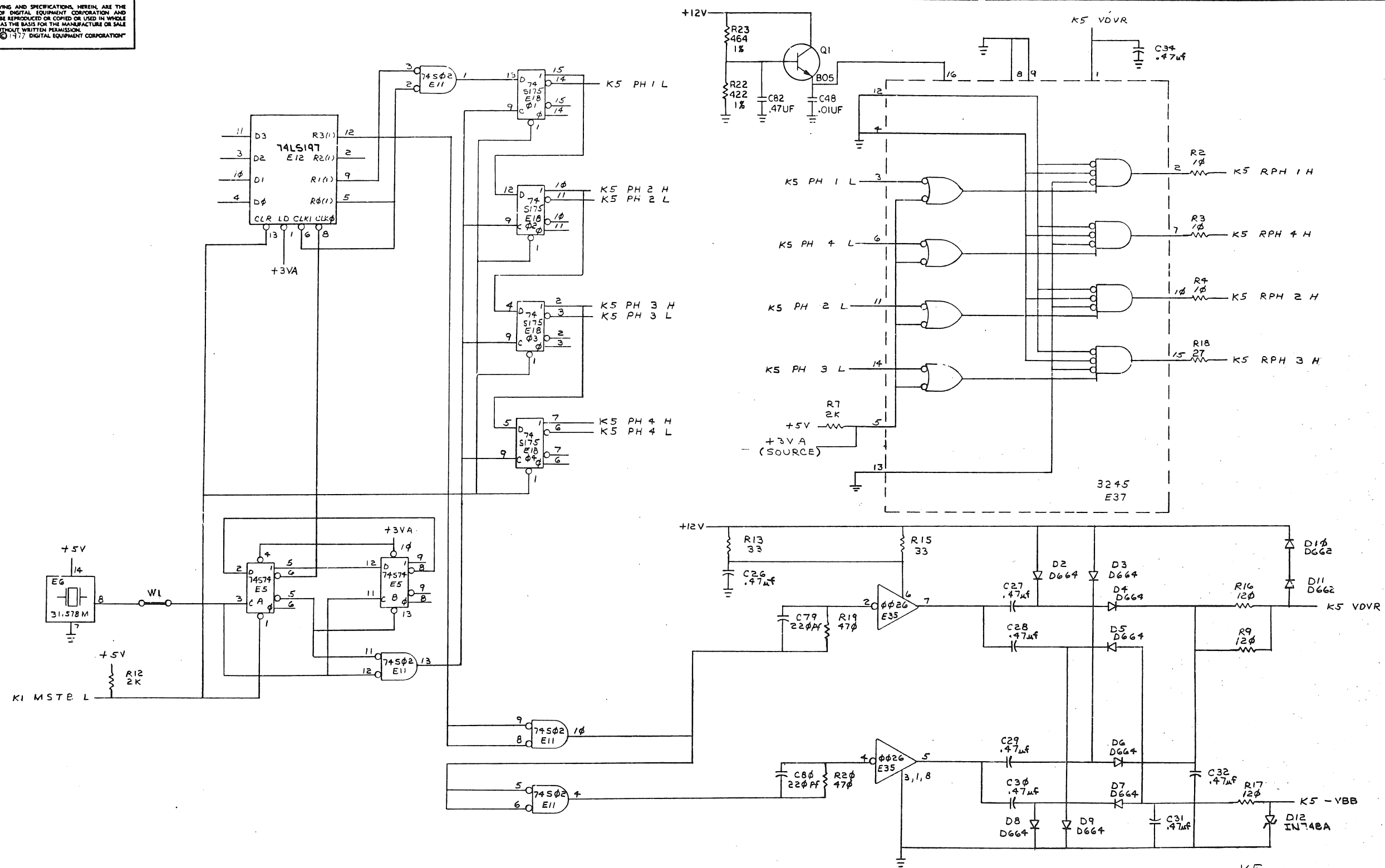


K4

REVISIONS		
CHK	CHANGE NO.	REV

TITLE				SIZE/CODE		NUMBER				REV.	
LSI-11 CPU				D CS		M7270-0-1				H	
SCALE		SHEET		OF		DIST.					
1-1		4		6							

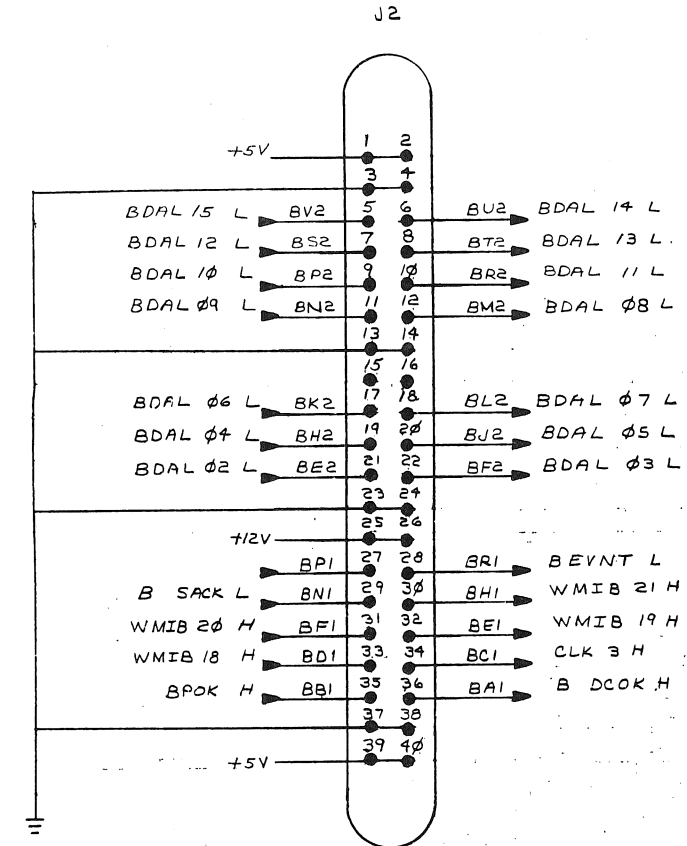
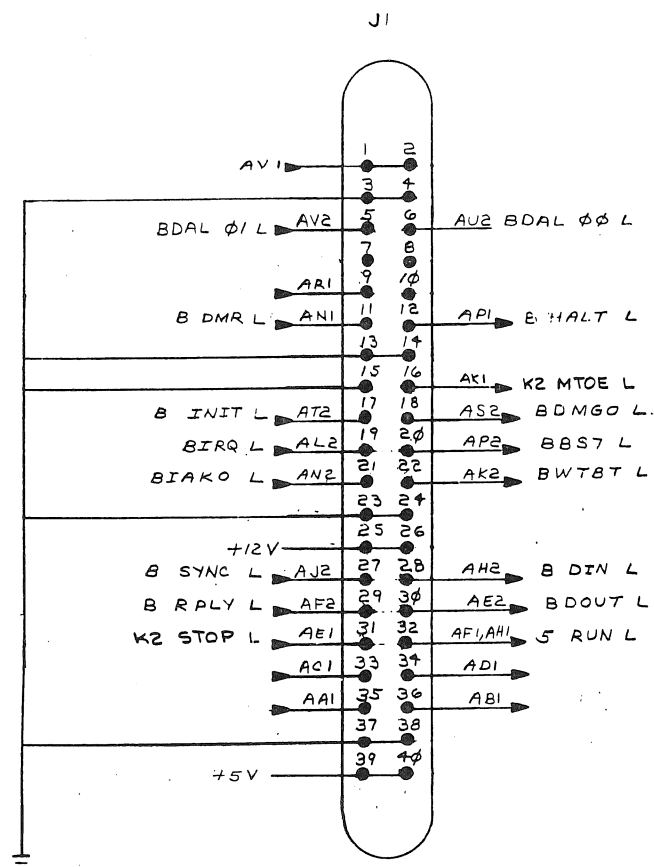
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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		SIZE CODE	NUMBER	REV.
LSI-II CPU		DCS	M7270-0-1	H
SCALE	SHEET 5 OF 6	DIST.		

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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		SIZE	CODE	NUMBER		REV.
LSI-11 CPU		D	C	M7270-0-1		H
SCALE	2/1	SHEET	6 OF 6	DIST.		

