

LAURIE

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FIELD MAINTENANCE PRINT SET

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UNIT VARIATIONS COVERED BY THIS PRINT SET

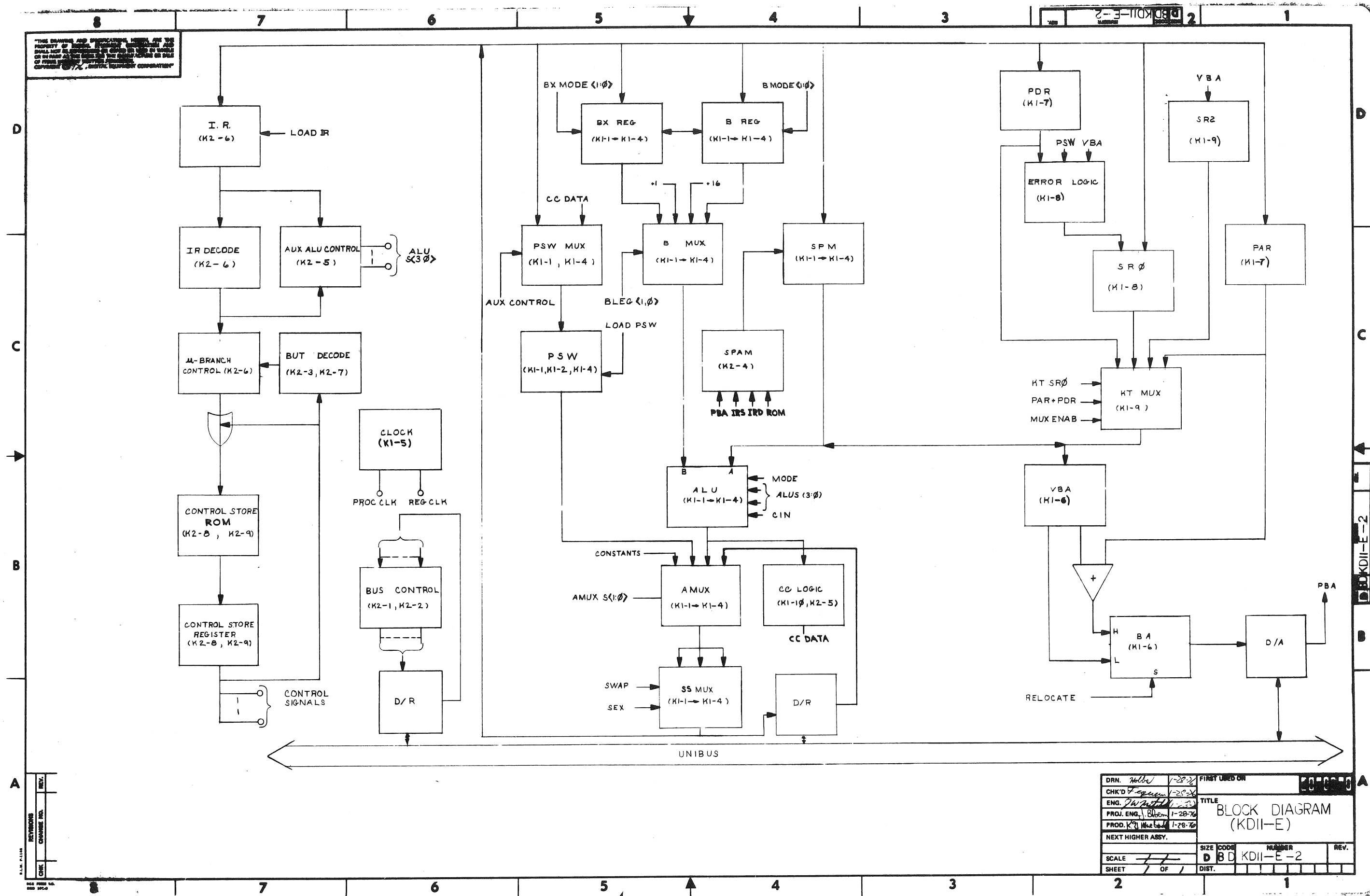
KD11-E

KD11-E
Field Maintenance
Print Set

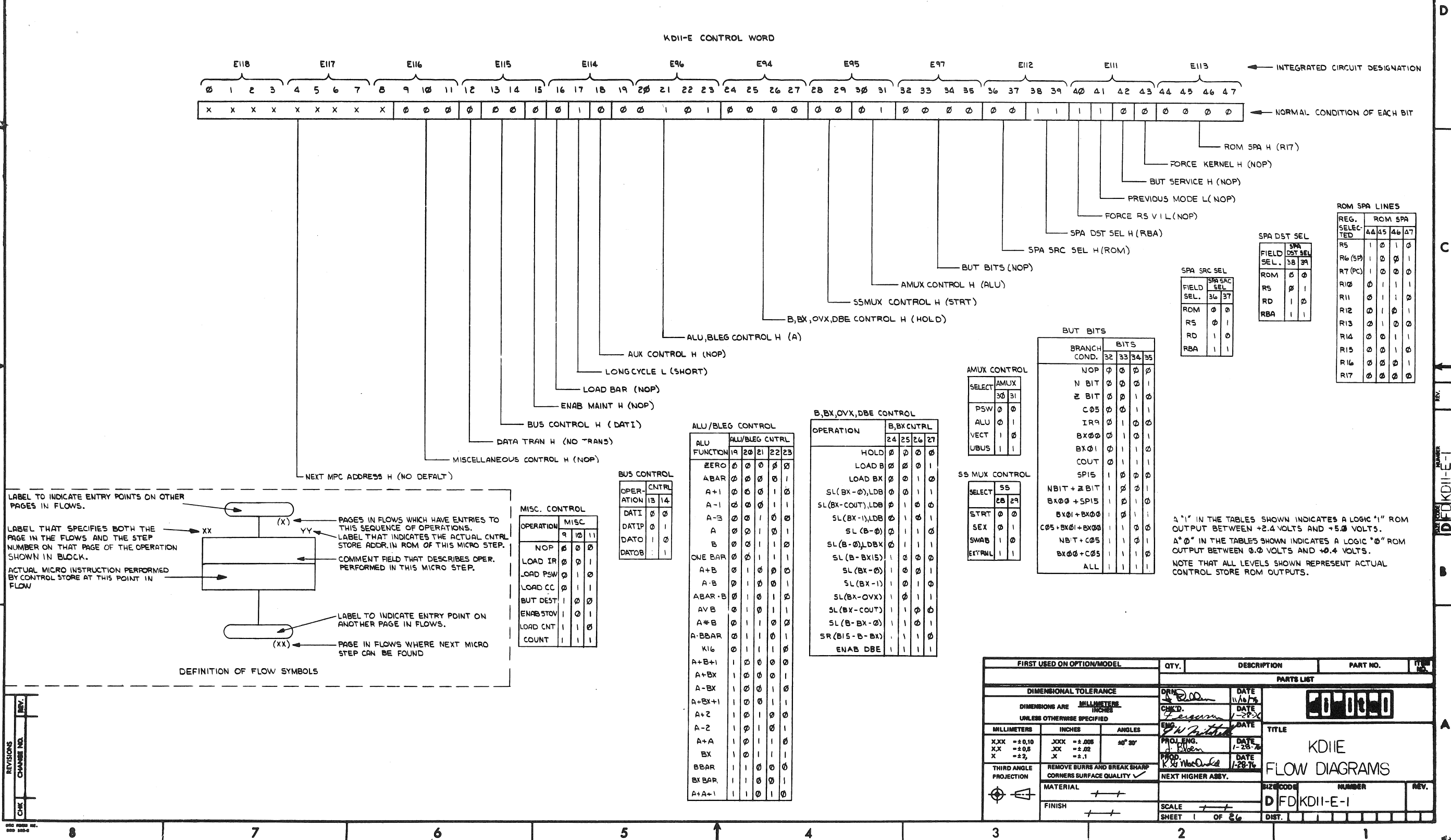
Digital Equipment Corporation

PRINT SET ORDER NO.
MP00043

[illegible]



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MOV(B) = MOV OR BYTE
DOP(B) = DOUBLE OPERAND (WORD OF BYTE)
SOP(B) = SINGLE OPERAND (WORD OF BYTE)
MOD = MODIFYING INSTRUCTION
NON MOD = NON-MODIFYING INSTRUCTION
SMØ = SOURCE MODE (IR[8-6]) = Ø
DMØ = DEST. MODE (IR[2-Ø]) = Ø

NOTE: SERVICE ALWAYS EXECUTED BEFORE FETCHING NEXT INSTRUCTION.
VECTOR ALWAYS LOADED, ONLY USED WHEN TRAP SEQUENCE ENTERED.

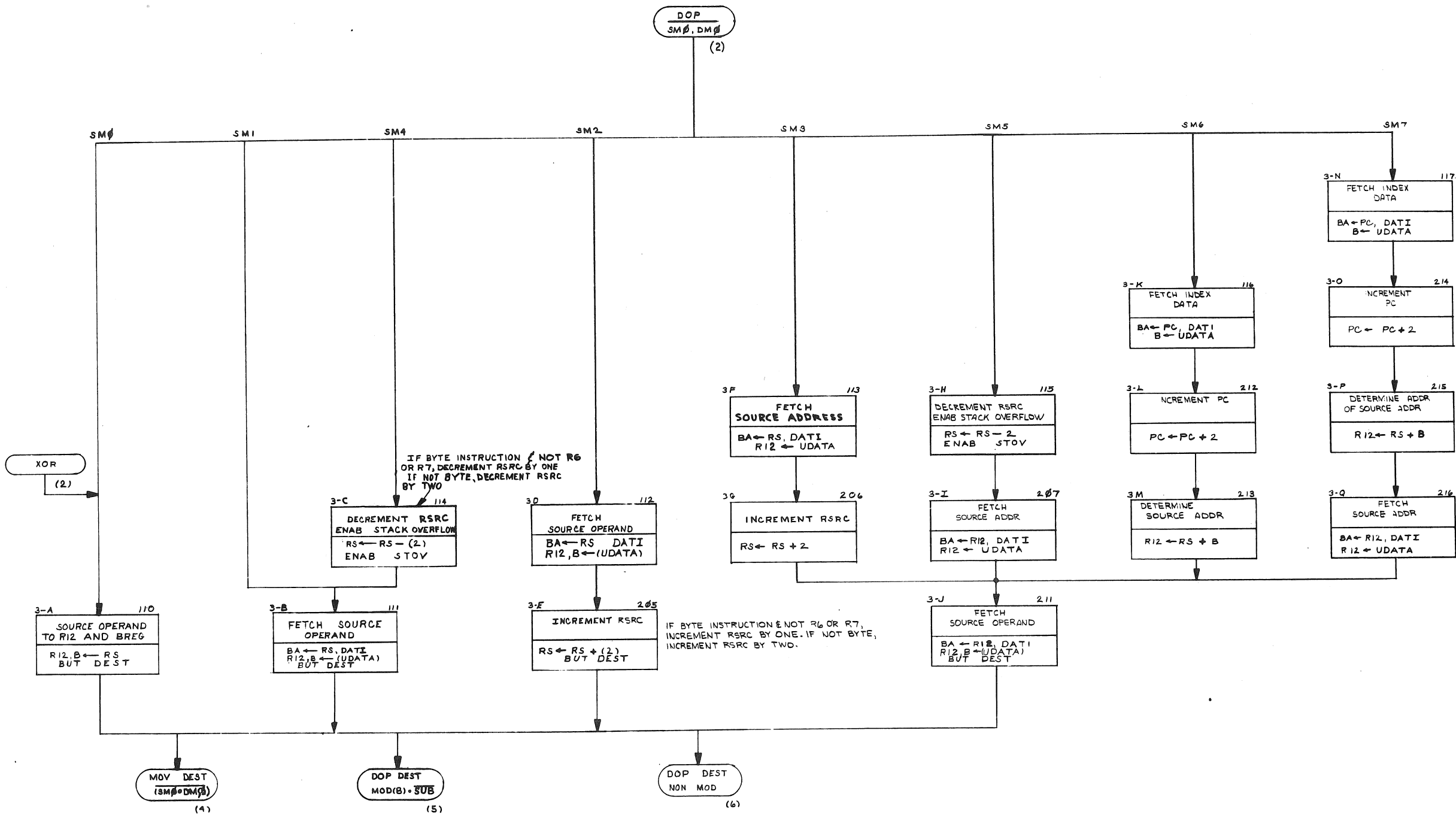
assert
MPC bit 7

REVISIONS		
CHK	CHANGE NO.	REV.

DEC FORM NO.
DMD 128

TITLE		SIZE CODE	NUMBER	REV.
KDIIE FLOW DIAGRAMS		D	FD KDIIE-1	
SCALE		SHEET	2 OF 26	DIST.

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REVISIONS		
CHK	CHANGE NO.	REV.

DEC FORM NO. DWD 128

TITLE
KDIE FLOW DIAGRAMS

SIZE CODE
D FDKDII-E-I

NUMBER

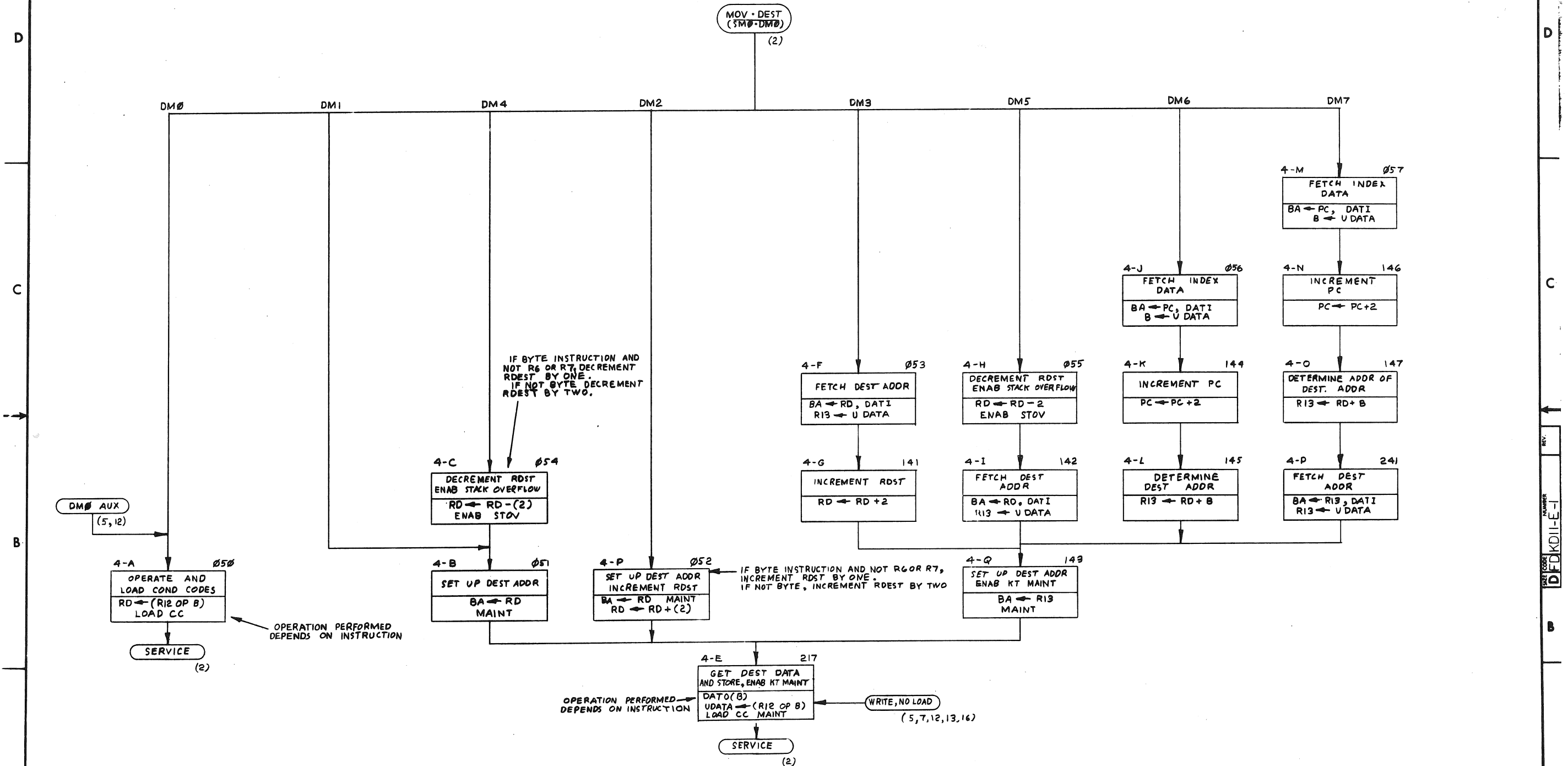
REV.

SCALE

SHEET 3 OF 26

DIST.

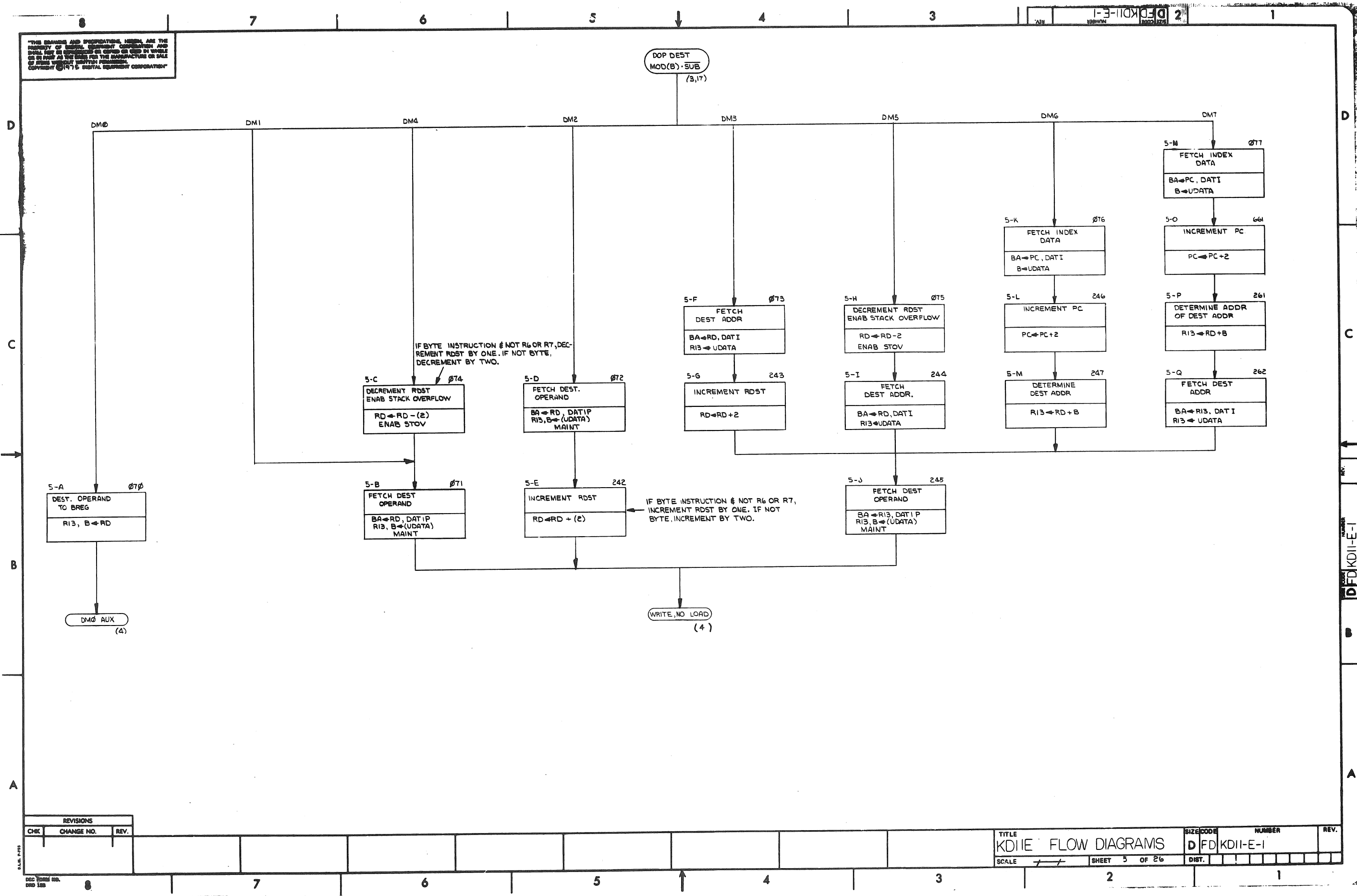
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REVISIONS		
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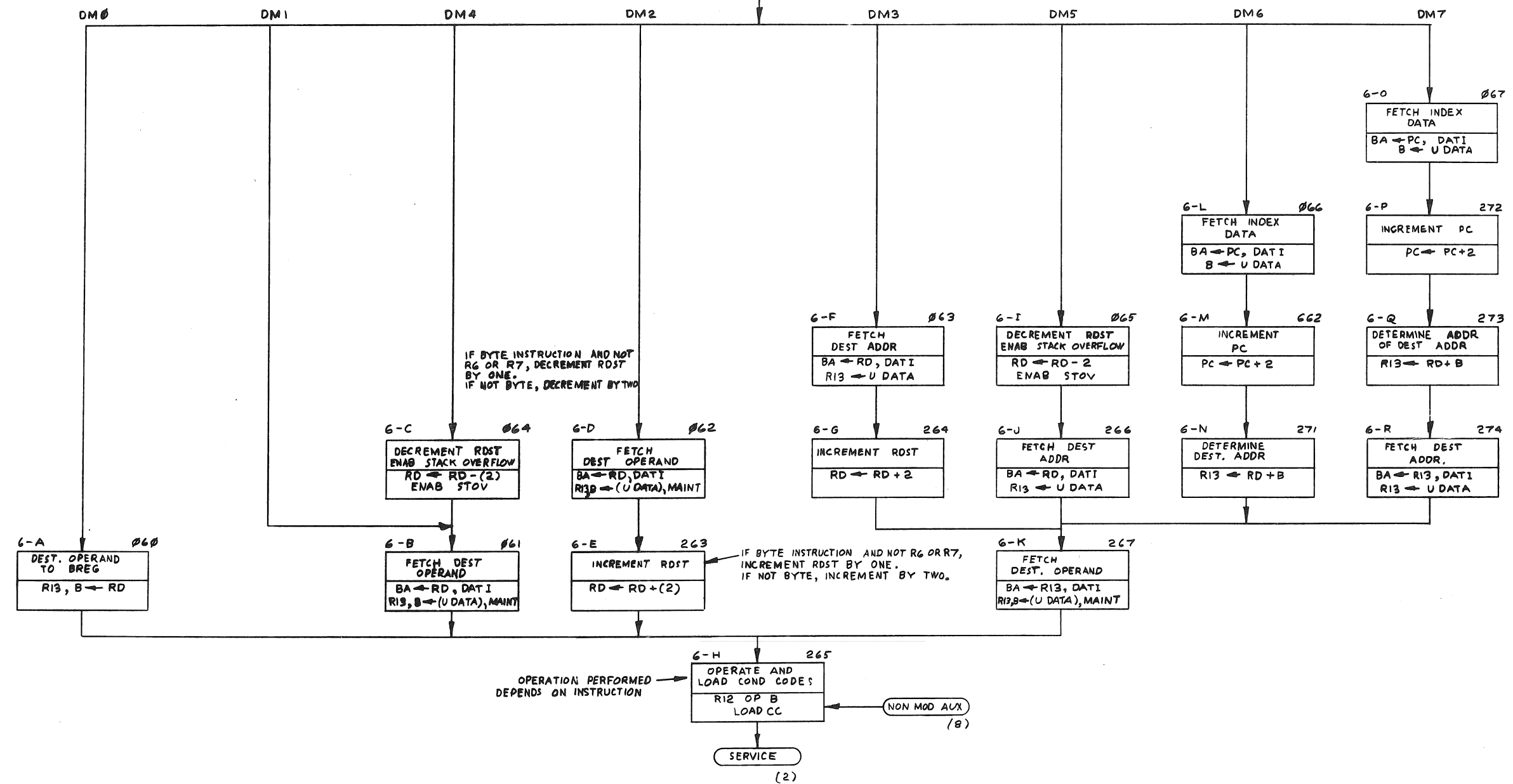
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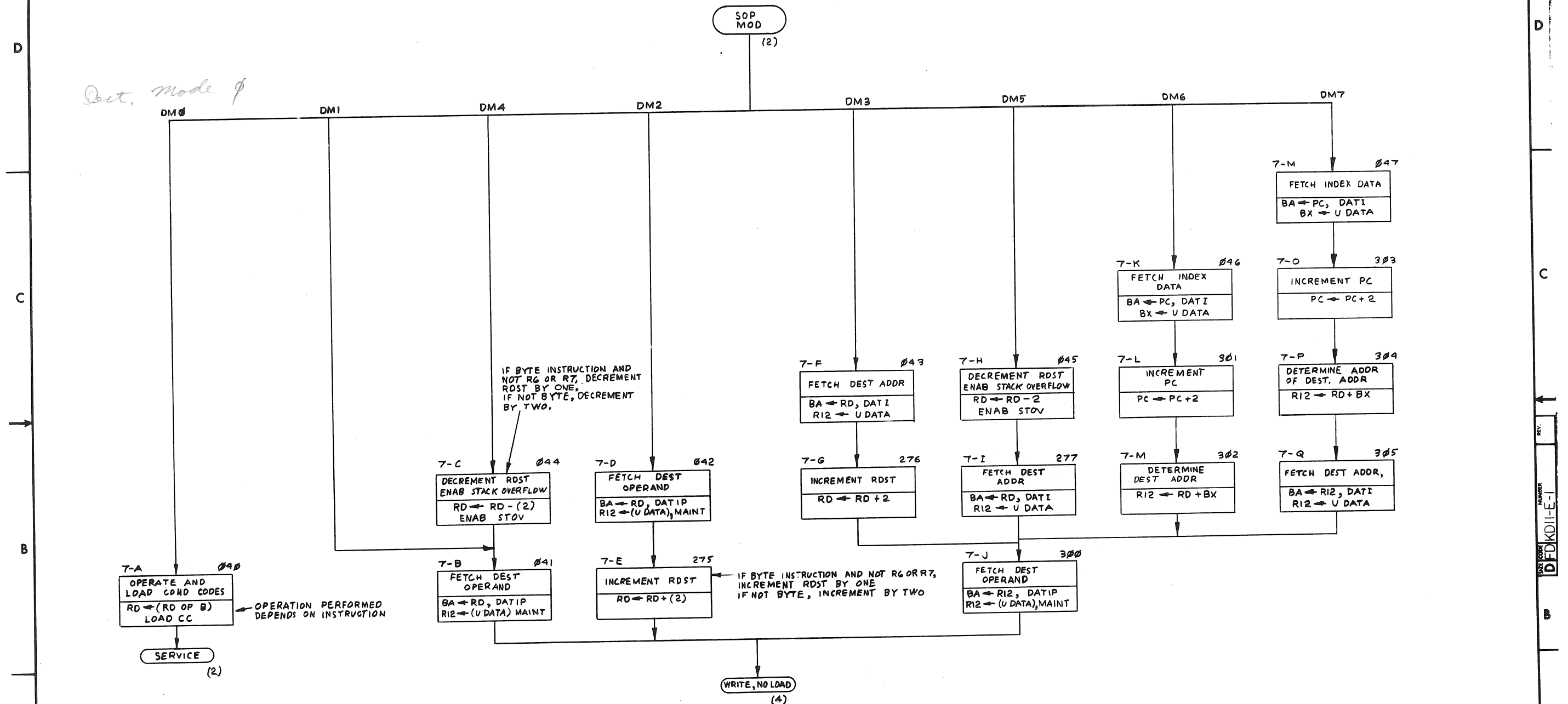


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DOP DEST
NON MOD
(3)

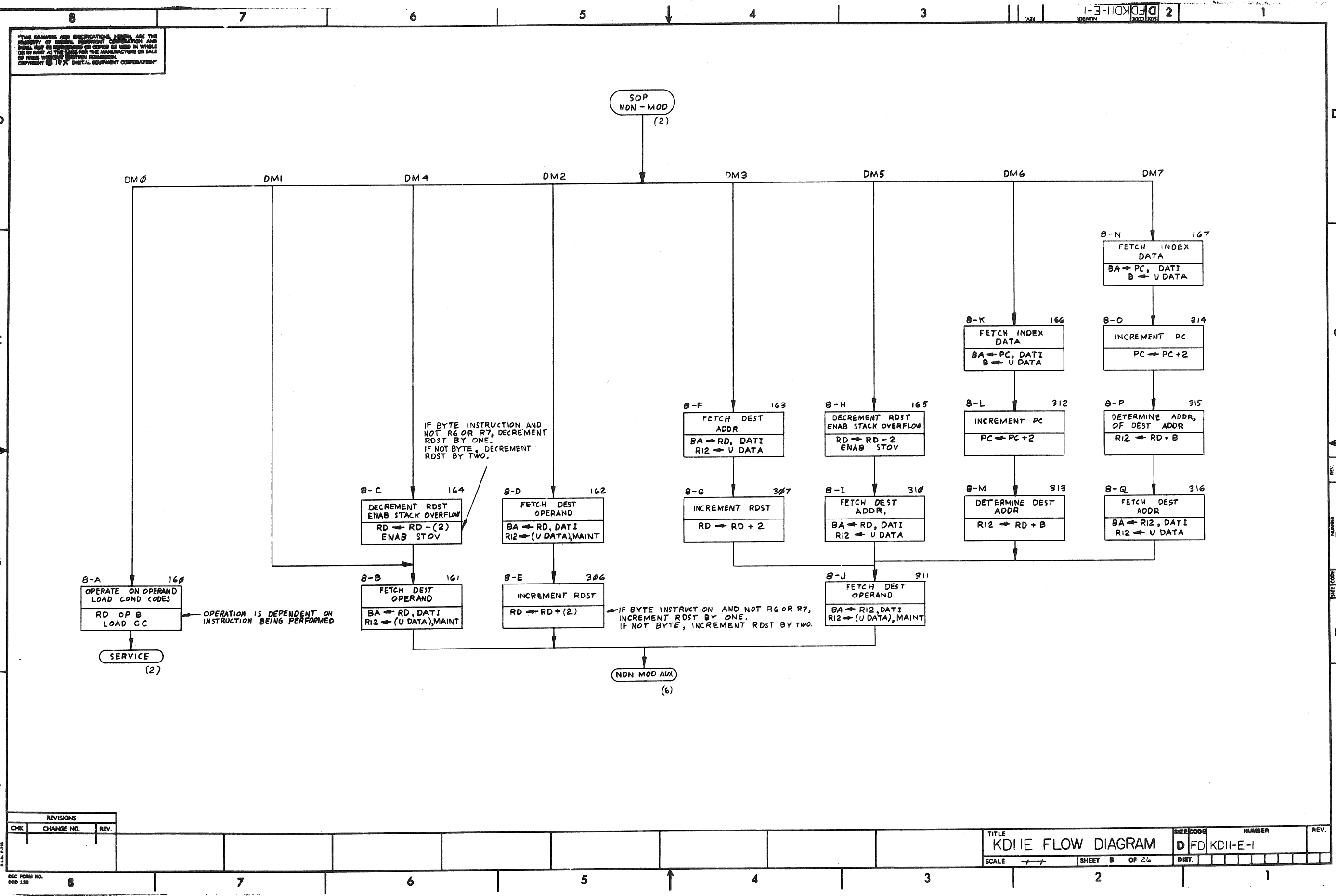


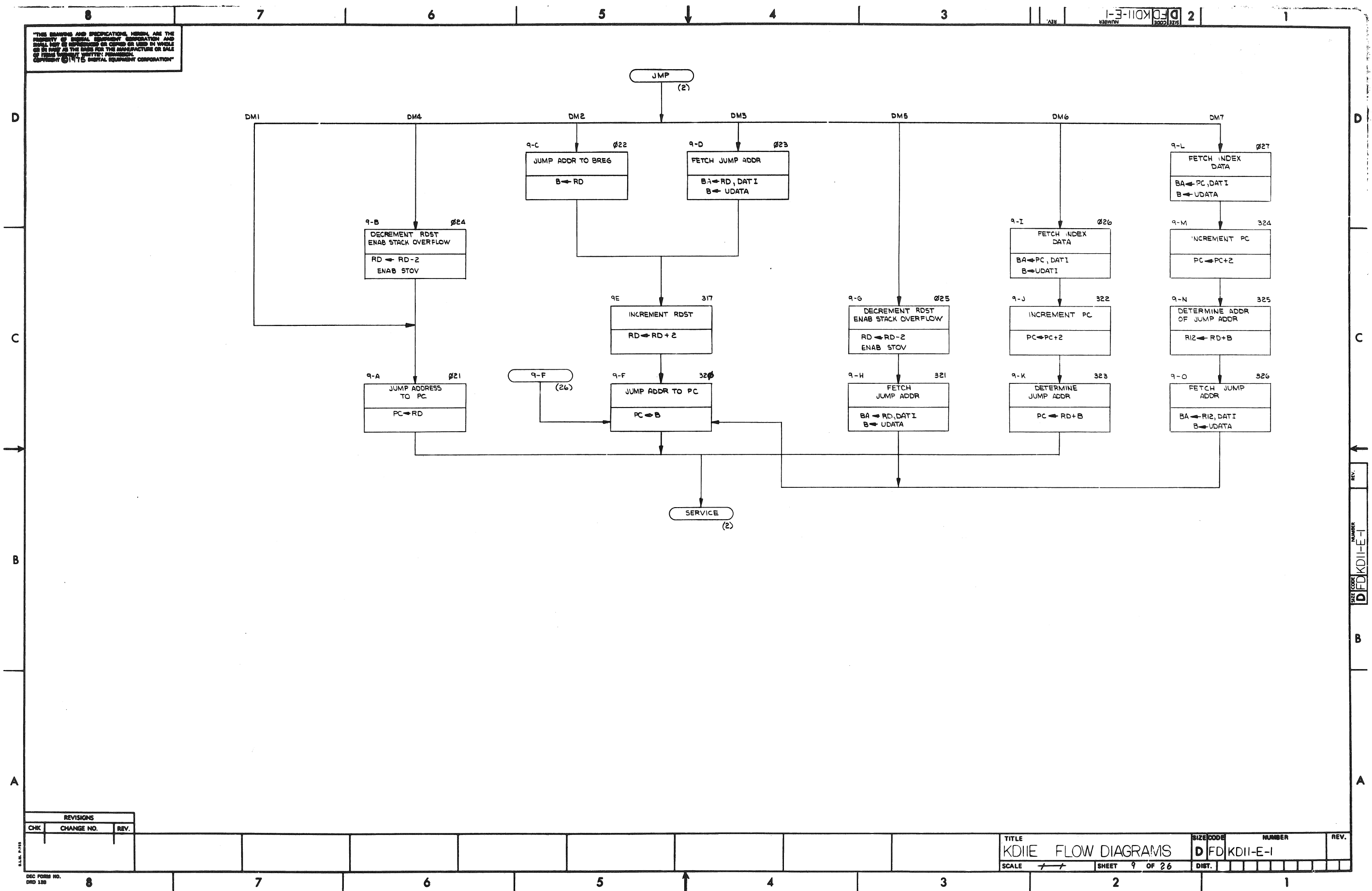
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CHK	CHANGE NO.	REV.



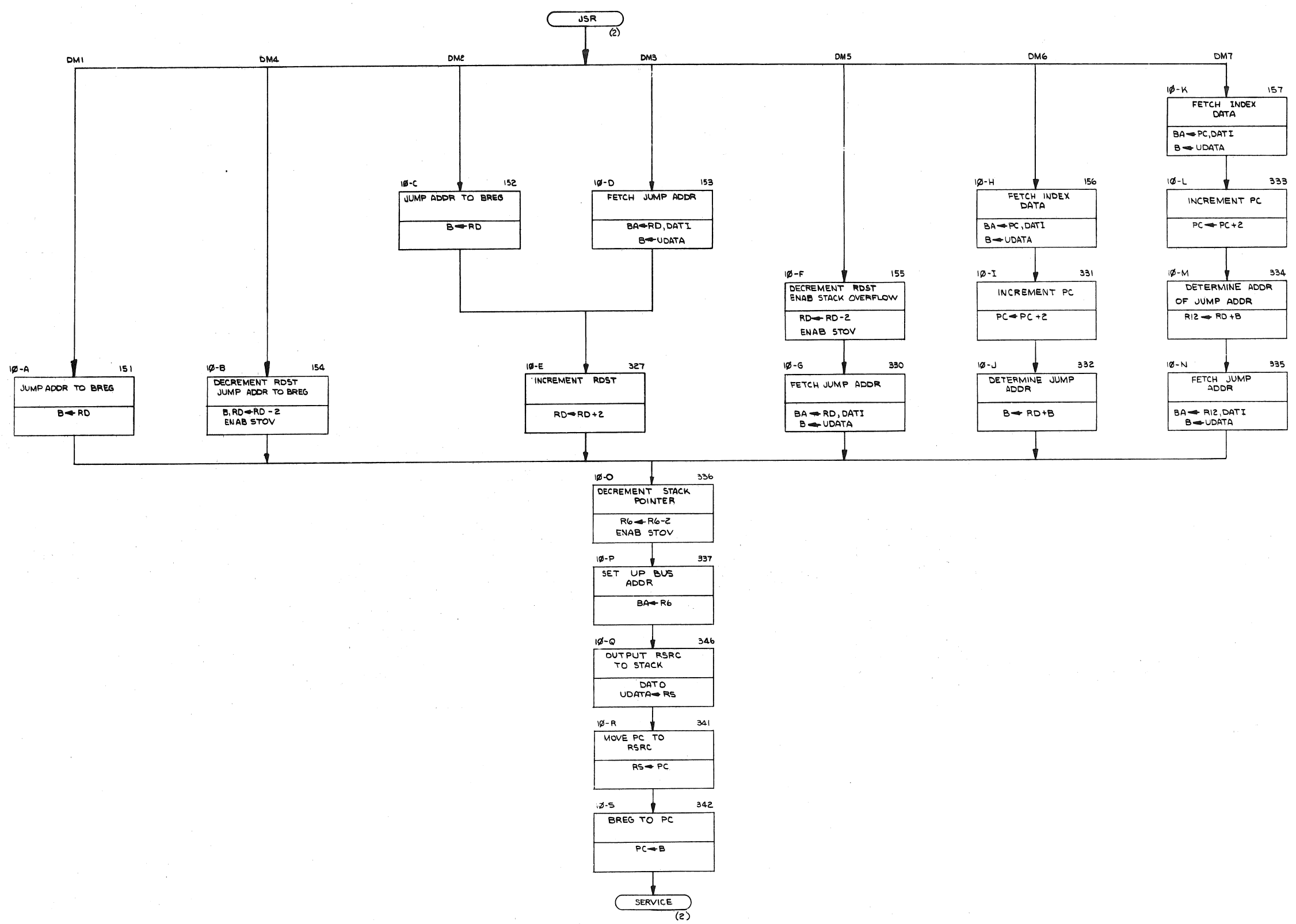
REVISIONS									TITLE		SIZE CODE	NUMBER		REV.			
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									SCALE		SHEET 7 OF 26		DIST.				
									1/2"								
											2		1				
8			7			6			5			4			3		

DEC FORM NO.
ORD 138



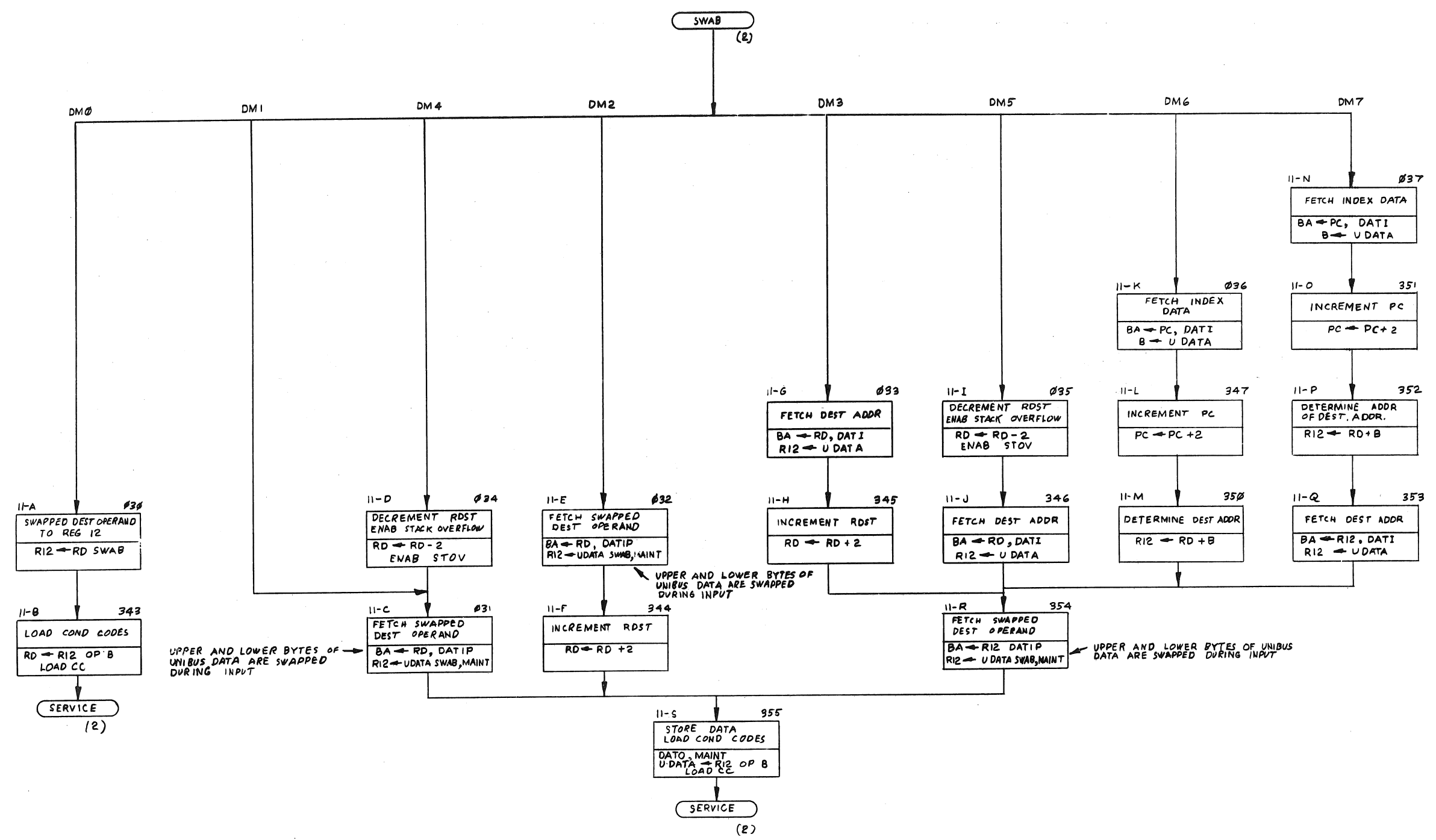


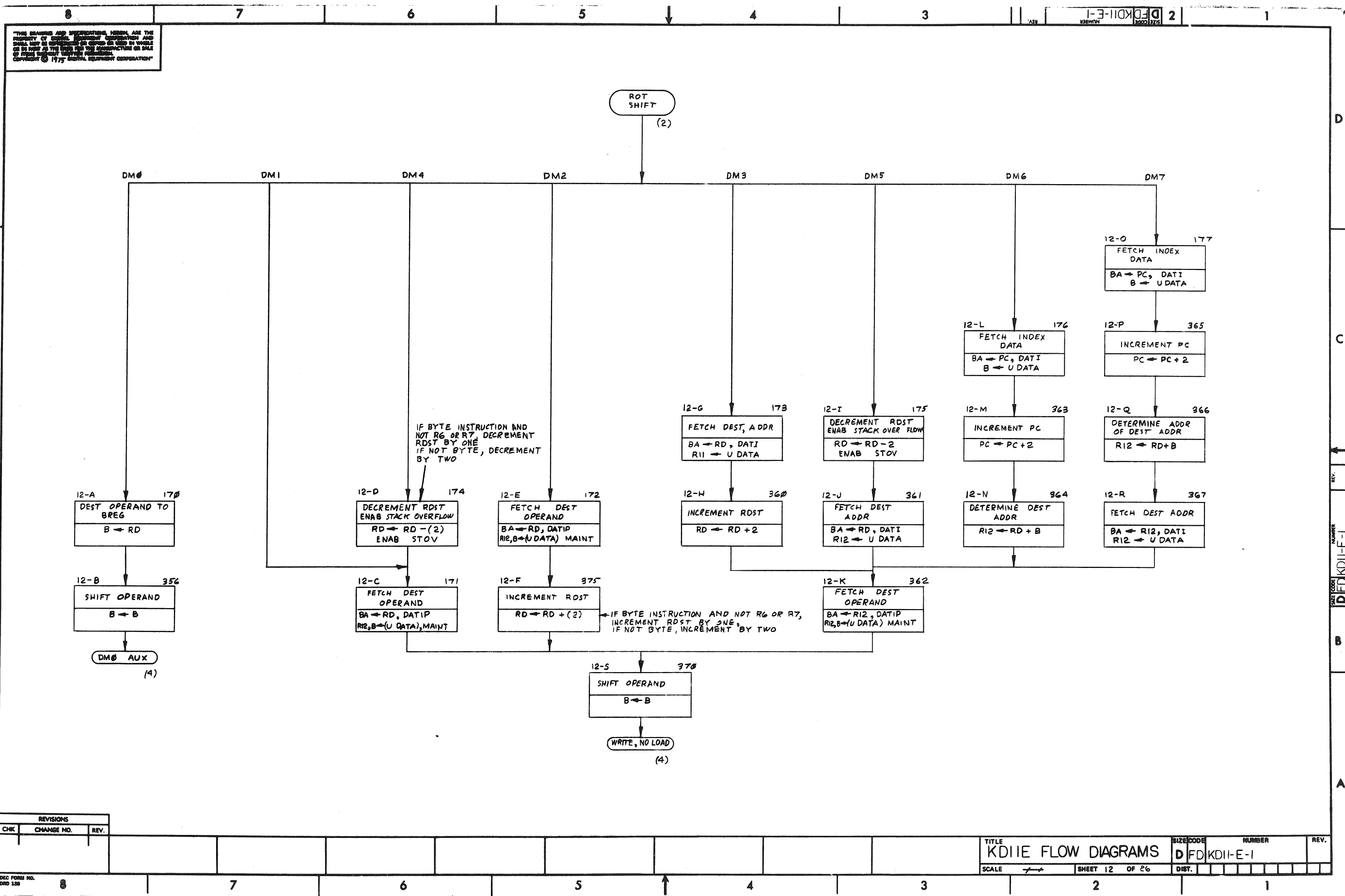
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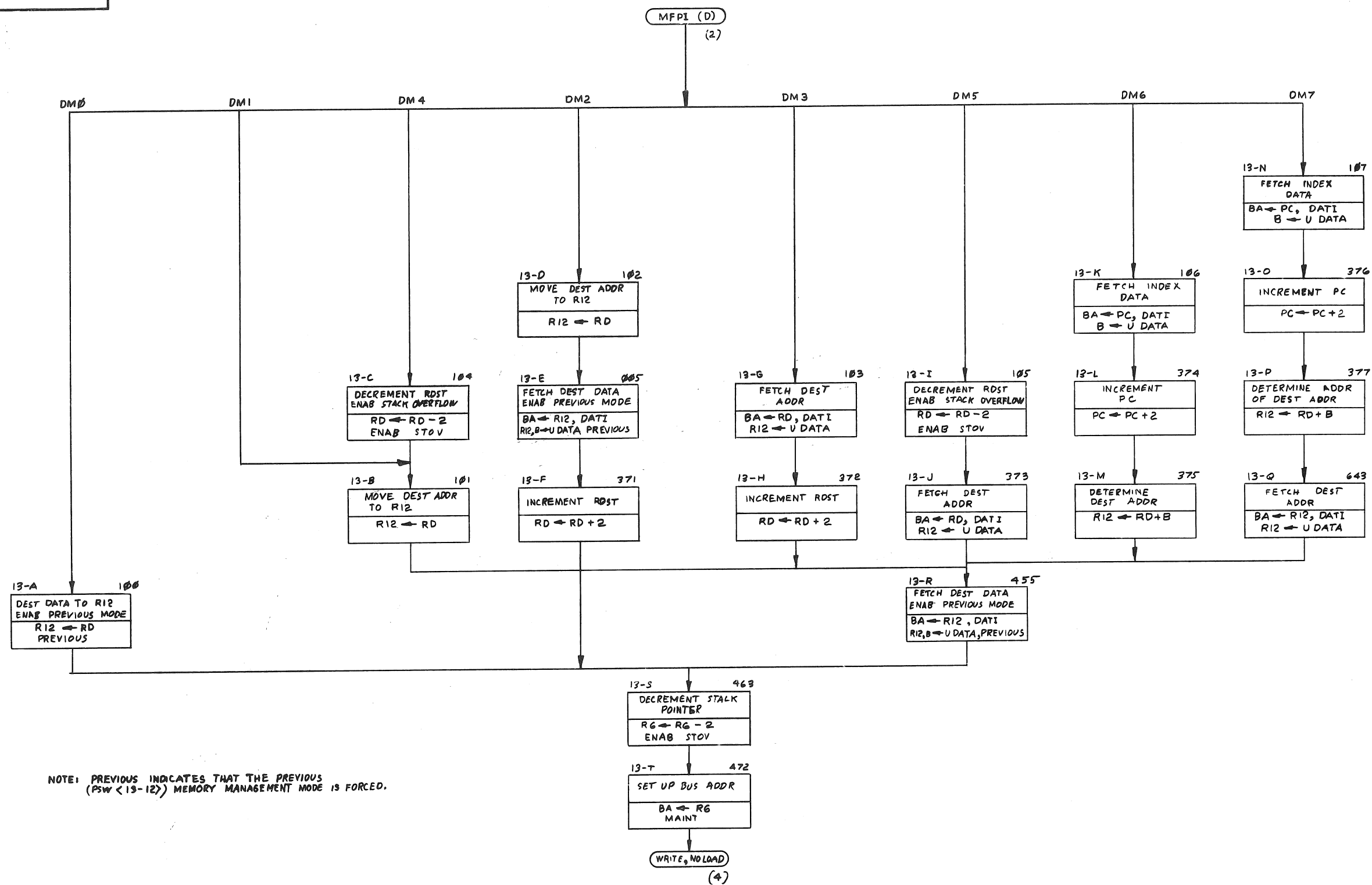
REVISIONS		
CHK	CHANGE NO.	REV.

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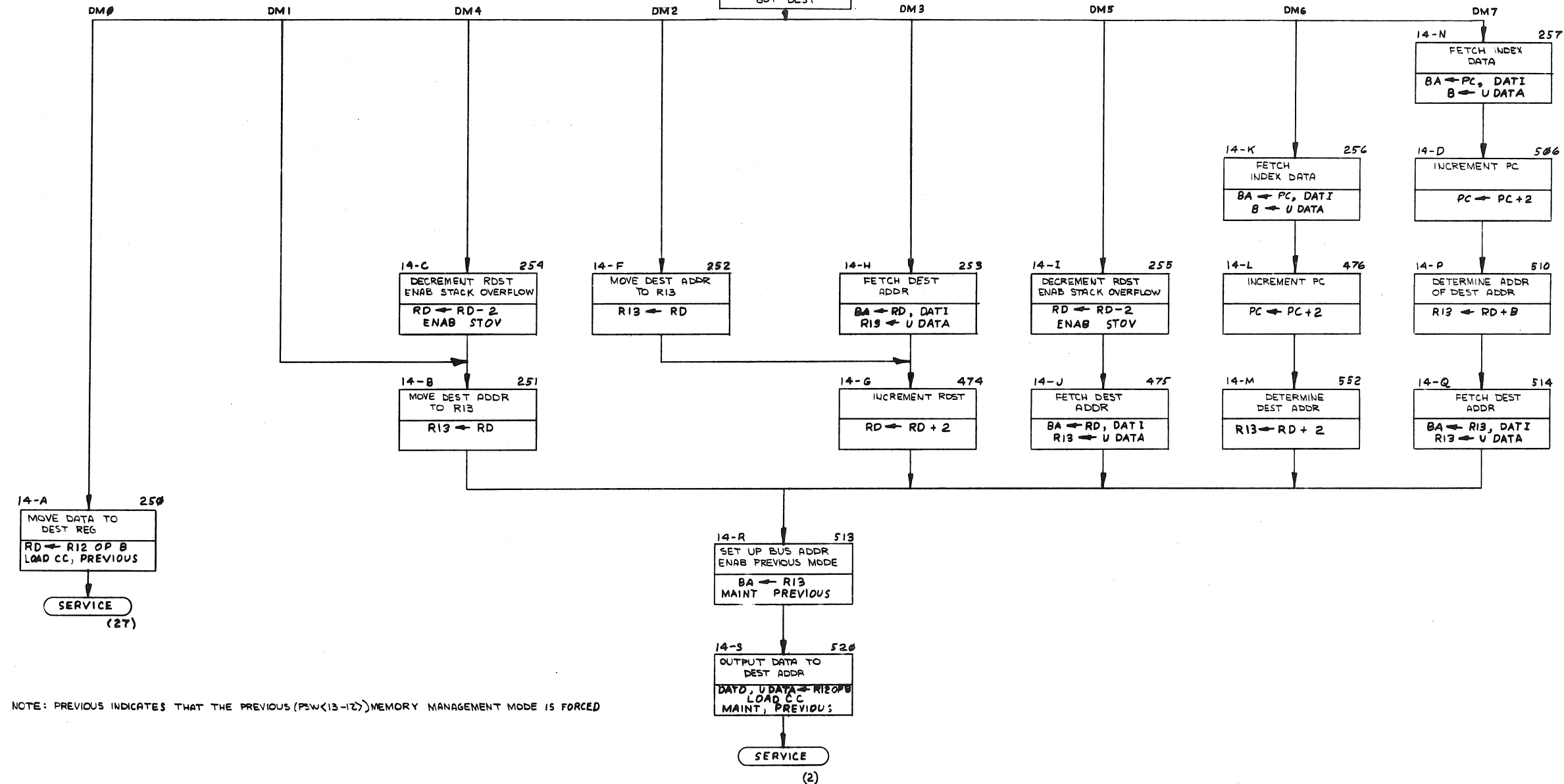
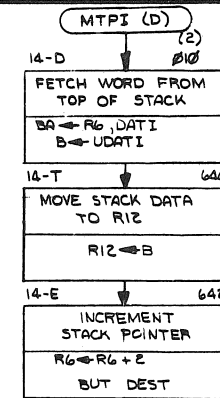


REVISIONS		
CHK	CHANGE NO.	REV.

TITLE KDIE FLOW DIAGRAMS							SIZE CODE D	FD	NUMBER KDII-E-1	REV.
SCALE ++							SHEET 13 OF 26		DIST.	

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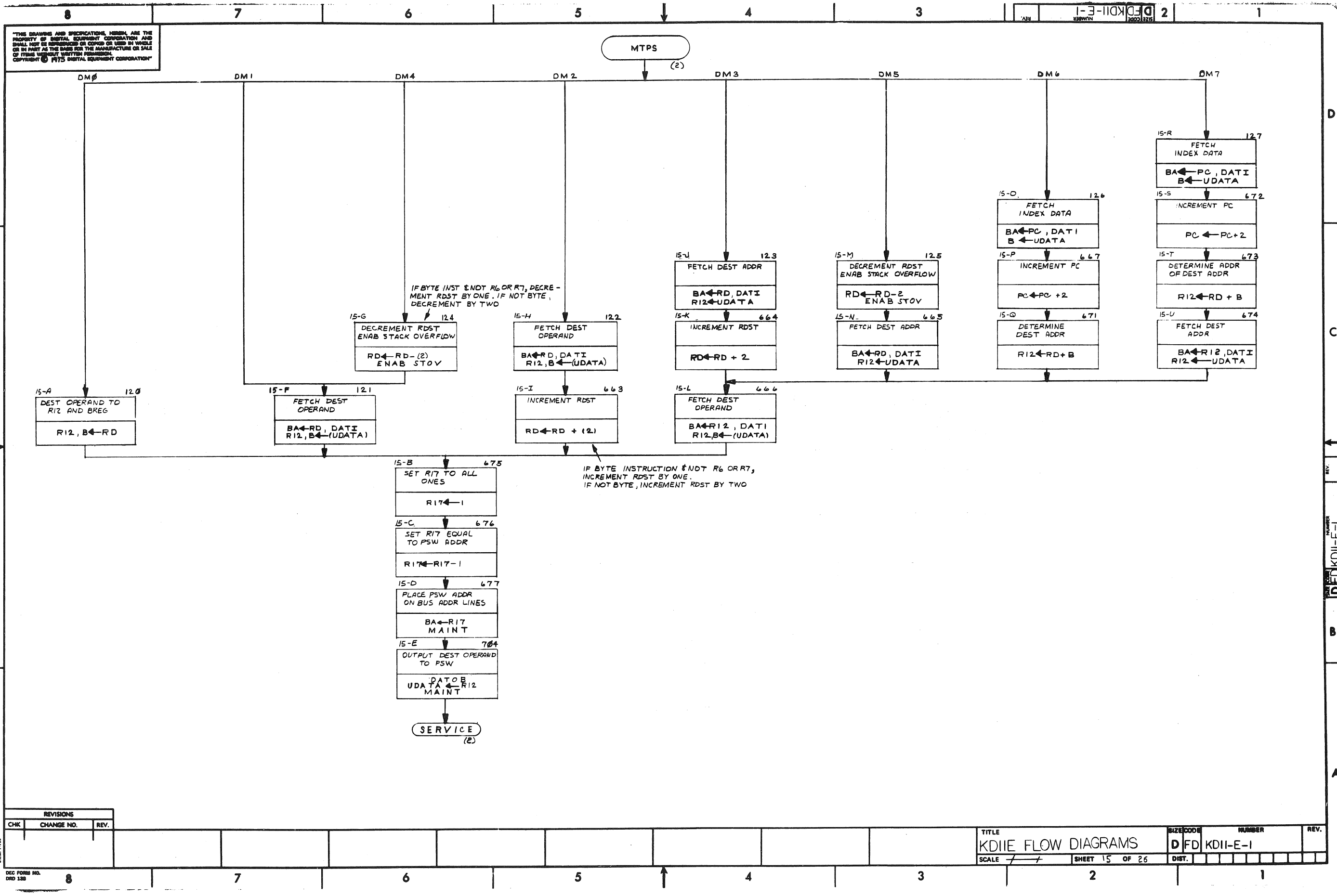
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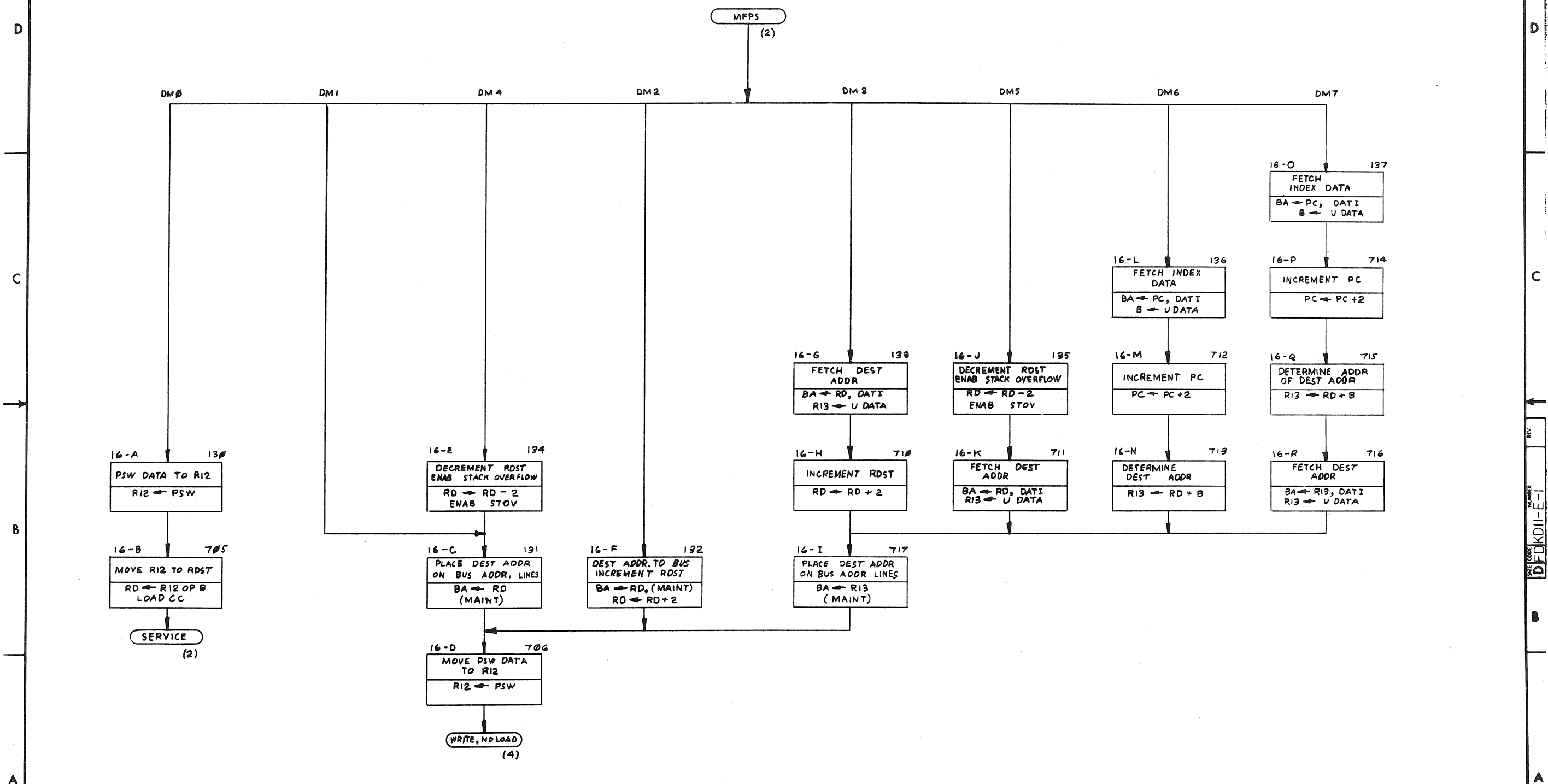
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CHK	CHANGE NO.	REV.

DEC FORM NO. 130

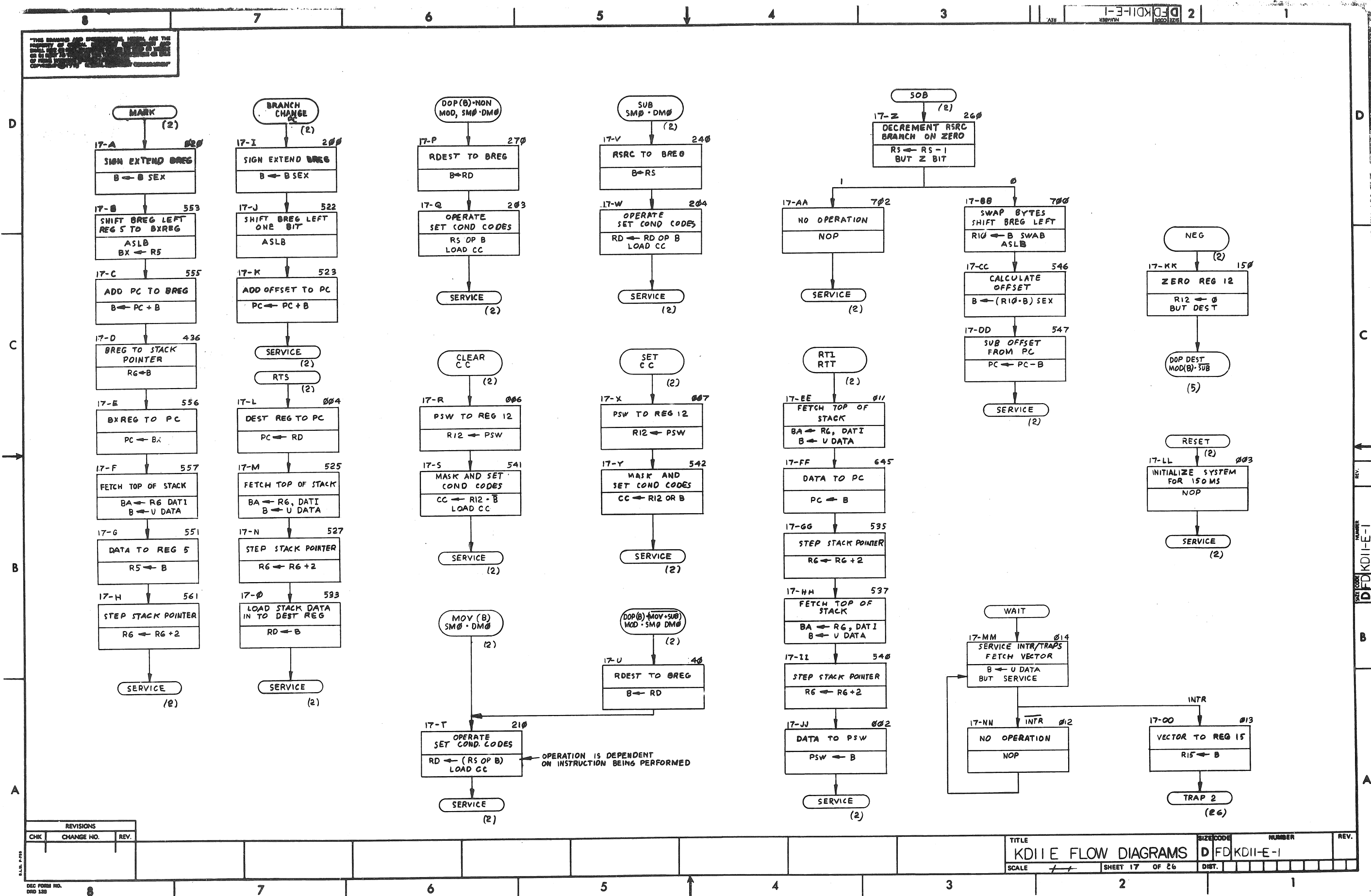
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KDII-E FLOW DIAGRAMS		D	FDKDII-E-1	
SCALE	+	SHEET	14 OF 26	DIST.



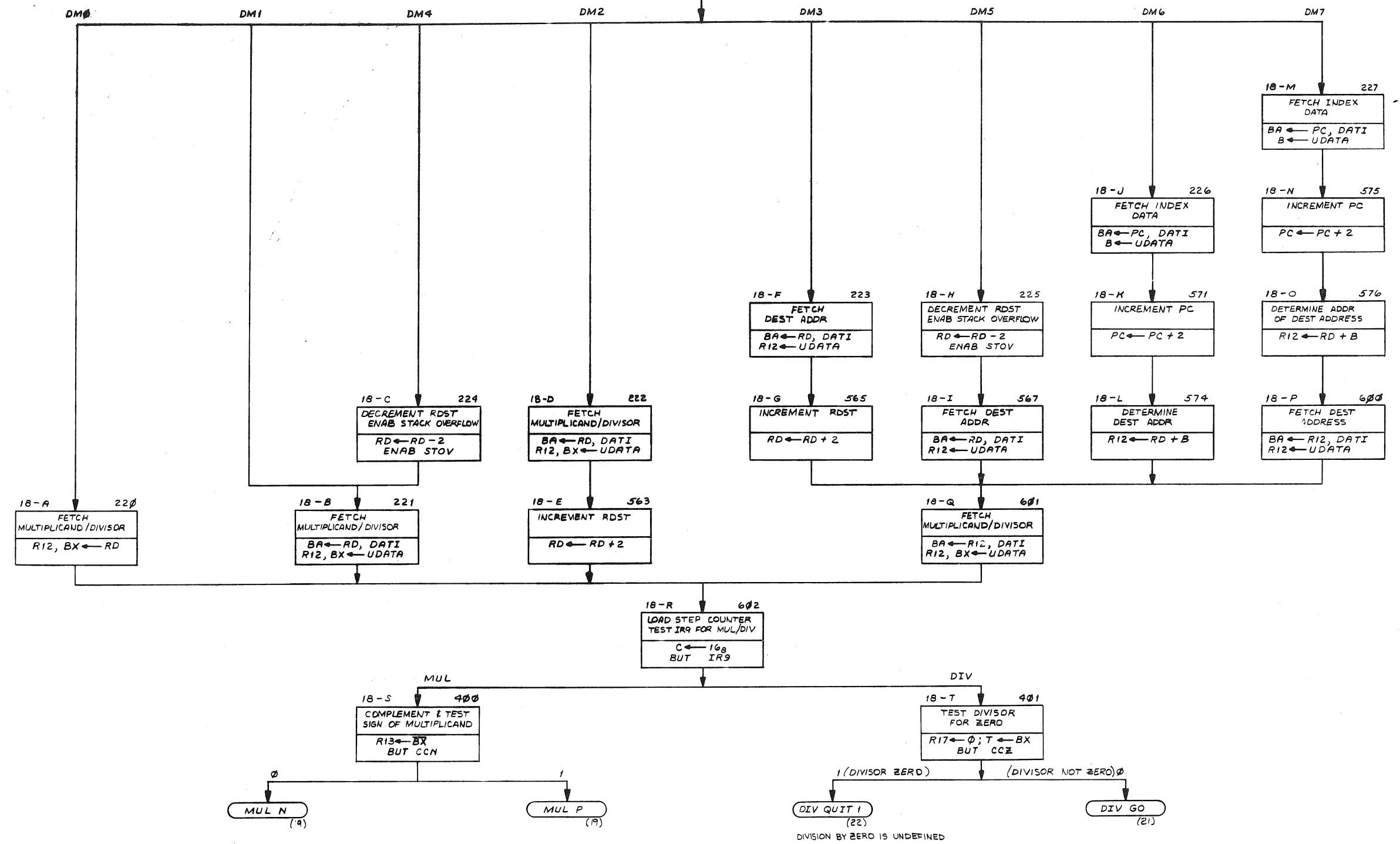
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REVISIONS		
CHK	CHANGE NO.	REV.



MUL - DIV
(2)



REVISIONS		
CHK	CHANGE NO.	REV.

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NOTE THAT FOR MUL VBIT IS ALWAYS ZERO

SHIFT CBIT = 0 INTO BX00 POSITION

SHIFT CBIT = 1 INTO BX00 POSITION

SHIFT CBIT = 0 INTO BX00 POSITION

THIS SECTION OF CODE ACCUMULATES THE COND. CODES IN THE LOWER FOUR BITS OF THE BX REG

SHIFT N BIT INTO BX00 POSITION

SHIFT ZBIT = 0 INTO BX00 POSITION

LP ZERO

SHIFT ZBIT = 1 INTO BX00 POSITION

SHIFT VBIT = 0 INTO BX00 POSITION

SERVICE (2)

SET CC (19)

STORE LP (19)

20-A 607
STORE LOWER PRODUCT
RSV1 ← BX

20-B 610
COUT IS THE SIGN OF THE PRODUCT
R17 ← 0, T ← RS + RS
SL(BX - COUT)

20-N 701
TEST UPPER PRODUCT FOR ZERONESS
T ← B
BUT CCZ

HP NOT ZERO

HP ZERO

20-C 410
TEST UPPER PRODUCT FOR ALL ONES
T ← B
BUT CCZ

20-H 412
TEST LOWER PRODUCT FOR ZERONESS
T ← RSV1, SL(BX-0)
BUT CCZ

UPPER PRODUCT ALL ONES

UPPER PRODUCT NOT ALL ONES

20-D 422
TEST MSB OF LOWER PRODUCT
T ← RSV1, SL(BX-0)
BUT CCN

20-E 420
SET VBIT
SL(BX-0)

20-I 416
SET ZBIT
NOTE THAT R17 = 0
BX ← R17 + BX + 1

20-J 414
TEST MSB OF LOWER PRODUCT
T ← RSV1, SL(BX-0)
BUT CCN

20-F 515
CLEAR CBIT
SL(BX-0)

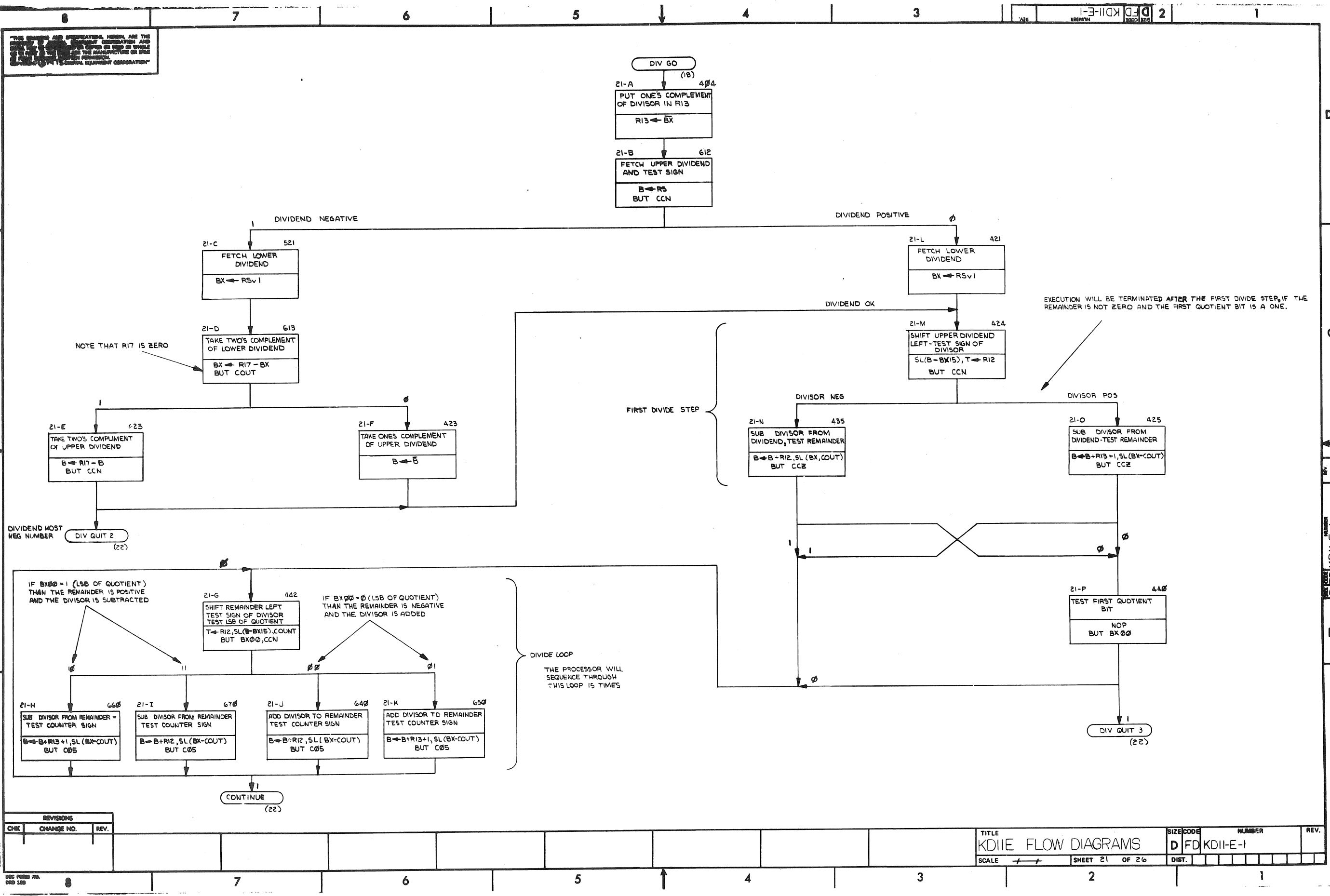
20-G 415
SET CBIT
SL(BX-1)

20-K 511
SET CBIT
SL(BX-1)

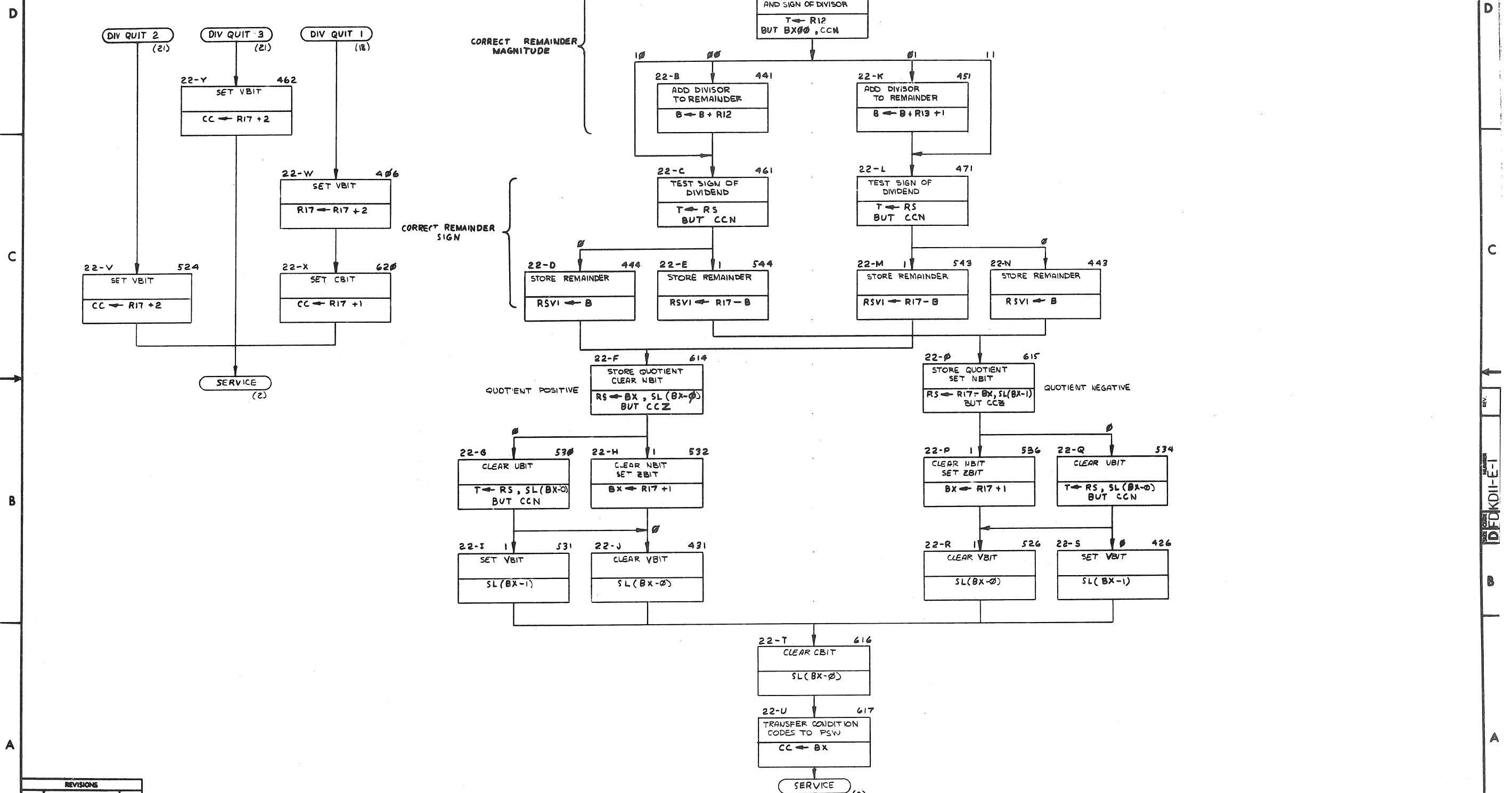
20-L 411
CLEAR CBIT
SL(BX-1)

20-M 611
TRANSFER CONDITION CODES TO PSW
CC ← BX

REVISIONS		
CHK	CHANGE NO.	REV.



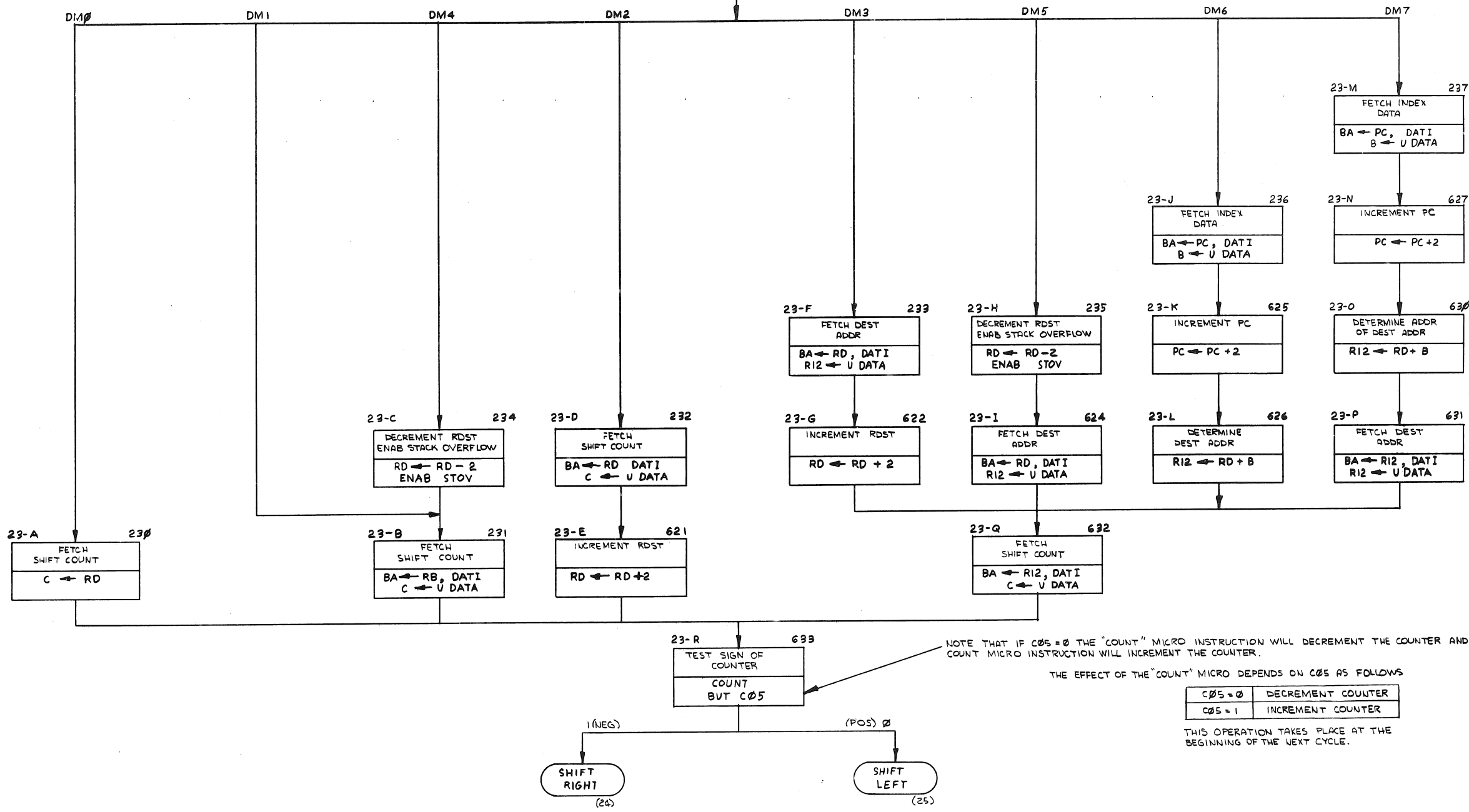
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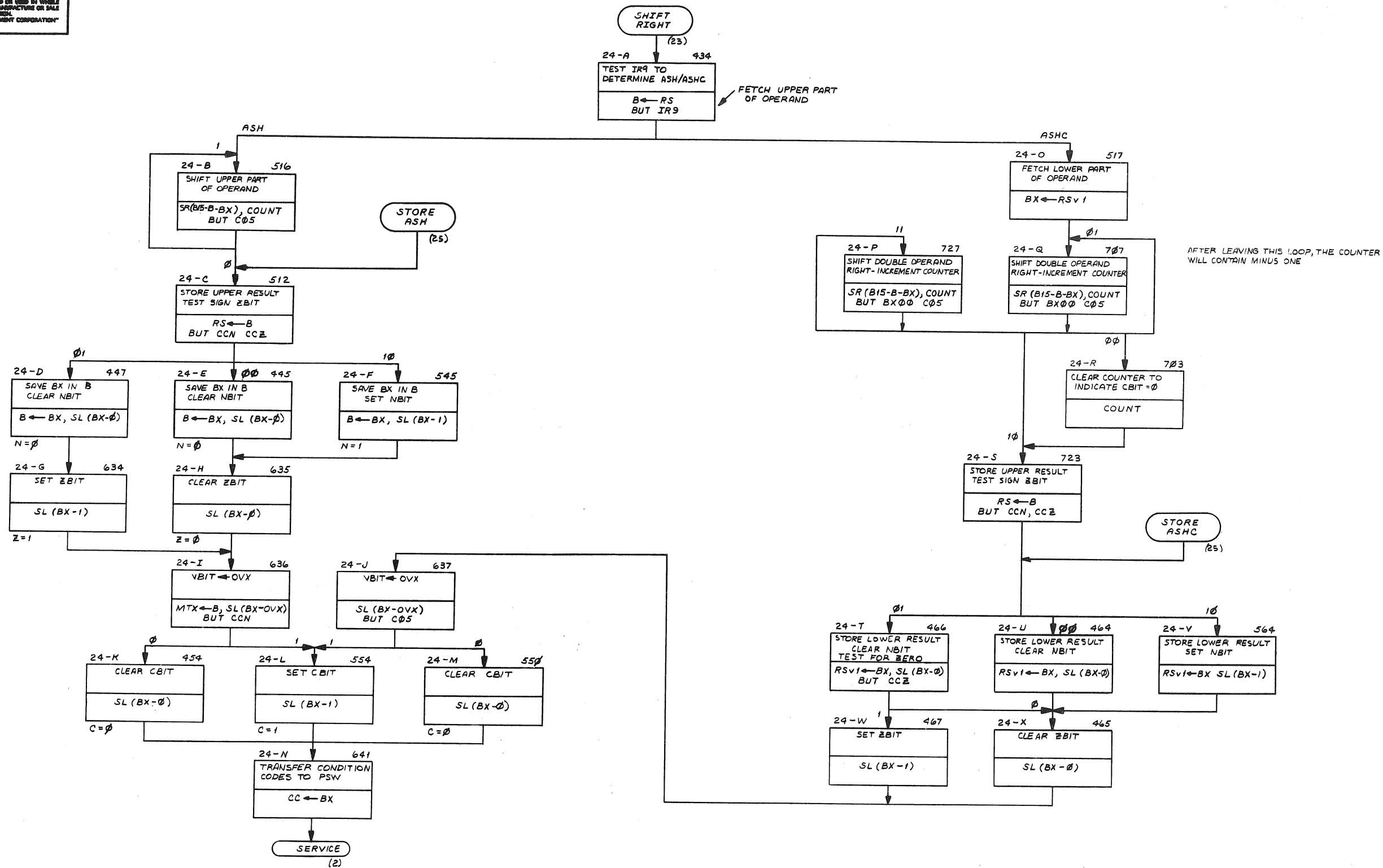
ASH - ASHC
(2)



REVISIONS		
CHK	CHANGE NO.	REV.

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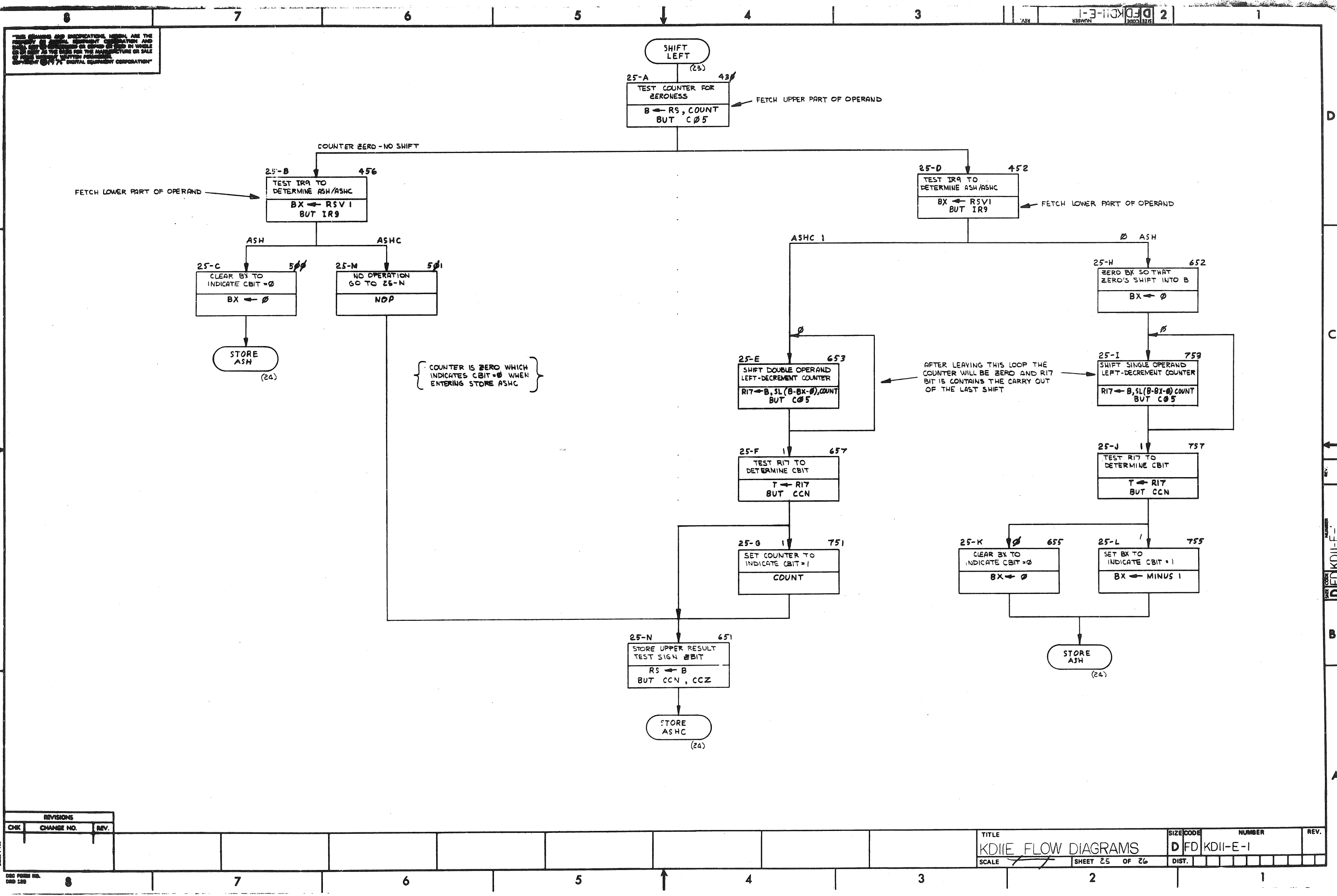
REV. 1
NUMBER 2
SIZE CODE D
FDKDII-E-1

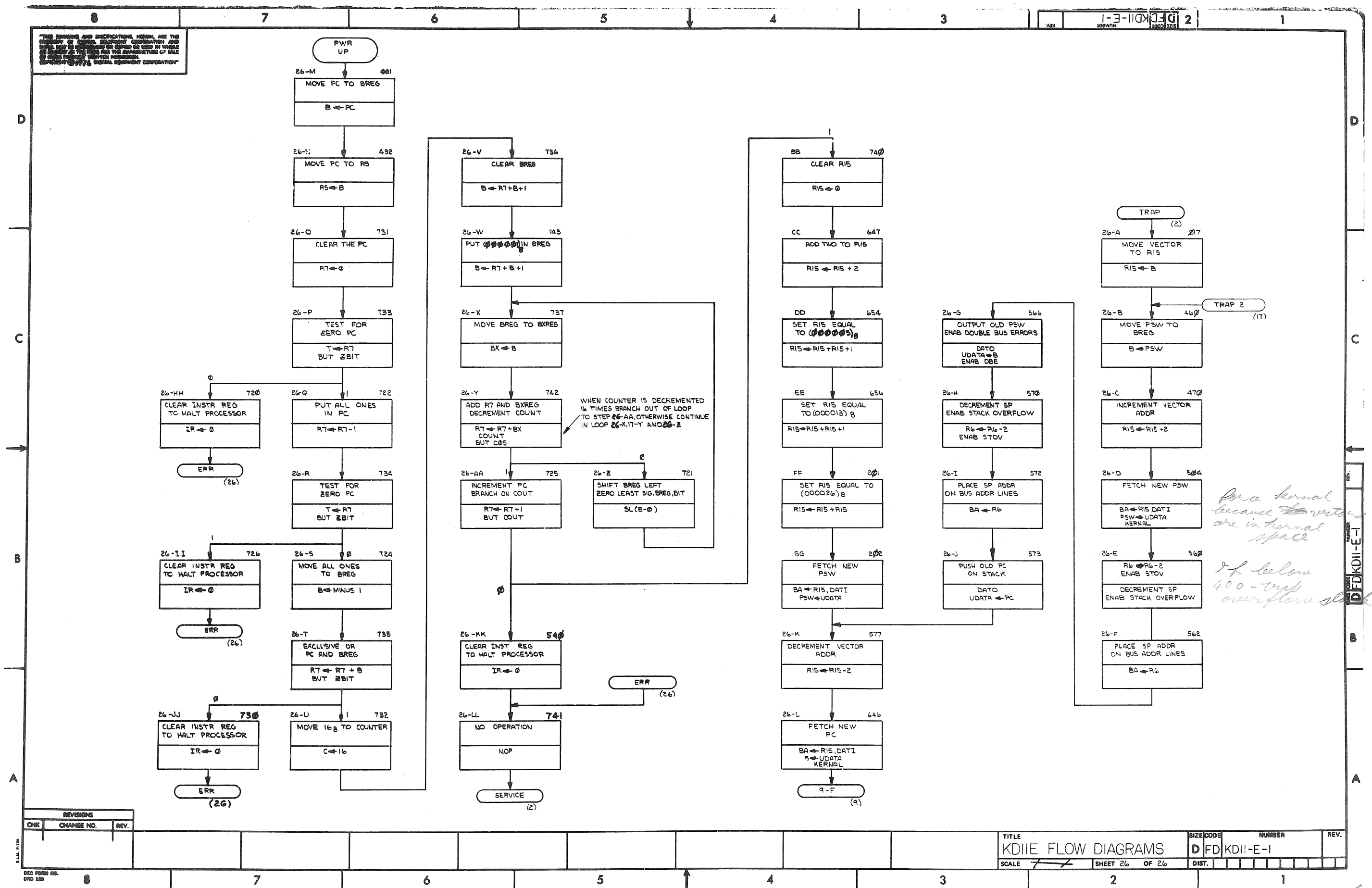


REVISIONS		
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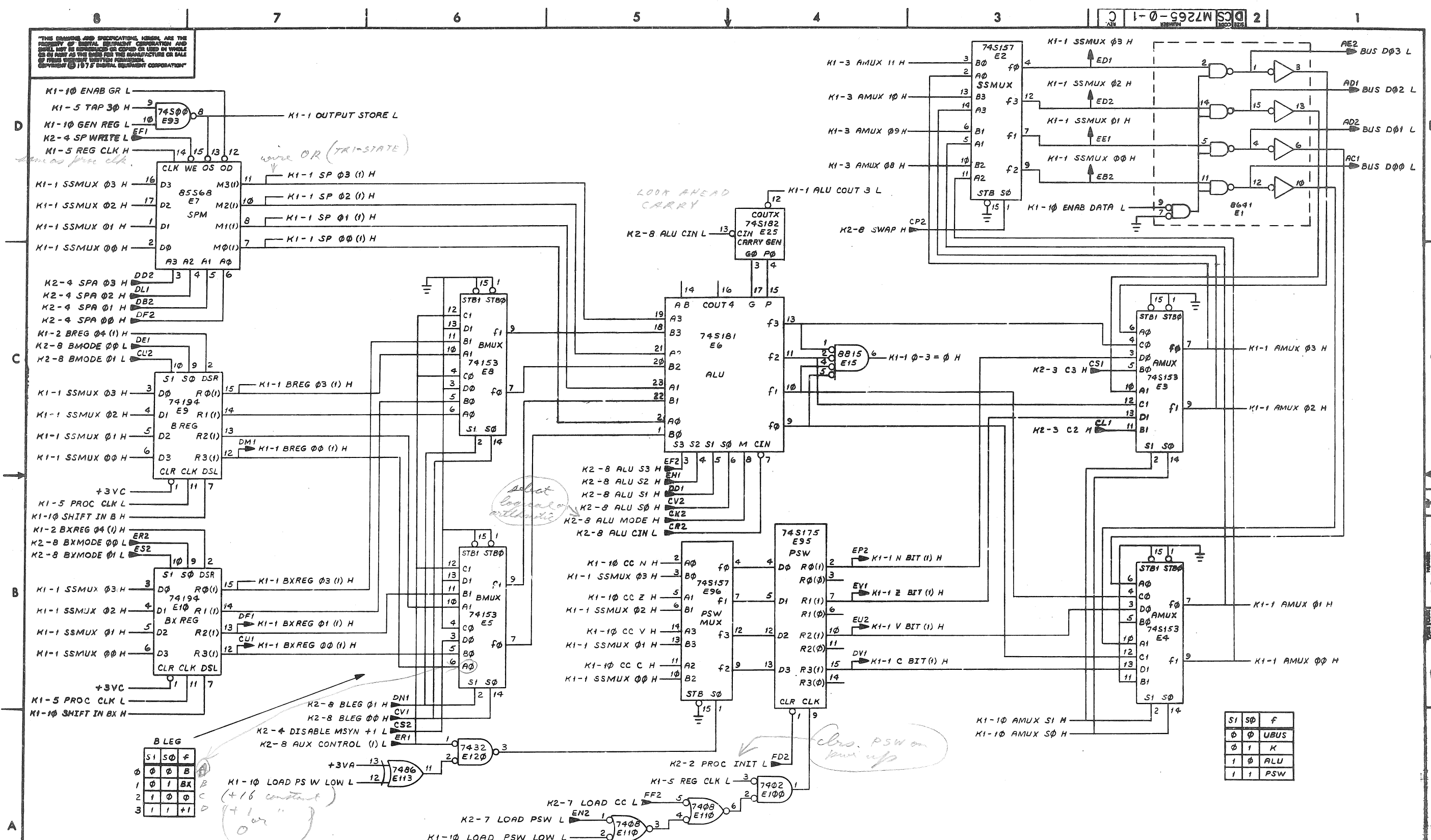
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TITLE
KDIIIE FLOW DIAGRAMS
SCALE 7-7
SHEET 24 OF 26
SIZE CODE D
FDKDII-E-1
NUMBER
REV.

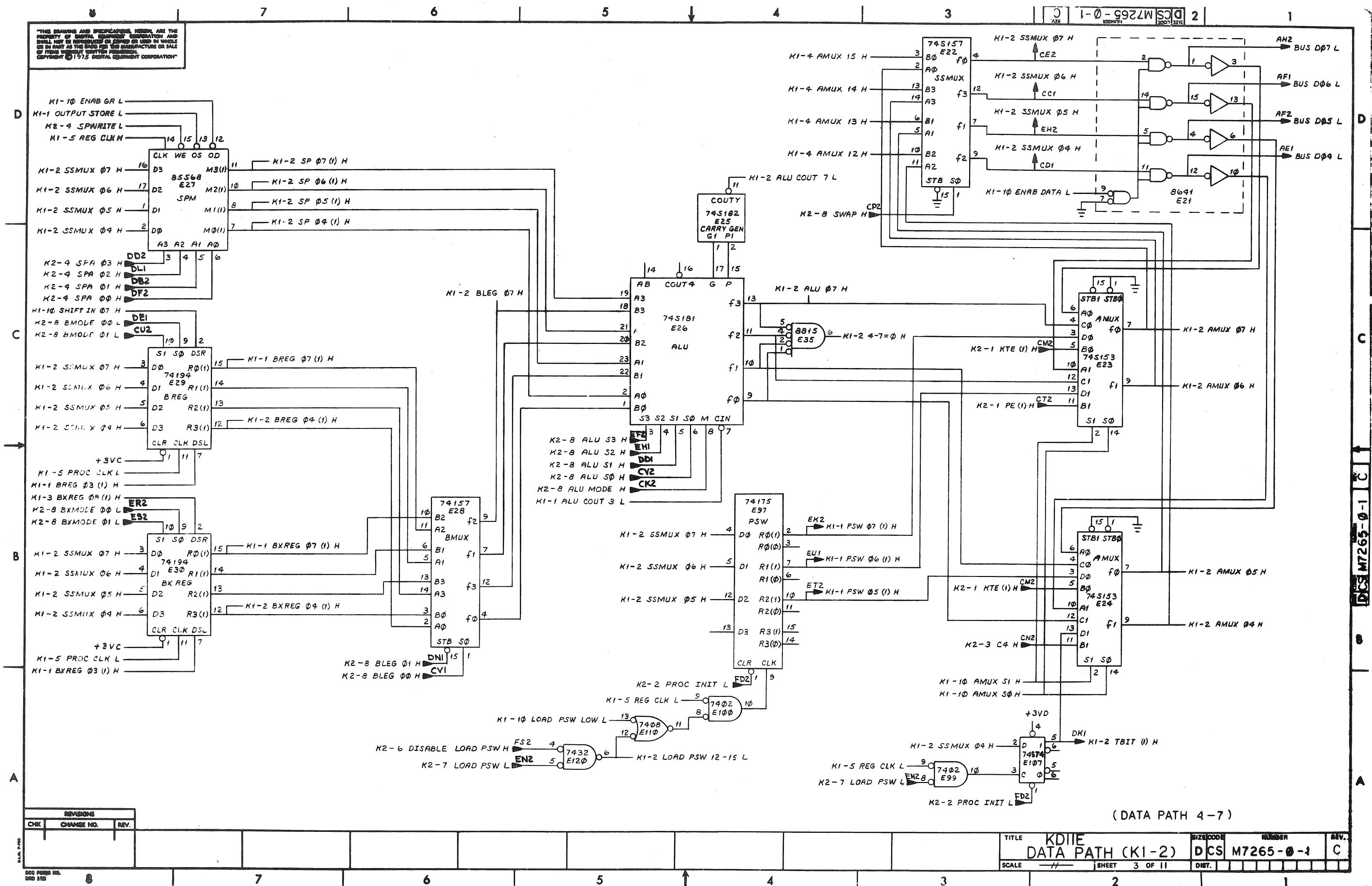




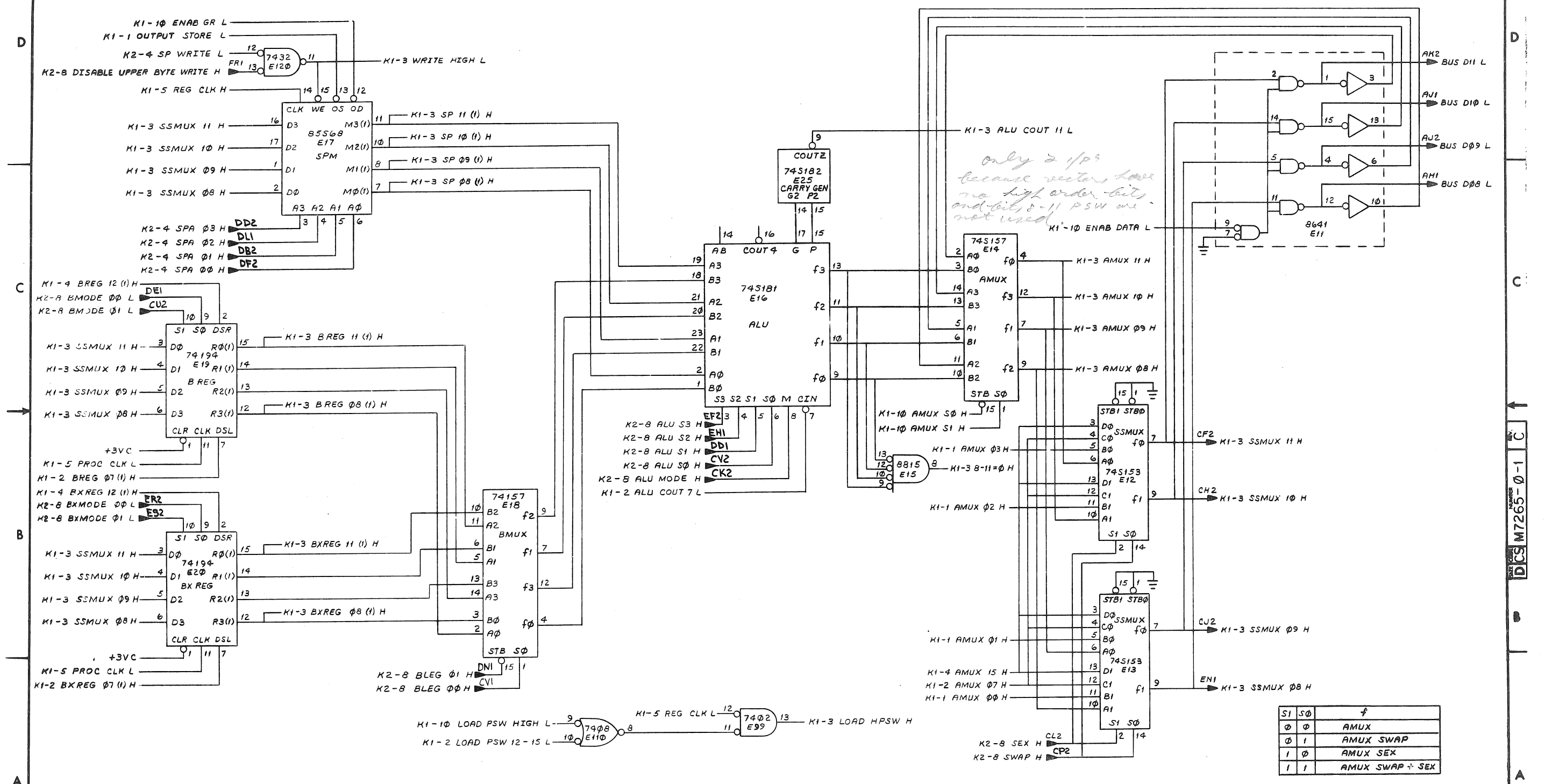
4 BIT SLICE



(DATA PATH 0-3)



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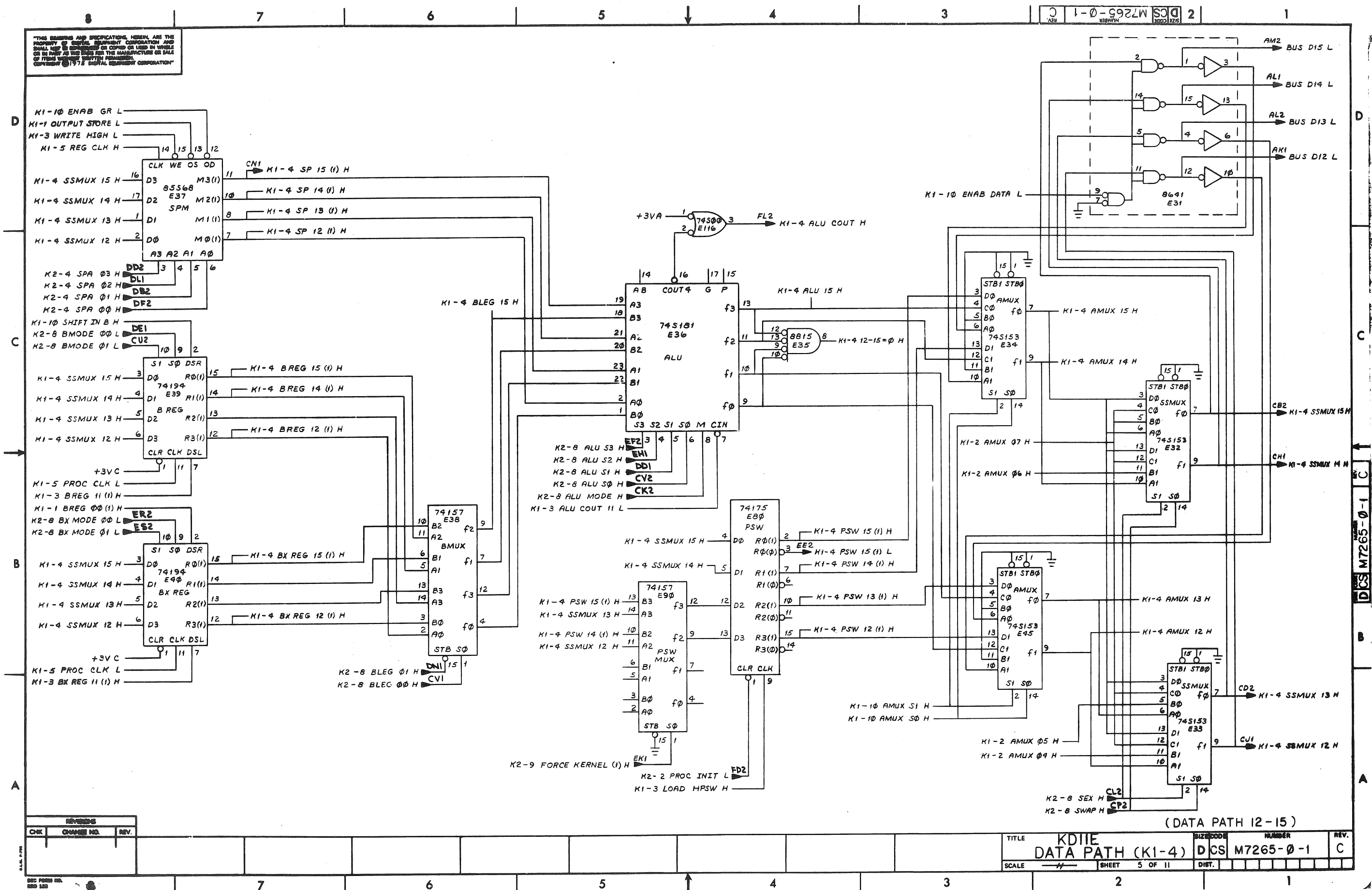


S1	S0	f
0	0	AMUX
0	1	AMUX SWAP
1	0	AMUX SEX
1	1	AMUX SWAP + SEX

REVISIONS		
CHK	CHANGE NO.	REV.

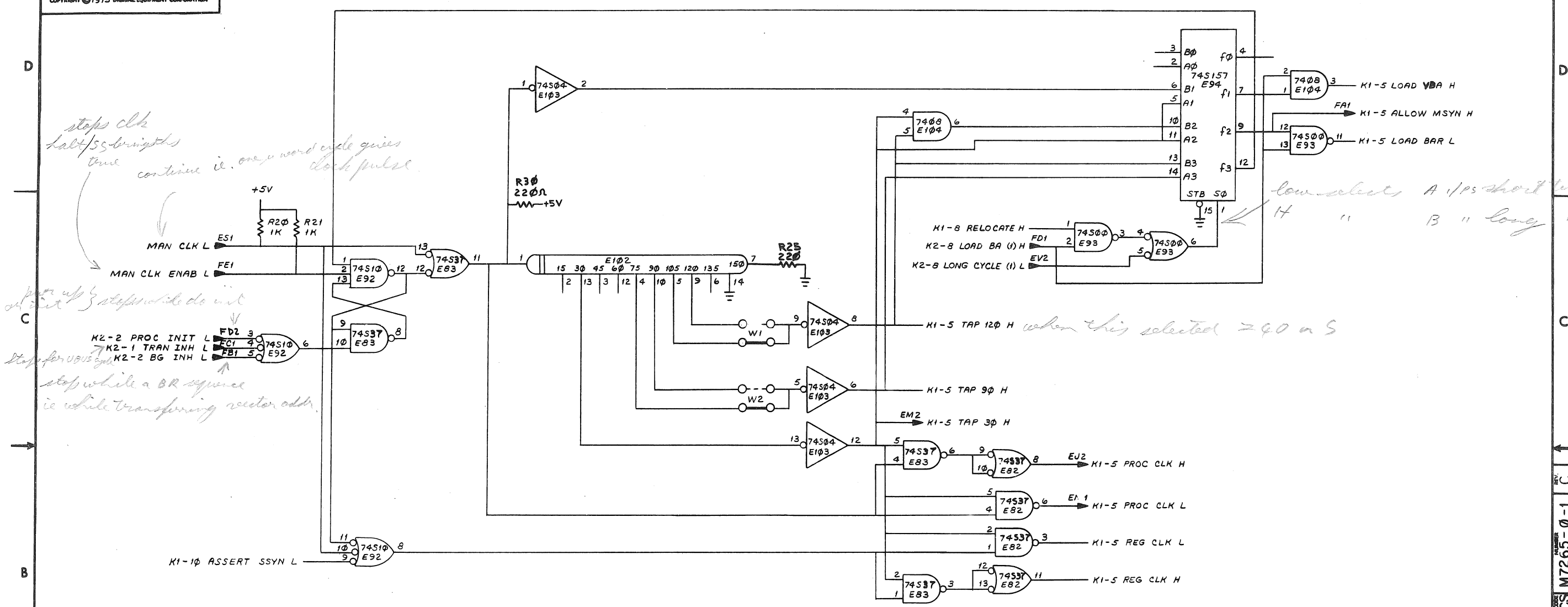
(DATA PATH 8-11)

TITLE	KD11E DATA PATH (K1-3)	SIZE/CODE	DCS M7265-0-1	NUMBER		REV.	C
SCALE		SHEET	4 OF 11	DIST.			



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1-0-0972LW DCS M7265-0-1 2

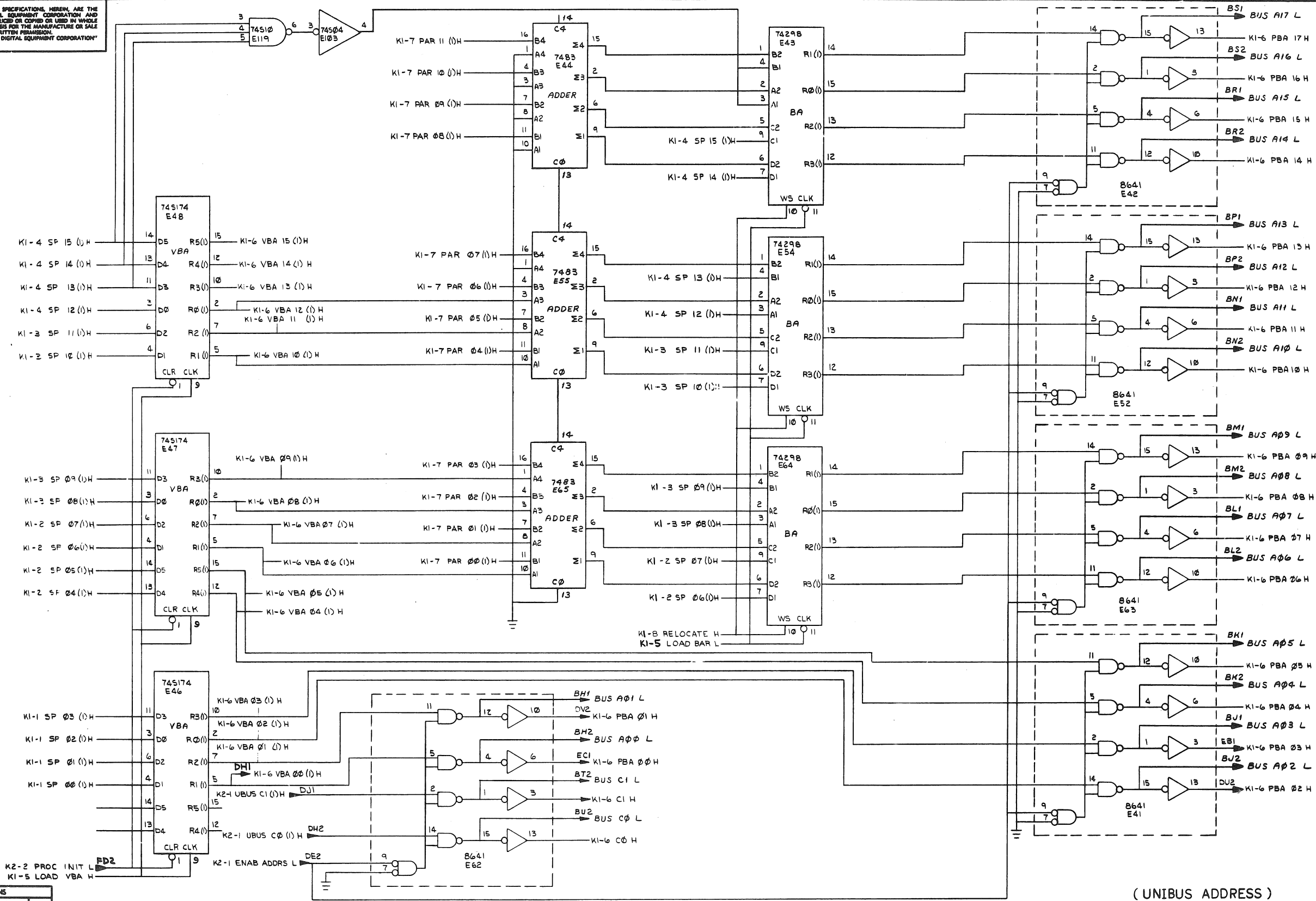


REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		SIZE CODE	NUMBER	REV.
KDIIE DATA PATH (K1-5)		DCS	M7265-0-1	C
SCALE	SHEET	DIST.		
1:1	6 OF 11			

(CPU CLK)

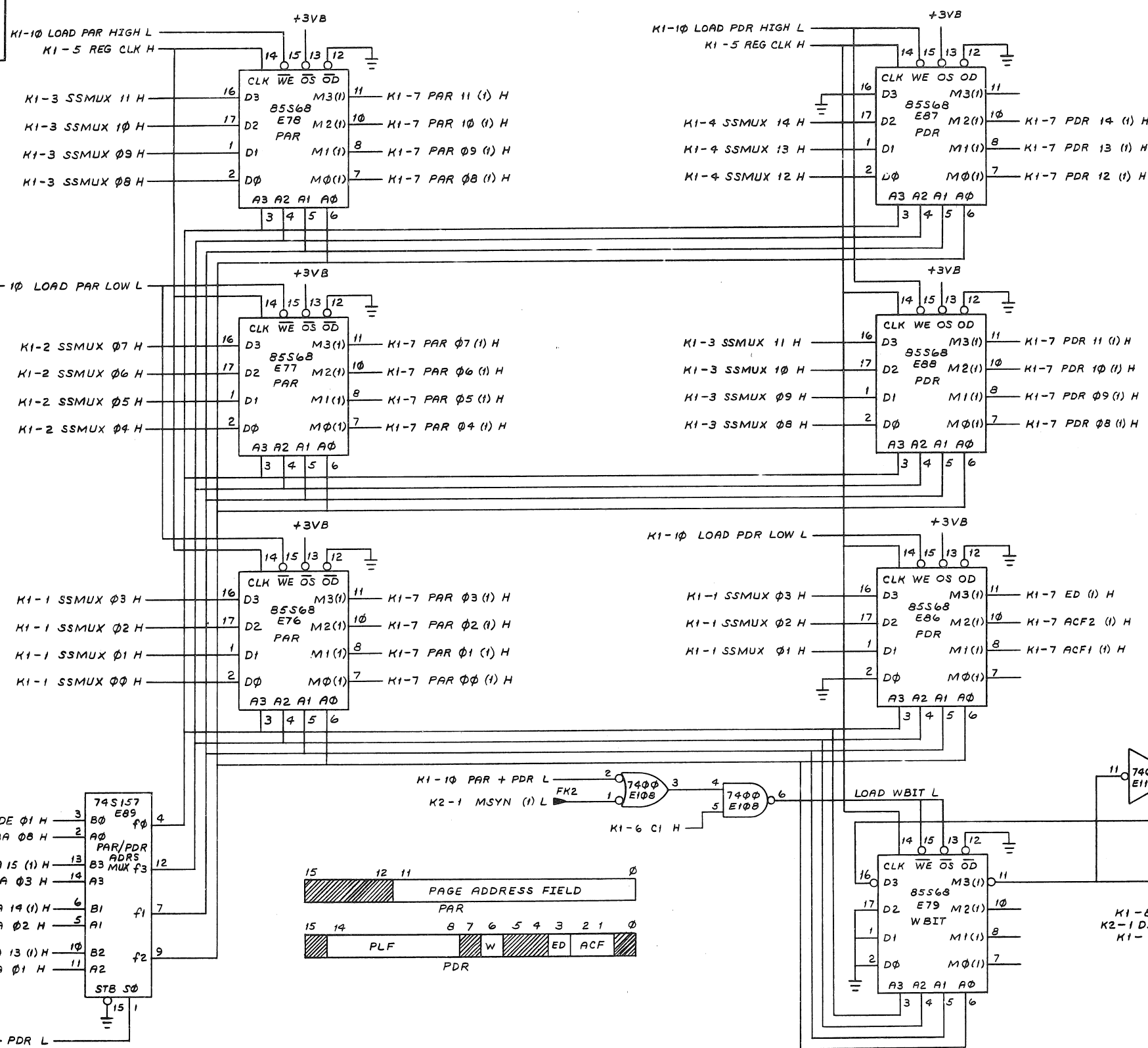
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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		SIZE	CODE	NUMBER	REV.
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7 OF 11					

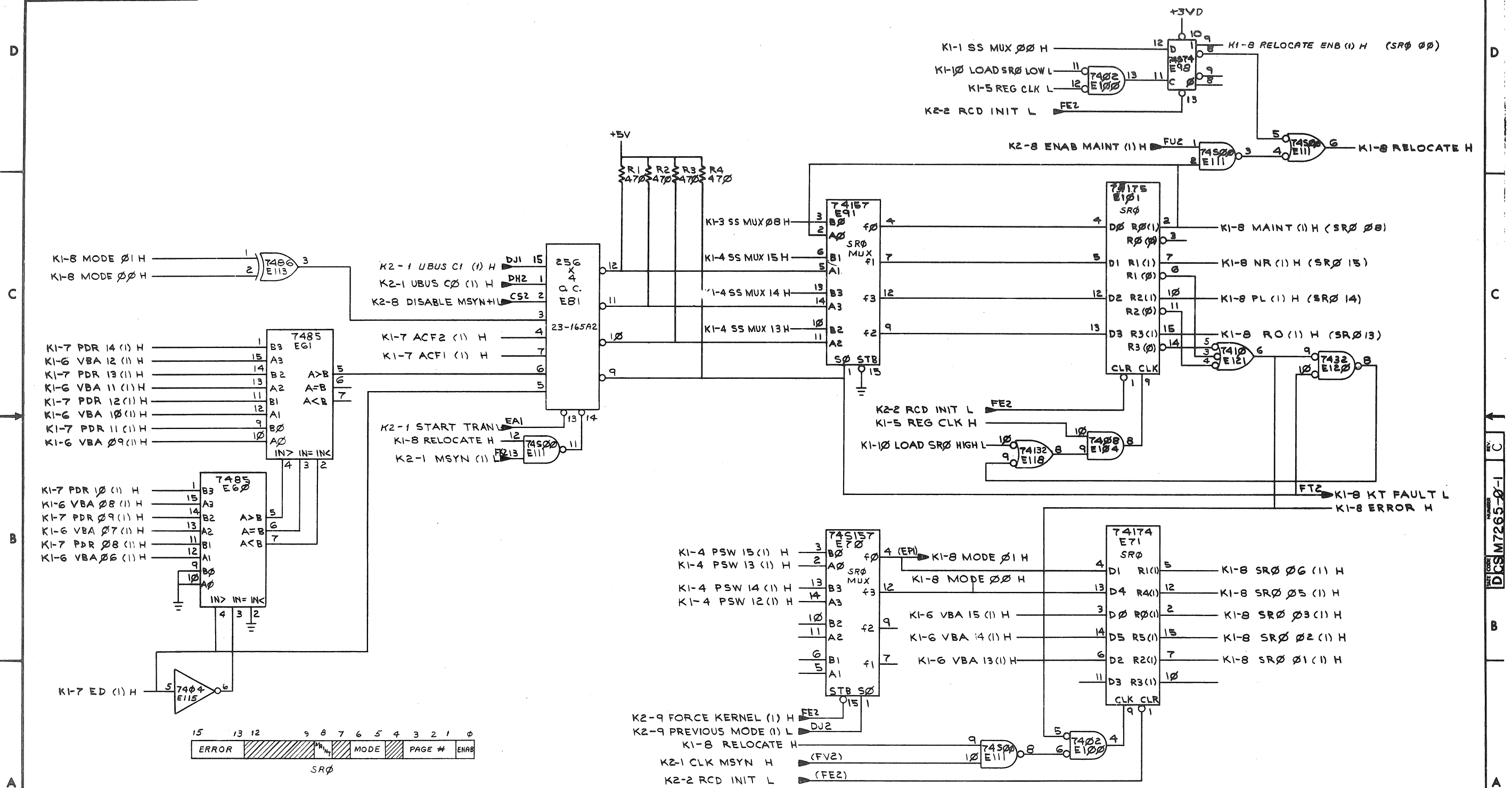
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ED	DIRECTION
0	UP
1	DOWN

ACF2	ACF1	DESCRIPTION
0	0	NONRESIDENT
0	1	RESIDENT READ ONLY
1	0	ABORT ALL ACCESSES
1	1	RESIDENT READ/WRITE

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SI	S0	F
H	H	SR0
H	L	SR2
L	H	PAR
L	L	PDR

D

C

B

A

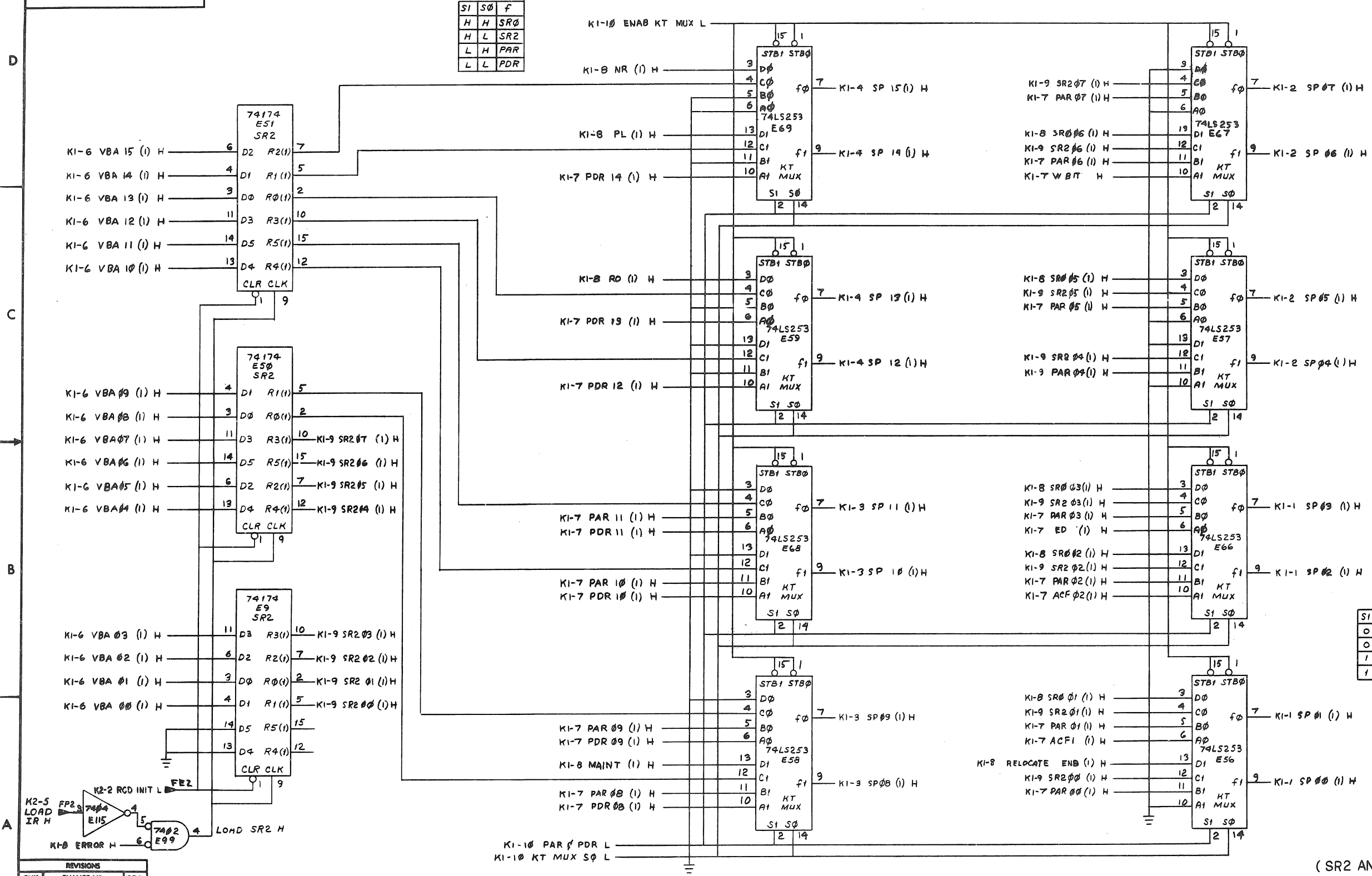
D

C

B

A

DCSM7265-0-1

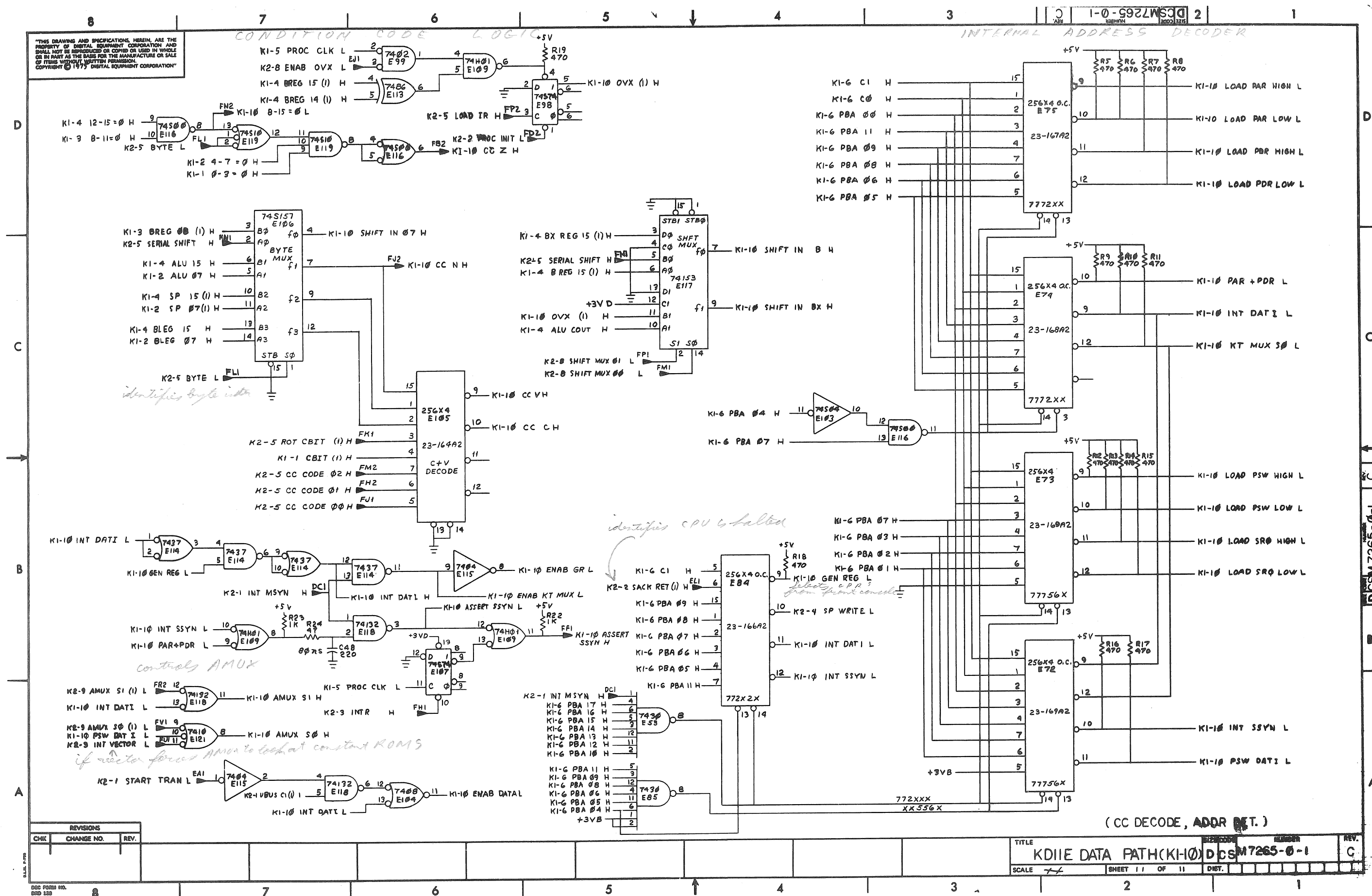


(SR2 AND KT MUX)

CHK	CHANGE NO.	REV.

TITLE	SIZE CODE	NUMBER	REV.
KDIIE DATA PATH(KI-9)	DCSM7265-0-1	C	
SCALE	SHEET 10 OF 11	DIST.	

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8

7

6

5

4

3

2

1

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1	E71	IC ROM 256X4 OC	23A22A2	74	1	E26	IC DEC 74537	1912746-00	52
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A/R		WIRE (WRAP) 30 AWG	9105740-35	92	1	E62	IC ROM 256X4 OC	23176A2	70
1	E83	IC DEC 74510	1910536	93	1	E67	IC ROM 512X4 TS	23014A9	71
					1	E68	IC ROM 256X4 OC	23172A2	72
					1	E69	IC ROM 256X4 OC	23171A2	73

57

58

1

2

3

4

44

FBS

REF

15.69 FBS REF

+5V

GND

+3VA

+3V

IC DEC 8640	1	8
IC DEC 8837	8	16
IC DEC 745175	8	16
IC DEC 74174	8	16
IC DEC 74175	8	16
IC DEC 745174	8	16
IC DEC 745153	8	16
IC DEC 74123	8	16
IC DEC 7442	8	16
IC DEC 74193	8	16
IC DEC 7473	11	4

IC TYPE

GND

+5V

GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY EXCEPTIONS ARE STATED ABOVE

IC PIN LOCATIONS

15.69 FBS REF

+5V

GND

+3VA

+3V

57

58

1

2

3

4

44

FBS

REF

15.69 FBS REF

IC DEC 8640

IC DEC 8837

IC DEC 745175

IC DEC 74174

IC DEC 74175

IC DEC 745174

IC DEC 745153

IC DEC 74123

IC DEC 7442

IC DEC 74193

IC DEC 7473

IC TYPE

GND

+5V

GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY EXCEPTIONS ARE STATED ABOVE

IC PIN LOCATIONS

15.69 FBS REF

+5V

GND

+3VA

+3V

57

58

1

2

3

4

44

FBS

REF

15.69 FBS REF

IC DEC 8640

IC DEC 8837

IC DEC 745175

IC DEC 74174

IC DEC 74175

IC DEC 745174

IC DEC 745153

IC DEC 74123

IC DEC 7442

IC DEC 74193

IC DEC 7473

IC TYPE

GND

+5V

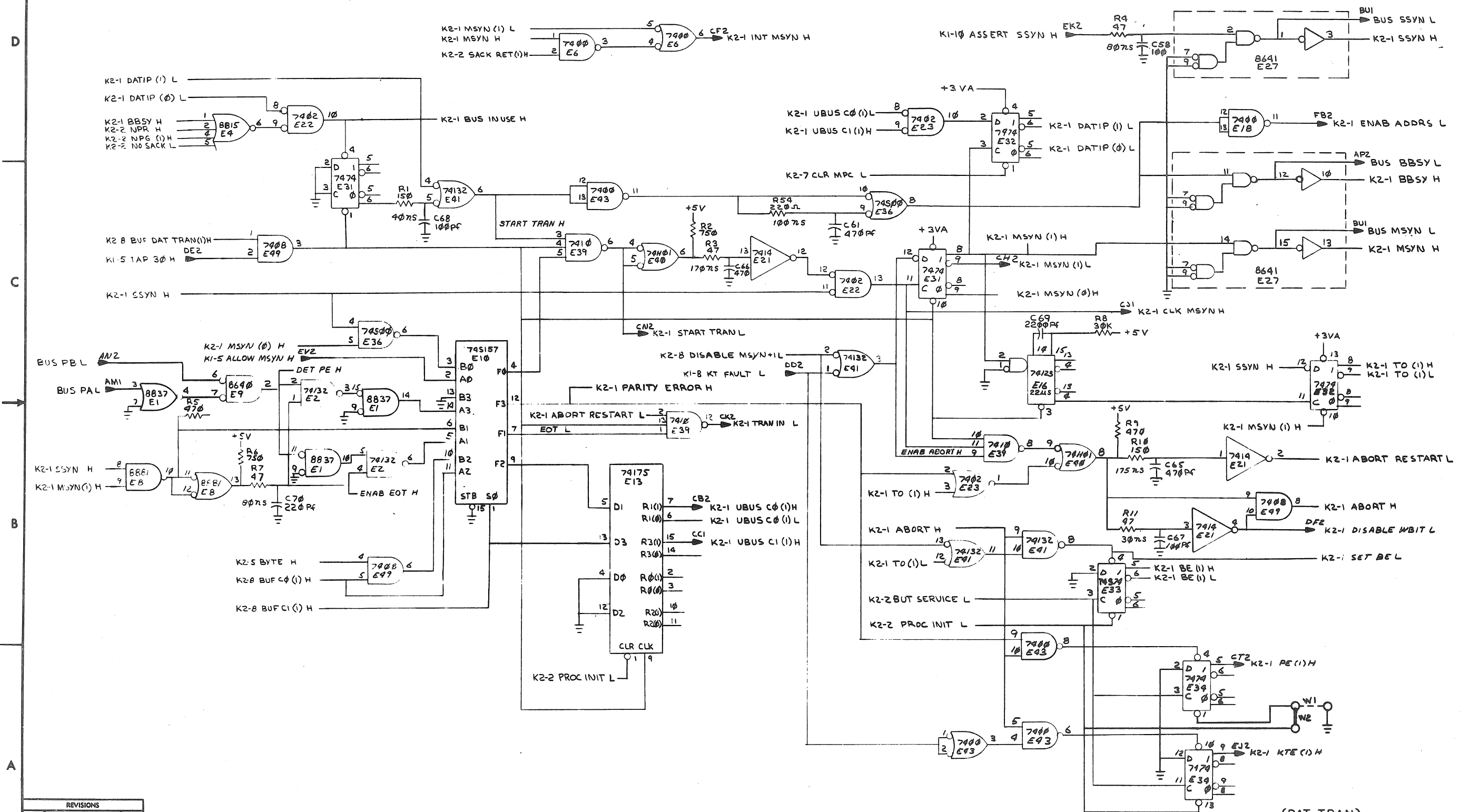
GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY EXCEPTIONS ARE STATED ABOVE

IC PIN LOCATIONS

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1	2	3	4	5	6	7	8
9	10	11	12	13	14	15	16
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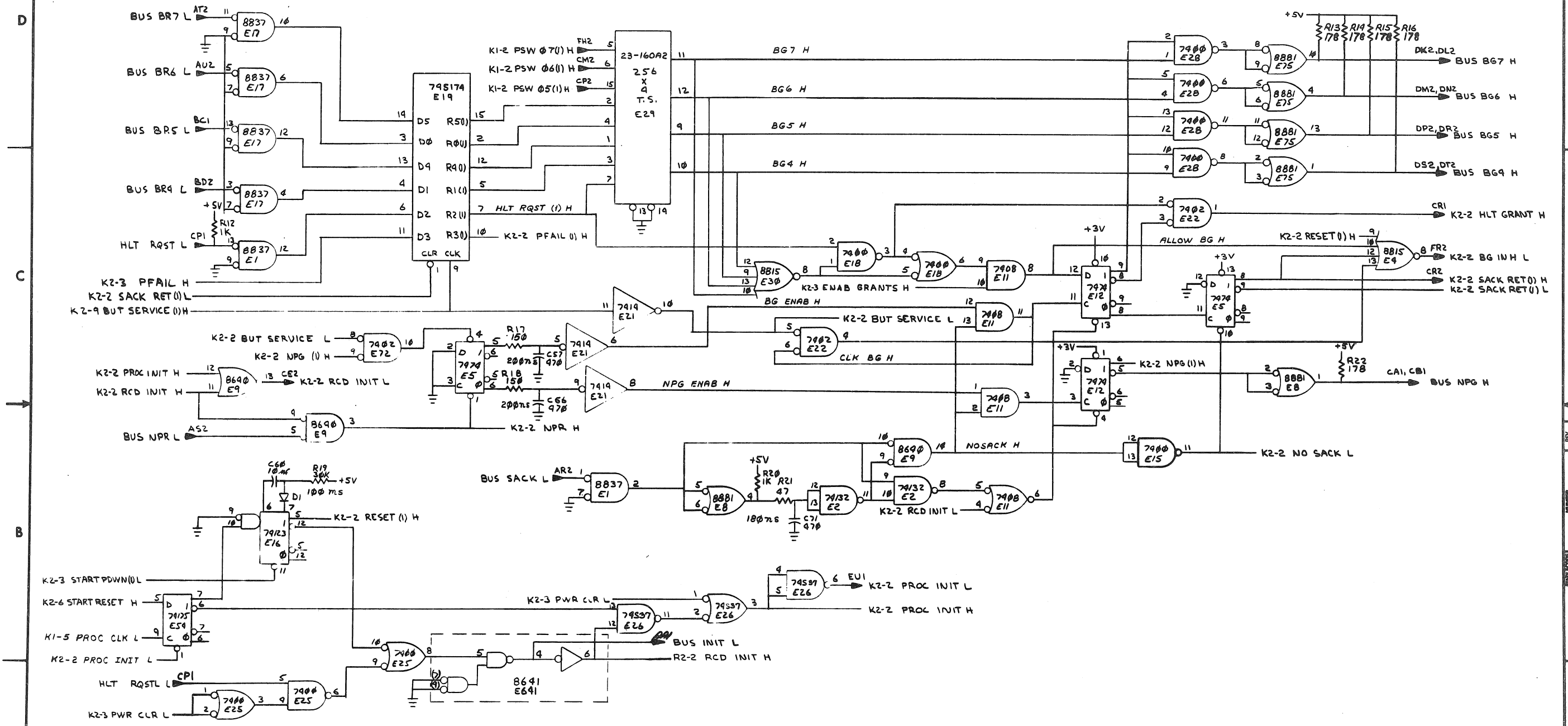
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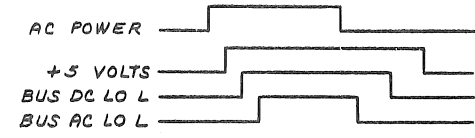
REVISIONS		
CHK	CHANGE NO.	REV.

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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE KDIIE CONTROL (K2-2)		DCSM7266-0-1		REV.	
SCALE	SHEET	3 OF 11	DIST.		



REVISIONS		
CHK	CHANGE NO.	REV.

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converts
SELECTS SP (USER) R6 to R16 if in user mode.

USER MODE SELECTS R16
KERNAL MODE SELECTS R6

forces reg. addr. to odd
from u word
for selecting 32 bit operands.

SI	S0	F
L	L	ROM
L	H	RS
H	L	RD
H	H	BA

A ROM SPA of MICRO INST.
B IR 8-6
C IR 2-4
D PBA 3-0

SPA DST = PBA

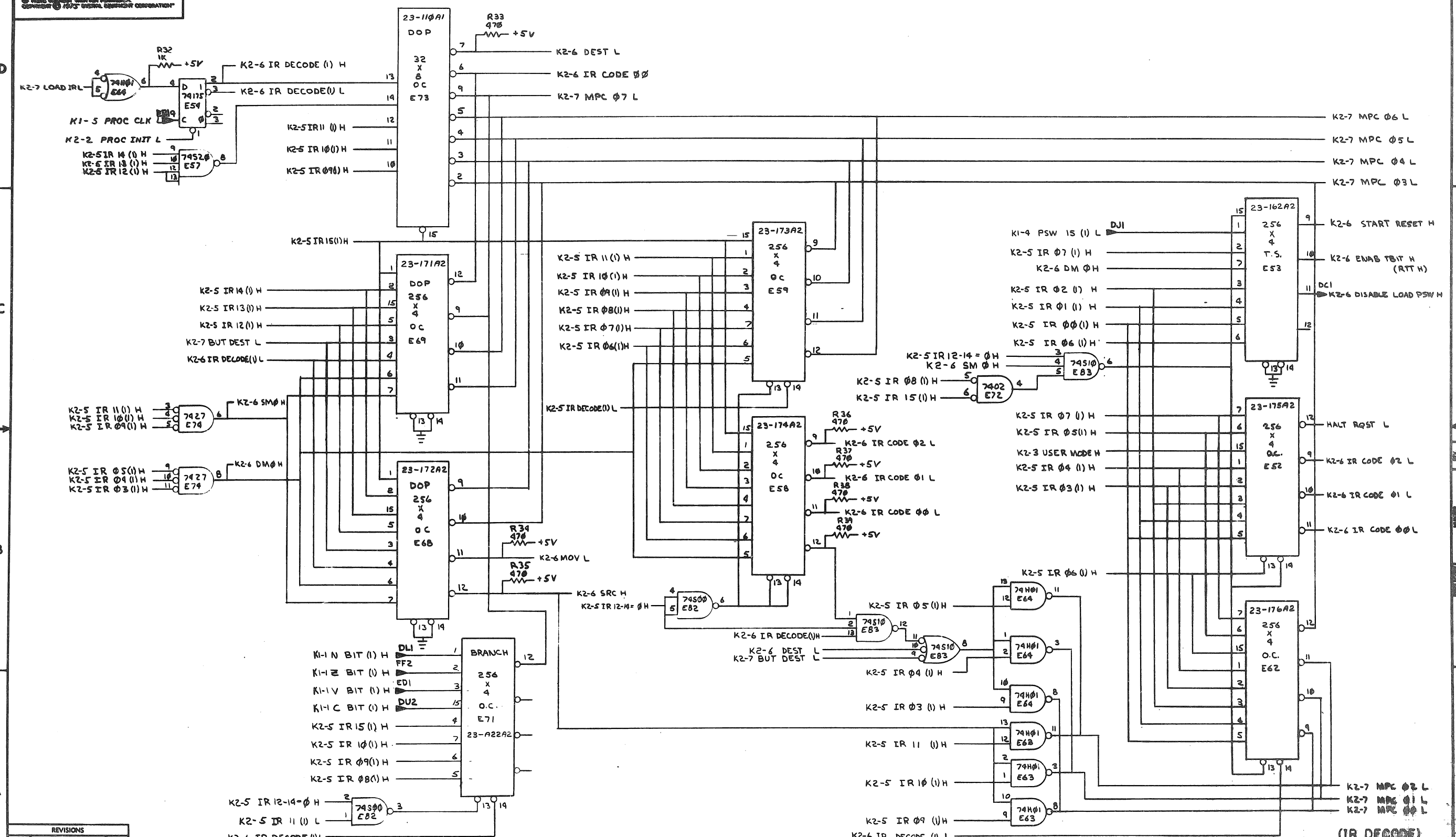
prevents writing into GPR
when machine is running.

write OR to K1-10

(SPA MUX)

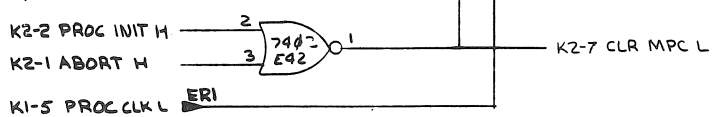
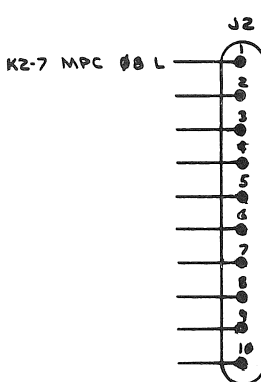
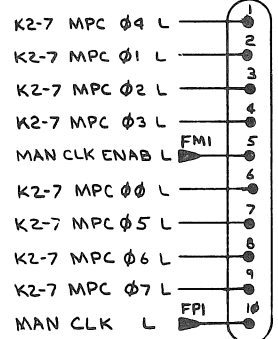
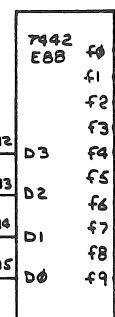
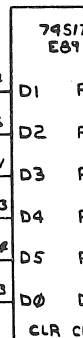
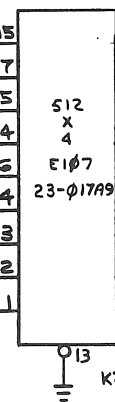
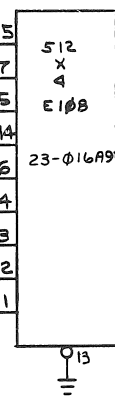
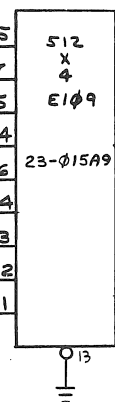
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SCALE	1/1	SHEET	5	OF	11	DIST.	

REVISIONS		
CHK	CHANGE NO.	REV.



REVISIONS		
CHK	CHANGE NO.	REV.

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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE KDIIE CONTROL (K2-7)	
SCALE 	SHEET 6 OF 11

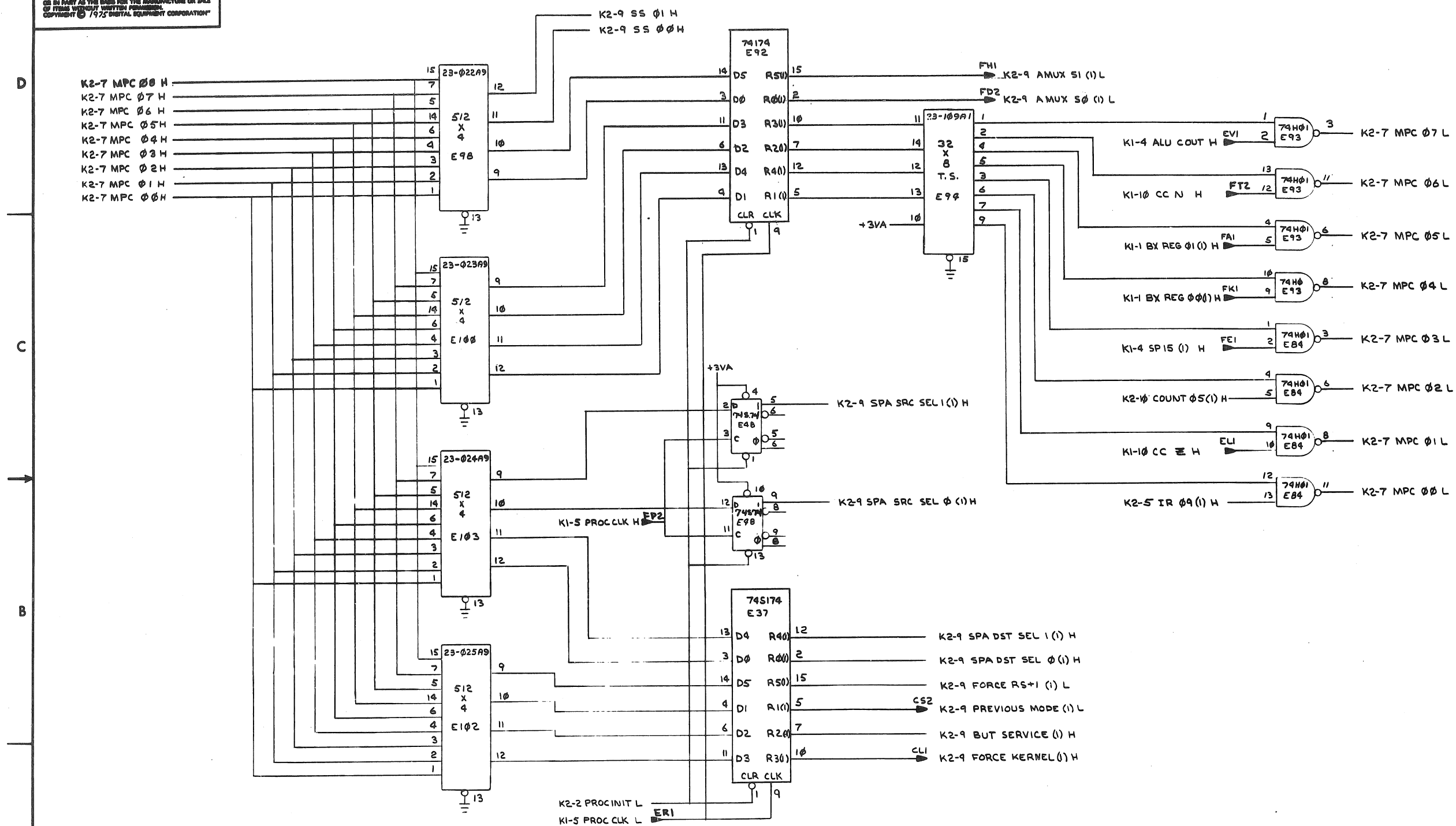
(MPC BUT FIELD)				
SIZE	CODE	NUMBER		
D	CS	M7266-0-1		
DIST.				

REV.
E
[] []

A



TITLE		SIZE CODE		NUMBER		REV	
KDIIE CONTROL (K2-8)		D CS		M7266 0 1		E	
SCALE 1/4"		SHEET 9 OF 11		DIST.			



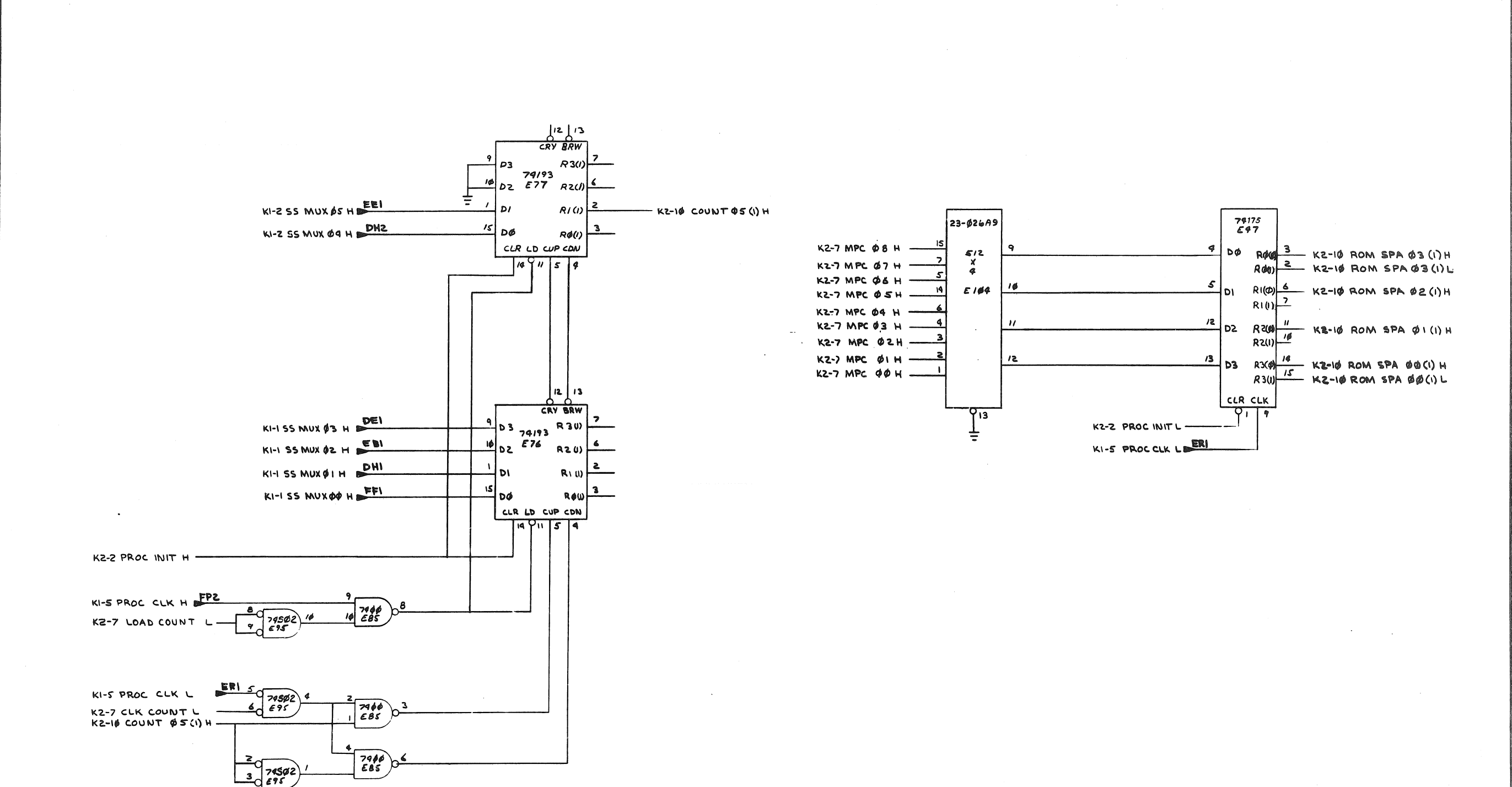
REVISIONS		
CHK	CHANGE NO.	REV.

DEC FORM NO.
DWD 220

(MICRO CODE)

TITLE		SIZE CODE	NUMBER	REV
KDIIE CONTROL (K 2-9)		D CS	M7266-0-1	E
SCALE	$\frac{1}{4}$	SHEET	10 OF 11	DNF.

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REVISIONS		
CHK	CHANGE NO.	REV.

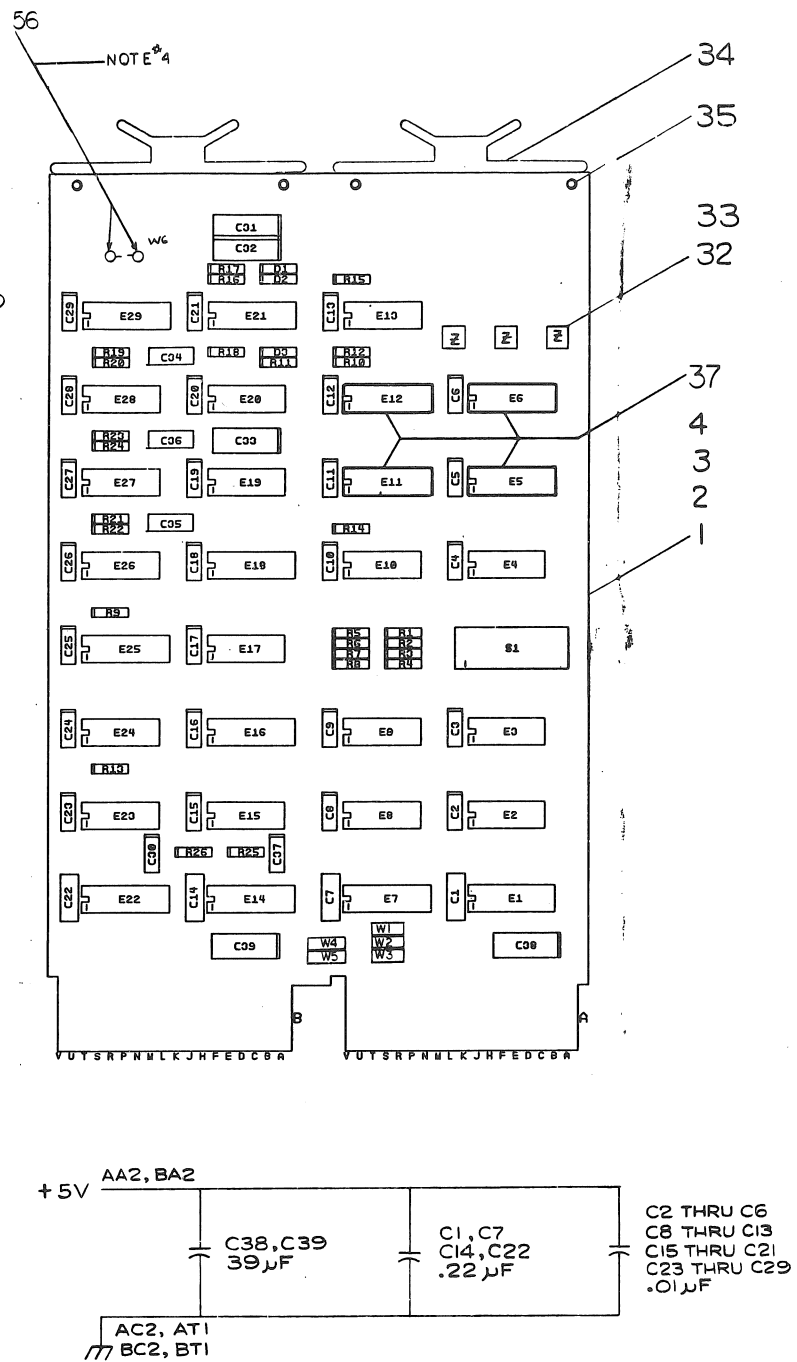
						KDIIE CONTROL (K2-10)		D	CS	M7266-0-1	1
SCALE								SHEET // OF //		DIST.	

DCSM 7266-0-1

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NOTES:

1. IN PDP11/04, 11/09 AND PDP 11/34, 11/39 SYSTEMS WITHOUT BATTERY BACKUP, TP1 SHOULD NOT BE CONNECTED TO THE POWER SUPPLY BATTERY STATUS SIGNAL UNLESS BOOTS ON ALL POWER RESTARTS ARE DESIRED.
2. IN ALL PDP11 SYSTEM WHOSE PROCESSOR MODULE DOES NOT CONTAIN GRANT PULL-UP RESISTORS, EITHER THE M9301 MUST BE POSITIONED IN THE TERMINATOR SLOT AT THE END OF THE UNIBUS FURTHEST FROM THE PROCESSOR OR JUMPERS W1 THRU W5 MUST BE INSTALLED, SO THE M9301 CAN BE INSERTED IN THE TERMINATOR SLOT NEXT TO THE PROCESSOR.
3. ON PDP11 SYSTEMS CONTAINING A BUS REPEATER OPTION, THE M9301 MUST BE INSTALLED ON THE PROCESSOR SIDE OF THE REPEATER SEE NOTE 2 FOR OTHER RESTRICTIONS.
4. JUMPER W6 SHOULD ONLY BE INSTALLED IN 11/55 AND 11/70 COMPUTERS WHOSE BACKPLANES HAVE BEEN MODIFIED FOR THIS MODULE. CONNECTOR PIN B01 IN THESE MODIFIED BACKPLANES WILL NOT BE TIED TO GROUND.
5. EARLIER VERSIONS USED PLASTIC SWITCH COVER (PN 121184-06) TO PREVENT INADVERTENT PROGRAMMING OF S1.



3					1-0-1026W		2		1	
REF	REF	REF	REF	REF	X-Y COORDINATE HOLE LOCATION			K-CO-M9301-0-4		1
REF	REF	REF	REF	REF	ASSY/DRILLING HOLE LAYOUT			D-AH-M9301-0-5		2
REF	REF	REF	REF	REF	MODULE ECO HISTORY			B-MH-M9301-0-6		3
1	1	1	1	1	ETCHED CIRCUIT BOARD			5011208		4
4	4	4	4	4	C1, C7, C14, C22	CAP .22 μ F 50V	1010274-01		5	
25	25	25	25	25	C2 THRU C6, C8 THRU C13 C15 THRU C21, C23 THRU C29	CAP .01 μ F 100V 20%	1001610-01		6	
2	2	2	2	2	C30, C37	CAP .001 μ F 250V 20%	1000043		7	
4	4	4	4	4	C32, C33, C38, C39	CAP 39 μ F 10V 10%	1000076		8	
3	3	3	3	3	C34, C35, C36	CAP 270 pF 100V 5%	1000022		9	
3	3	3	3	3	D1, D2, D3	DIODE D664 1N3606	1100114		10	
8	8	8	8	8	R1 THRU R8	RES 10K 1/4W 5%	1300479		11	
2	2	2	2	2	R9, R12	RES 1K 1/4W 5%	1300365		12	
4	4	4	4	4	R11, R13, R14, R18	RES 4.7K 1/4W 5%	1300447		13	
2	2	2	2	2	R16, R17	RES 30K 1/4W 5%	1302394		14	
3	3	3	3	3	R19, R21, R23	RES 47 Ω 1/4W 5%	1300202		15	
3	3	3	3	3	R20, R22, R24	RES 2K 1/4W 5%	1302388		16	
2	2	2	2	2	R25, R26	RES 383 Ω 1/4W 5%	1305125		17	
2	2	2	2	2	E1, E22	20 RES DIVIDER NETWORK	13-12628-00		18	
2	2	2	2	2	E7, E14	25 RES DIVIDER NETWORK	13-12628-01		19	
8	8	8	8	8	E2, E3, E9, E9 E15, E23, E27, E28	T.C. DEC 8861	1909705		20	
2	2	2	2	2	E4, E10	I.C. DEC 7402	1909004		21	
1	1	1	1	1	E13	I.C. DEC 7474	1905547		22	
3	3	3	3	3	E16, E18, E25	I.C. DEC 8837	1911116		23	
1	1	1	1	1	E17	I.C. DEC 7420	1905577		24	
2	2	2	2	2	E19, E24	I.C. DEC 314A	1909704		25	
1	1	1	1	1	E20	I.C. DEC 8640	1911469		26	
1	1	1	1	1	E21	I.C. DEC 9602	1910951		27	
1	1	1	1	1	E26	I.C. DEC 7437	1910091		28	
1	1	1	1	1	E29	I.C. DEC 7476	1905585		29	
1	1	1	1	1	SI	SWITCH, DIP, 10 POLE	1211164-06		30	
1	1	1	1	1		SWITCH COVER	1211164-03		31	
3	3	3	3	3		EYELET (FASTON)	9009000		32	
3	3	3	3	3	TP1, TP2, TP3	TAB (FASTON)	9007112		33	
2	2	2	2	2		HANDLE FLIP/CHIP MAGENTA	9008337-06		34	
4	4	4	4	4		EYELET	9006732		35	
1	1	1	1	1	C31	CAP 1 μ F 35V 10%	1001776		36	

[illegible]

John Bloom	7 Sept. 76				
J. BLOEM					
W. Arnold	9 AUG. 76				
SR M9301 - 00006	L				
J. Bloem					
J. Bloem	6-17-76				
M9301 - 00005	K				
J. Bloem	Apr. 12, 1976				
J. BLOEM					
J. Bloem	11 March 76				
SR M9301 - 00004	J				
J. Bloem	12-2-76				
JOHN BLOEM					
J. Carter	12-1-75				
M9301 - 00003	H				
J. Bloem	9-9-76				
JOHN BLOEM					
J. Carter	6/29/75				
REDESIGN-00002	F				
CHK	CHANGE NO.				
	REVISIONS				

[illegible]