

digital

DR11-K
Engineering Drawings
Digital Equipment Corporation

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SEQUENCE

B-DD-DR11K-Ø
A-PL-DR11K-Ø-Ø
D-CS-M7843-Ø-1
C-UA-BCØ8R-Ø-Ø
C-IA-5408776-Ø-Ø
B-CS-5408776-Ø-1
A-SP-DR11K-O-2

[illegible]

REVISIONS		
DATE	CHG. NO.	REV
1 - 75	DR11K-00001	A

USED ON OPTION/MODEL		DRN.	DATE	TITLE DR11K DIGITAL I/O									
		R. Iknaijan	10/31/74										
		CHK'D.	DATE										
		1-1-74	12-2-74										
		PROJ ENG.	DATE										
		Buss Iknaijan	11-15-74										
		PROD.	DATE	SIZE	CODE	NUMBER						REV	
		John Hess	12-3-74	B	DD	DR11K-Ø						A	
		FIELD SERV.	DATE	DIST									
		1-1-74	12-3-74										
SHEET 1 OF 2													

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NOTES:

1. VECTOR ADDRESS - PLACE SWITCH ON FOR LOGICAL ONE.
2. DEVICE ADDRESS - PLACE SWITCH OFF FOR LOGICAL ONE.
3. ANY SIGNAL NOT PRODUCED ON THE RESPECTIVE PRINT IS REFERENCED TO THE SOURCED PRINT BY THE PREFIX D1 THRU D8.
4. R118, R120 & R121 ARE CUSTOMER OPTIONS. DO NOT INSERT.
5. R119 IS AN ENGINEERING OPTION. DO NOT INSERT.
6. VARIATIONS-00&01 MAY BE SUBSTITUTED IF THE-02 IS NOT AVAILABLE SEE ITEM 44

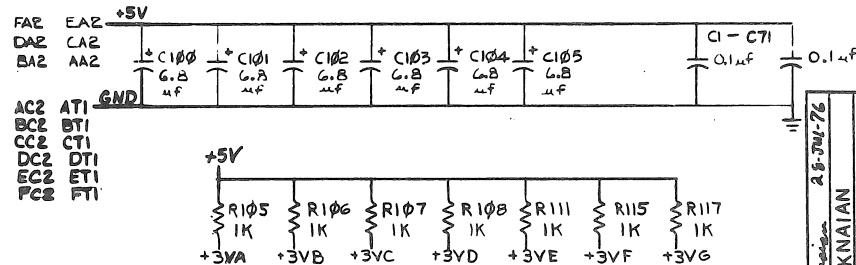
20 FI THRU F20 FUSE 1/4 AMP PICO 1210929-04 50

*DO NOT PUT EYELETS IN THESE FOUR HOLES

*X HOLES TYP. SURFACES

U. 44 REF F.B.S.

15.68 REF F.B.S.



DEC	74123	8	16
DEC	74157	8	16
DEC	74175	8	16
DEC	8266	8	16
DEC	8837	8	16
DEC	8838	8	16
DEC	8640	8	16
IC TYPE	GND	+5V	
GND AND 5V ARE USUALLY PIN 7 AND 14 RESPECTIVELY. EXCEPTIONS ARE STATED ABOVE			
IC PIN LOCATIONS			

DEC FORM NO. DDD 121-B

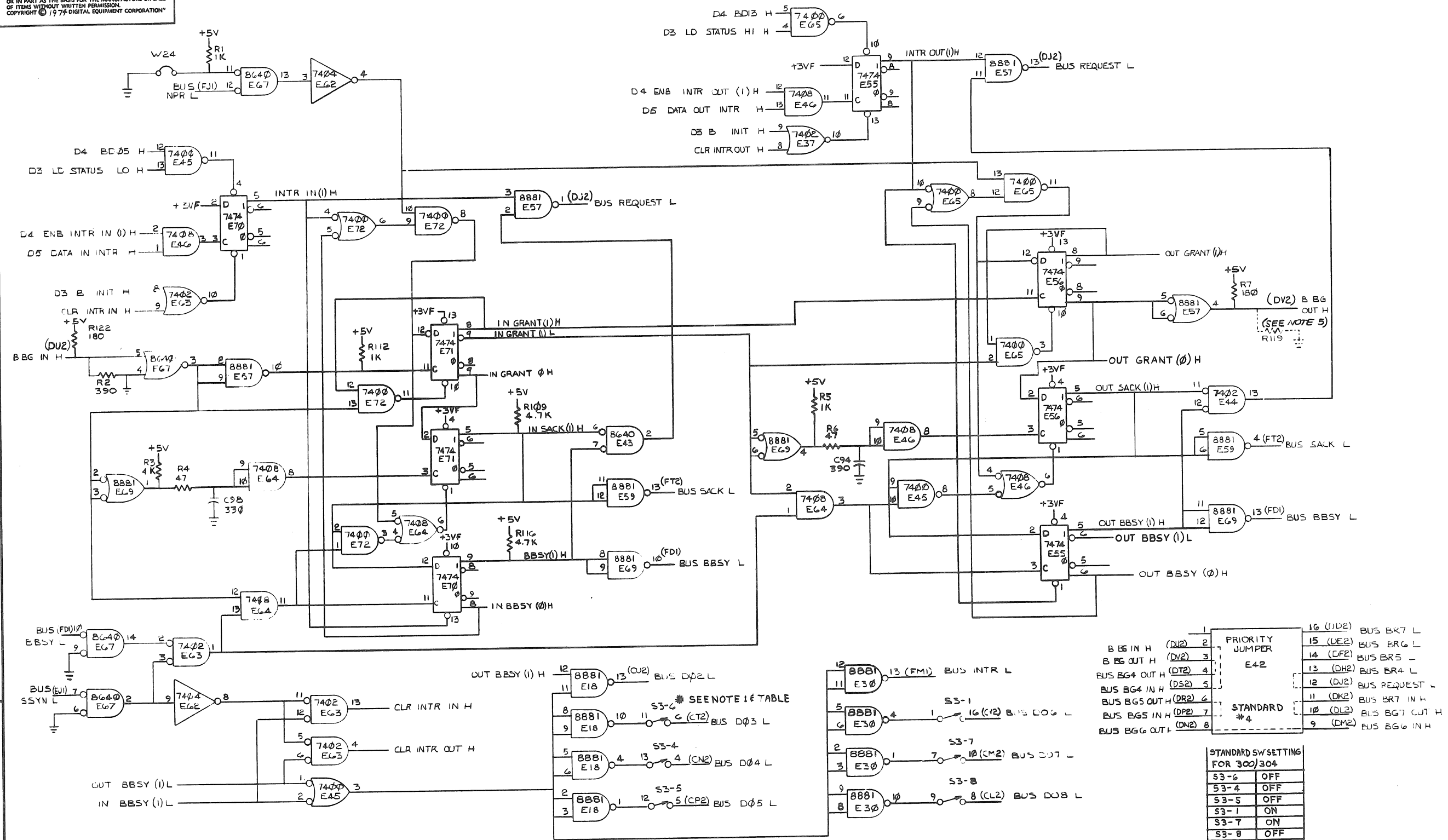
REF	DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
REF	ASSY/DRILLING HOLE LOCATION	K-CO-M7843-0-4		1
REF	MODULE ECO HISTORY	D-AH-M7843-0-5		2
1	ETCHED CIRCUIT BOARD	B-MH-M7843-0-6		3
9	C90 THRU C98	CAP 330PF 100V 5%	1000023	4
1	C99	CAP 470PF 100V 5%	1000024	5
10	C72 THRU C89	CAP 820PF 100V 5%	1000027	6
71	C1 THRU C71	CAP .01uF 100V 20% DISC	1001810-01	7
8	C100 THRU C105	CAP 6.8uF 35V 10%	1005308	8
1	E42	IC SOCKET 18 PINS	1209838	9
2	J1, J2	40 PIN BERG CONN	1209941-02	10
4	S1 THRU S4	SWITCH PACK (4-11V) 8 POSITION	1211184-04	11
10	R11, R13, R15, R17, R19, R21, R23, R25, R27, R31, R33, R35, R37, R39, R41, R43, R45, R47	RES 47 1/4W 1% FUSIBLE	1310881-02	12
2	R4, R6	RES 47 1/4W 5%	1300202	13
2	R109, R110	RES 4.7K 1/4W 5%	1300447	14
5	R0, R49 THRU R52	RES 100 1/4W 5%	1300229	15
2	R7, R122	RES 180 1/4W 5%	1301322	16
2	R53, R54	RES 330 1/4W 5%	1300295	17
1	R2	RES 380 1/4W 5%	1300309	18
20	R47, R55, R57, R59, R61, R63, R65, R67, R69, R71, R73, R75, R77, R79, R81, R83, R85, R87, R89, R91	RES 470 1/4W 5%	1300316	19
12	R1, R3, R5, R9, R105, R106, R107, R108, R111, R112, R115, R117	RES 1K 1/4W 5%	1300365	20
10	R10, R12, R14, R16, R18, R20, R22, R24, R26, R28, R30, R32, R34, R36, R38, R40, R42, R44	RES 3.3K 1/4W 5%	1300439	21
15	R48, R73, R74, R83 THRU R104	RES 10K 1/4W 5%	1300479	22
1	R110	RES 270 1/4W 5%	1301972	23
4	E45, E52, E65, E72	IC DEC 7400	1905575	24
3	E37, E63, E44	IC DEC 7402	1909004	25
3	E5, E54, E82	IC DEC 7404	1909686	26
3	E1, E7, E13	IC DEC 7405	1909930	27
2	E40, E84	IC DEC 7408	1910155	28
14	E2, E3, E8, E9, E10, E11, E14, E15, E19, E31, E55, E58, E70, E71	IC DEC 7474	1905547	29
1	E4	IC DEC 7486	1910011	30
2	E47, E49	IC DEC 74123	1910436	31
4	E21, E28, E33, E39	IC DEC 74157	1910655	32
2	E53, E81	IC DEC 74155	1910056	33
4	E22, E27, E34, E40	IC DEC 74175	1910651	34
3	E51, E80, E88	IC DEC 8242	1909712	35
4	E20, E35, E41, E48	IC DEC 7418	1909928	36
4	E20, E25, E32, E38	IC DEC 8266	1909934	37
5	E8, E12, E16, E58, E66	IC DEC 8037	1911110	38
4	E17, E23, E29, E36	IC DEC 8038	1911117	39
8	E10, E24, E30, E57, E59, E69	IC DEC 8881	1909705	40
3	E90, E87, E43	IC DEC 8840	1911469	41
5	D1 THRU D5 (SEE NOTE 6)	DIODE ARRAY DEC 2501-02	1910010-02	42
79		SPLIT LUGS	8008735	43
24	W1A THRU W23A, W24	JUMPER, # 22 AWG (INSULATED)	9009185	44
1		HANDLE (MEX)	1210711-02	45
8		EYELET (HANDLE)	9008732	46

FIRST USED ON OPTION MODEL

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
ETCH BOARD REV C				
DRN: J. Amell DATE: 10/10/74				
CHKD: J. Amell DATE: 10/16/74				
ENG: J. Amell DATE: 10/27/74				
PROJ. ENG: J. Amell DATE: 10/27/74				
PROD. J. Amell DATE: 11/29/74				
NEXT HIGHER ASSY				
TITLE: DRII-K DIGITAL I/O				
SIZE CODE: DCS M7843-0-1				
REV. D				
SCALE: 1 OF 8				
SHEET 1 OF 8				

SEMICONDUCTOR CONVERSION CHART

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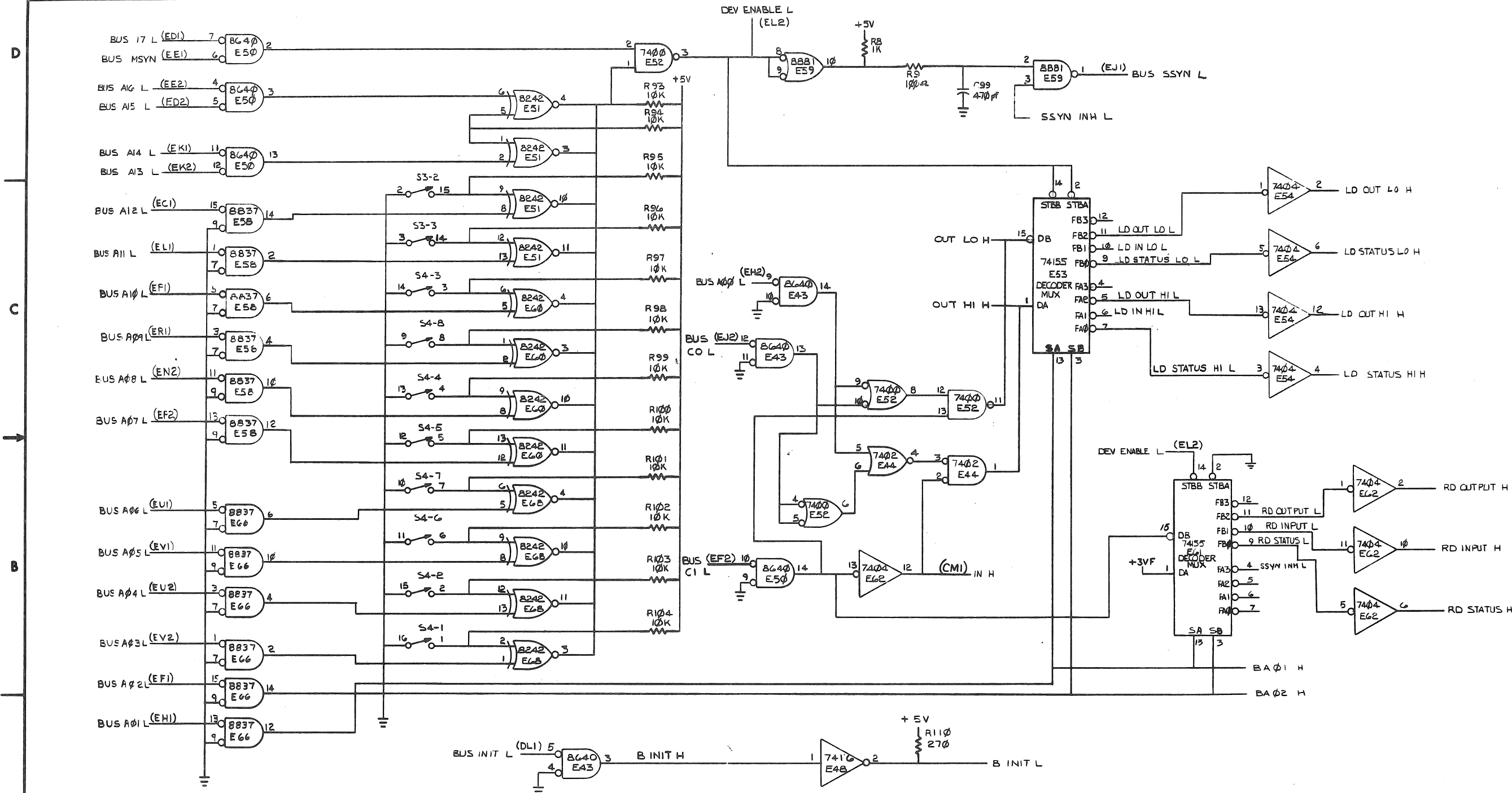


REVISIONS		
CHK	CHANGE NO.	REV.

(UNIBUS CONTROL)

TITLE	SIZE CODE	NUMBER	REV.
DR11-K DIGITAL I/O (D2)	D CS	M7843-0-1	D
SCALE	SHEET 2 OF 8	DIST.	

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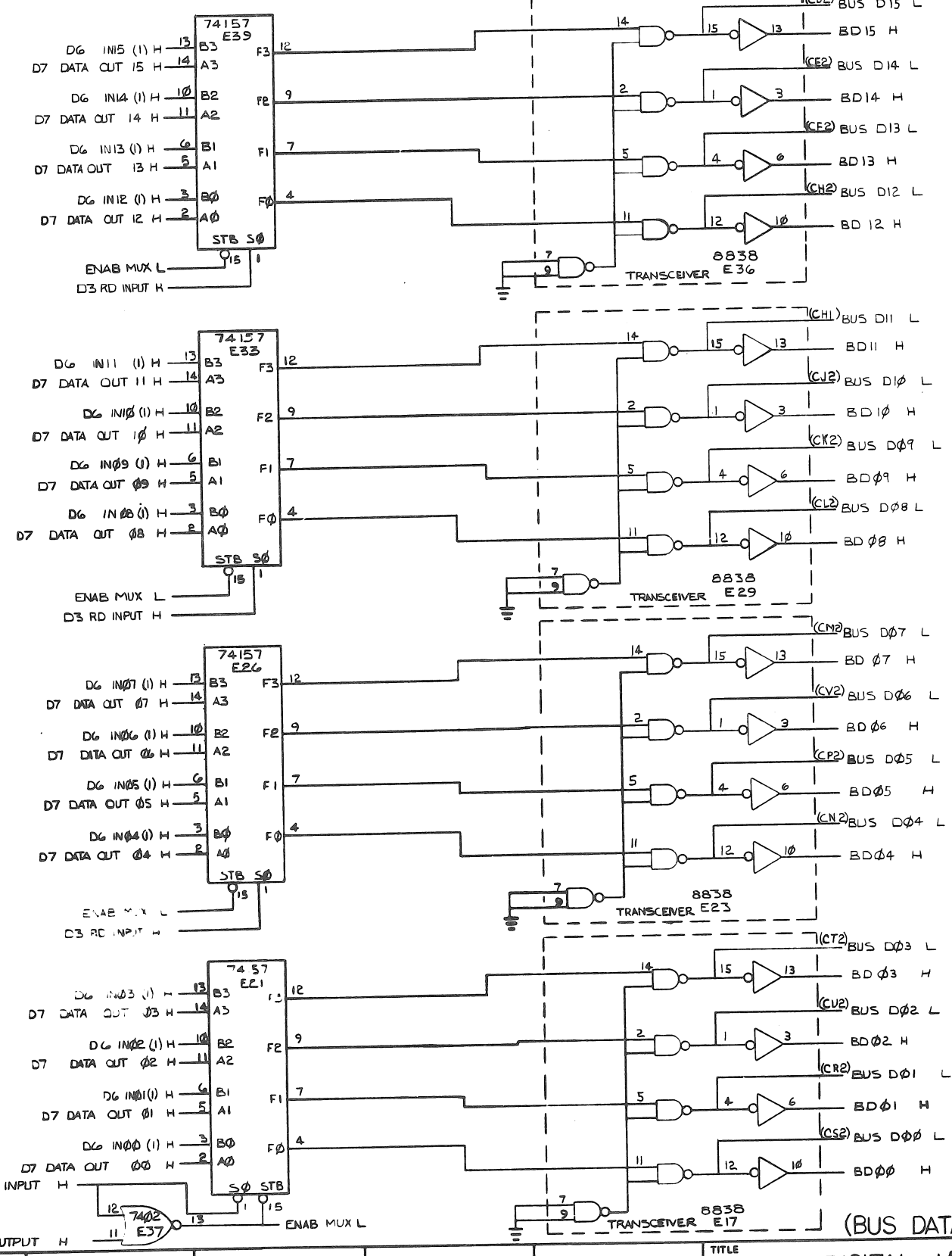
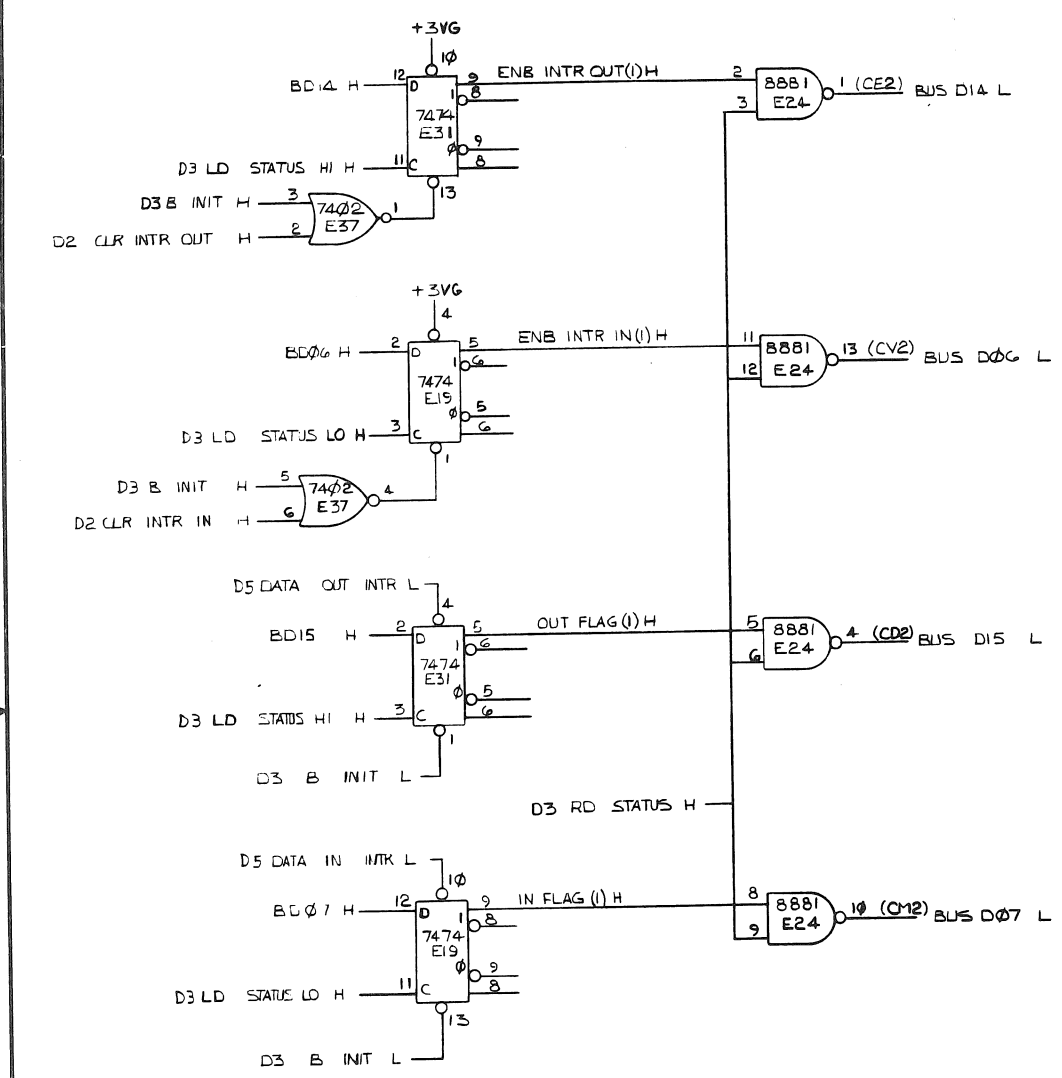


REVISIONS		
CHK	CHANGE NO.	REV.

(ADDRESS SELECTION)

TITLE	DR11-K DIGITAL I/O (D3)	SIZE CODE	D CS	NUMBER	M7843-0-1	REV.	D
SCALE	74	SHEET	3	OF	8	DIST.	

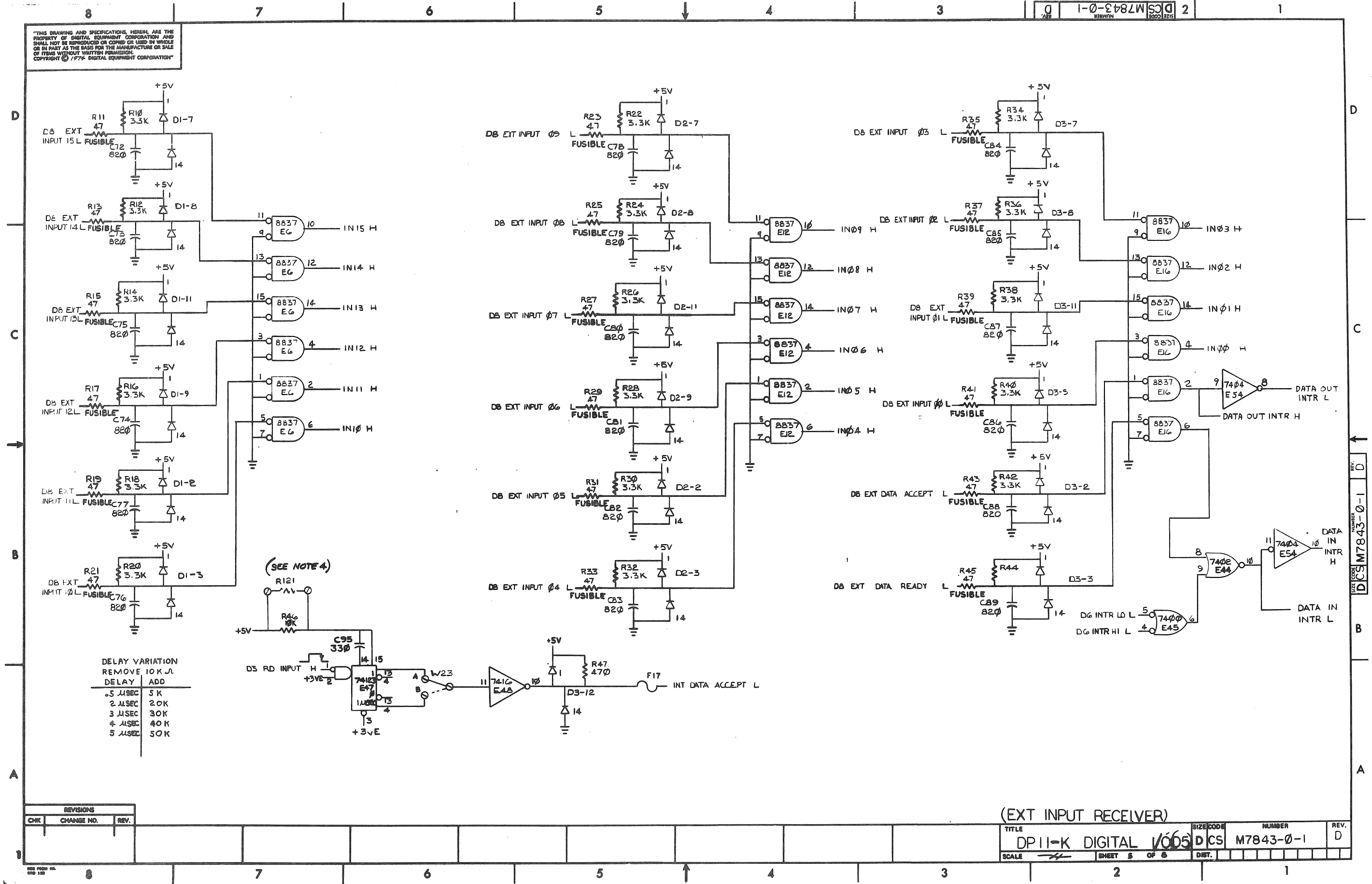
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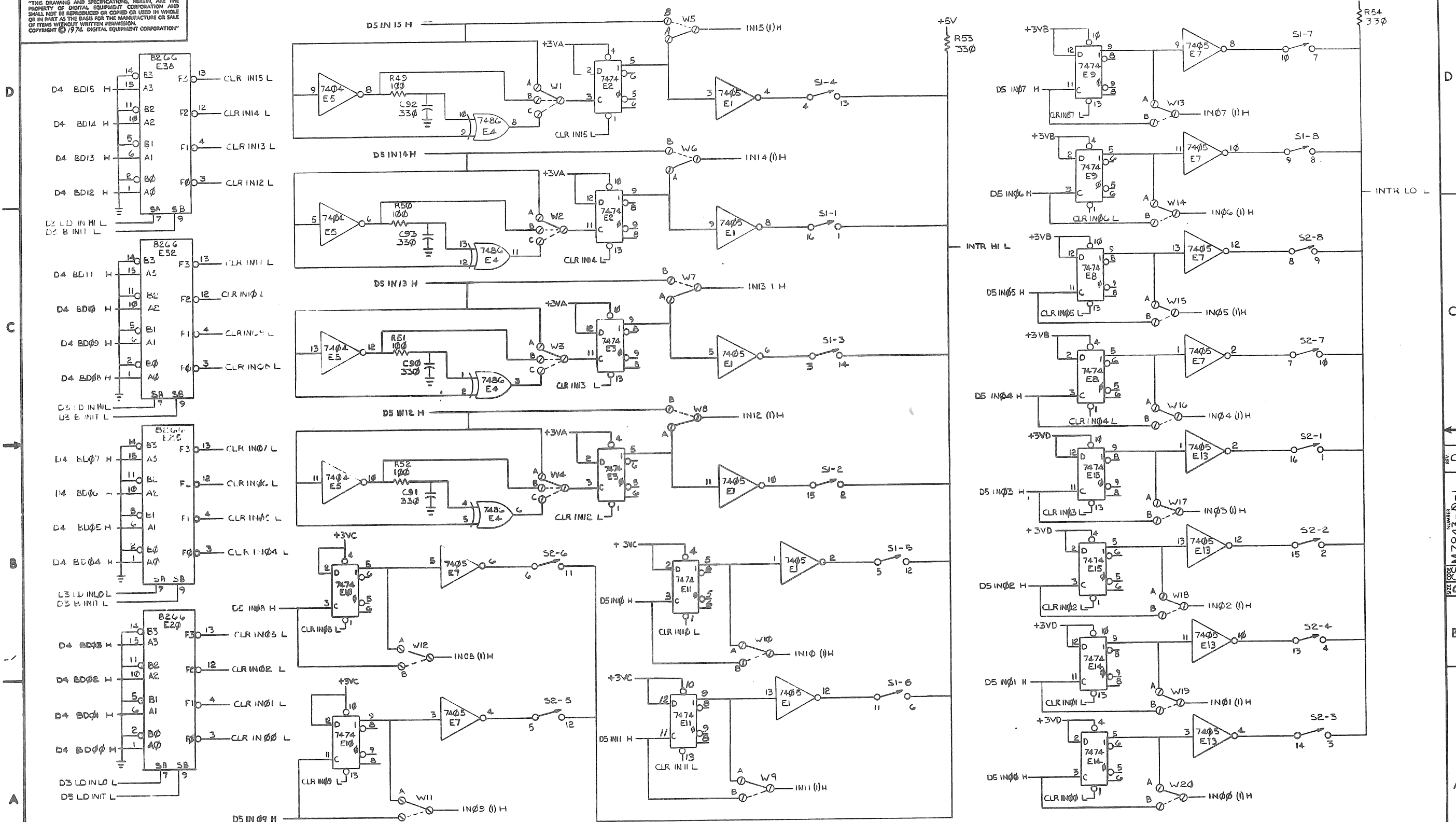
REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		SIZE CODE	NUMBER	REV.
DRIVEK DIGITAL 1004		DCS	M7843-0-1	D
SCALE	SHEET	OF	DIST.	
	4	8		

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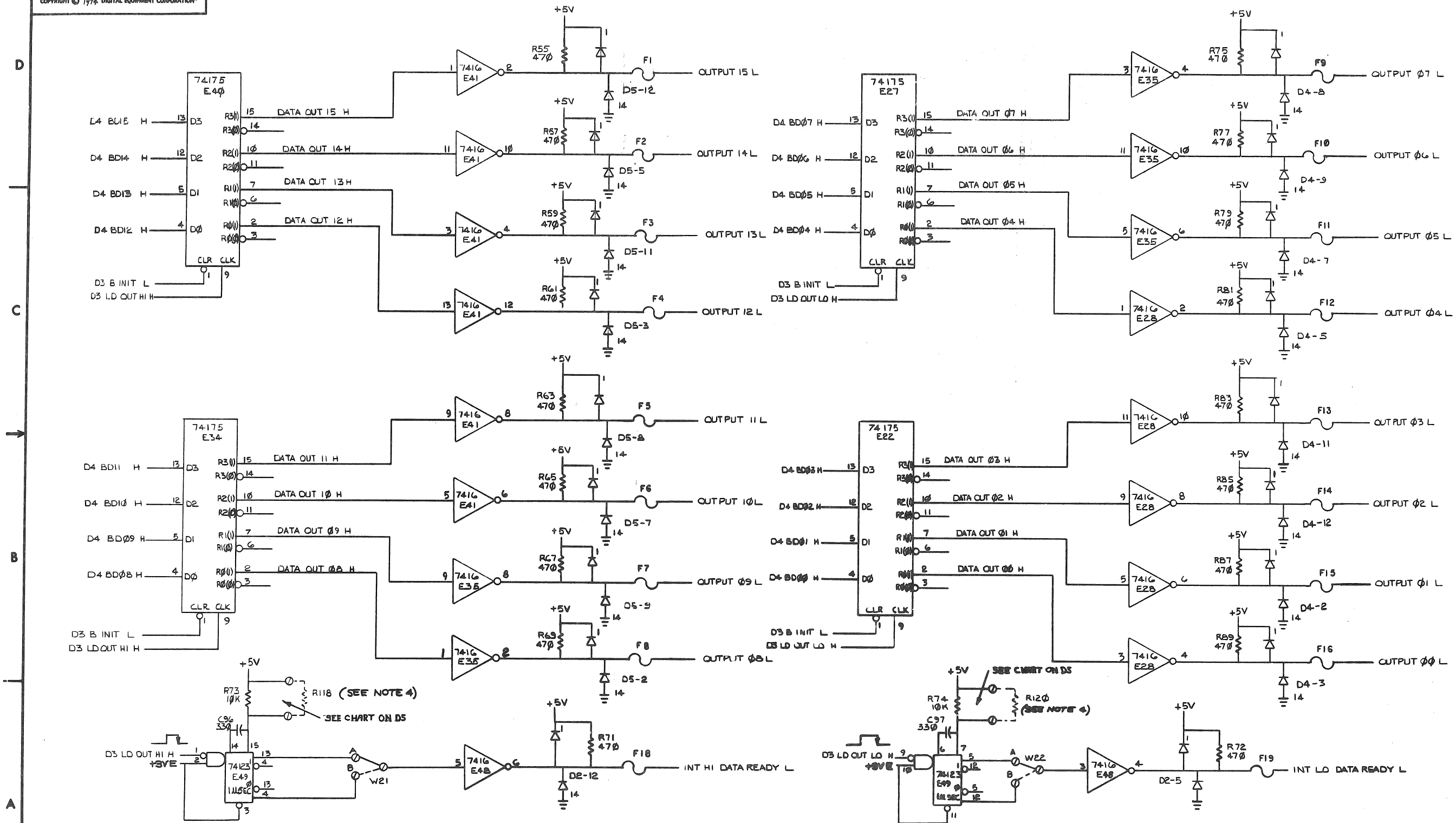
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REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		SIZE CODE	NUMBER	REV.
DRII-K DIGITAL I/O (D6)		D CS	M7843-0-1	D
SCALE		SHEET 6	OF 8	

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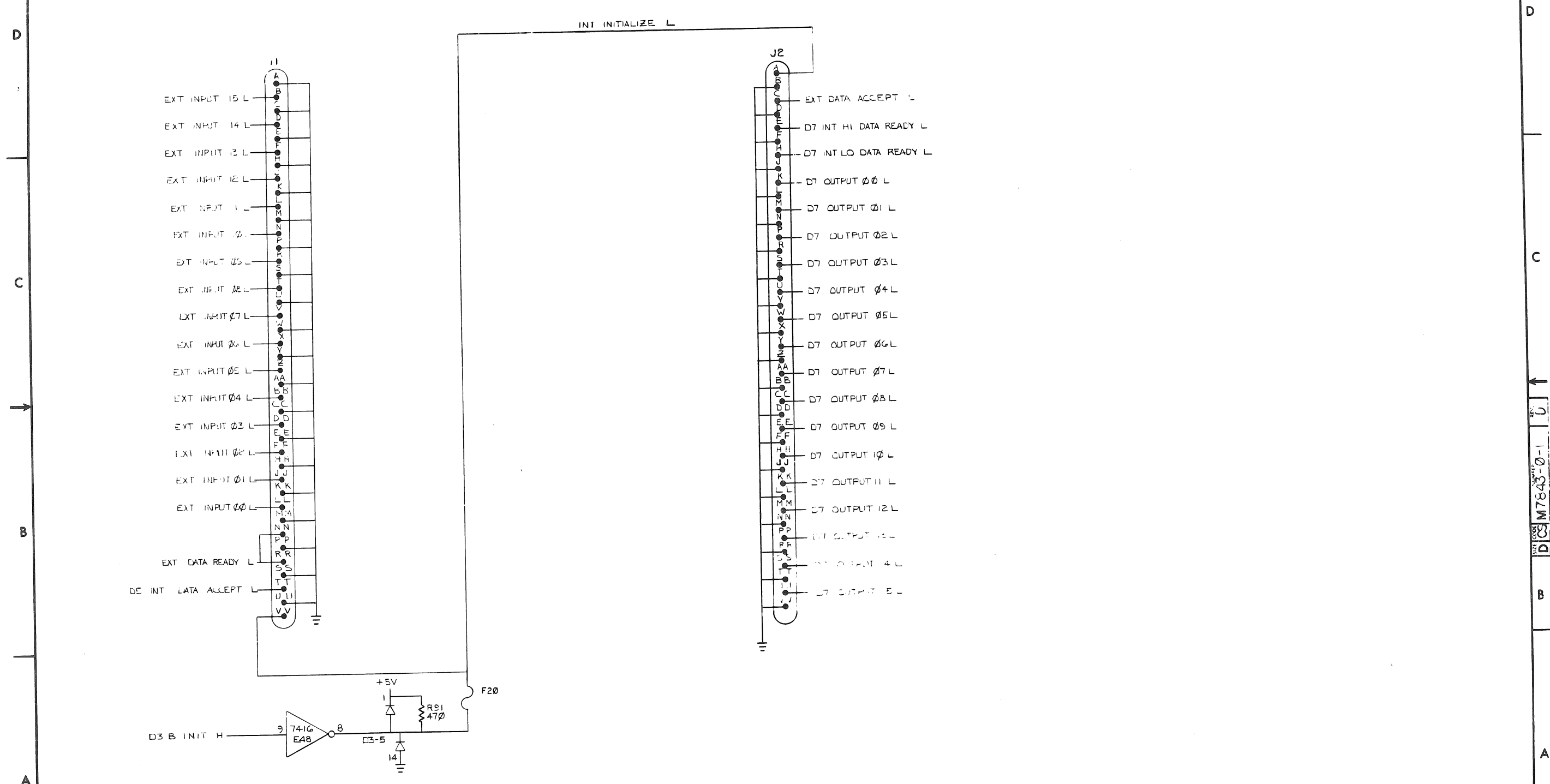


(OUTPUT REG)

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	SIZE CODE	NUMBER	REV.
DR11-K DIGITAL I/O (D7)	D	CS M7843-0-1	D
SCALE	SHEET 7	OF 8	DATE

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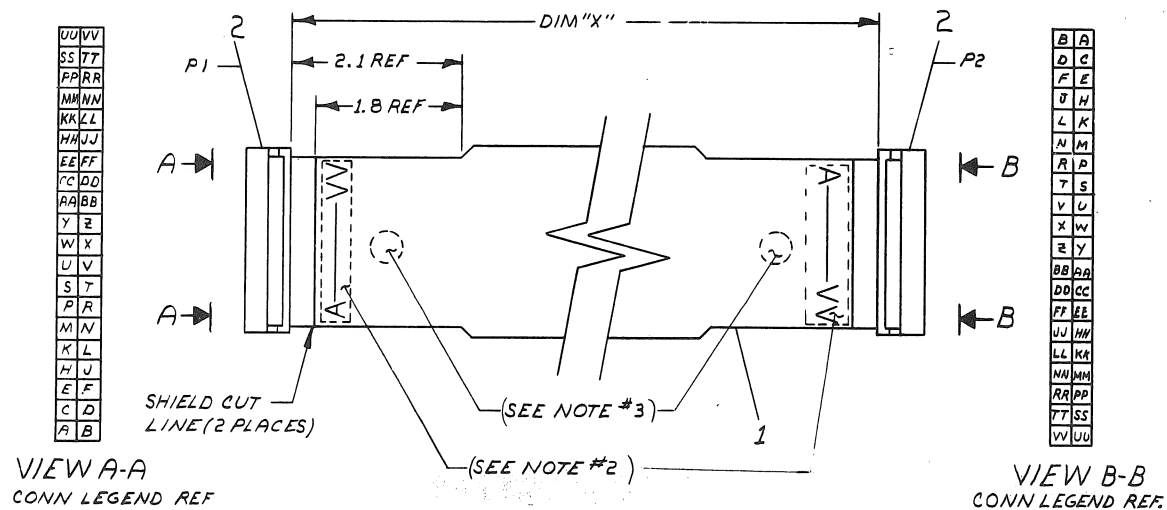
REVISIONS			(BERG PIN ASSIGN)										SIZE CODE		NUMBER		REV.
CHK	CHANGE NO.	REV.	TITLE										D CS		M7843-0-1		D
			DR11-K DIGITAL I/O (D8)														
			SCALE <i>TL</i>										SHEET 8 OF 8		DIST.		
													2		1		
DEC FORM NO. DND 135			8 7 6 5 4 3														

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WIRE TABLE			
FROM	TO	FROM	TO
P1-A	P2-VV	P1-Y	P2-X
P1-B	P2-UU	P1-Z	P2-W
P1-C	P2-TT	P1-AA	P2-V
P1-D	P2-SS	P1-BB	P2-U
P1-E	P2-RR	P1-CC	P2-T
P1-F	P2-PP	P1-DD	P2-S
P1-H	P2-NN	P1-EE	P2-R
P1-J	P2-MM	P1-FF	P2-P
P1-K	P2-LL	P1-HH	P2-N
P1-L	P2-KK	P1-JJ	P2-M
P1-M	P2-UU	P1-KK	P2-L
P1-N	P2-HH	P1-LL	P2-K
P1-P	P2-FF	P1-MM	P2-J
P1-R	P2-EE	P1-NN	P2-H
P1-S	P2-DD	P1-PP	P2-F
P1-T	P2-CC	P1-RR	P2-E
P1-U	P2-BB	P1-SS	P2-D
P1-V	P2-AA	P1-TT	P2-C
P1-W	P2-Z	P1-UU	P2-B
P1-X	P2-Y	P1-VV	P2-A

LEGEND		
NUMBER	DIM "X"	PRECUT LENGTH
BC08R-01	1.00 FT.	1.00 FT. 1.5 IN. 100 IN
BC08R-02	2.00 FT.	2.00 FT. 1.5 IN. 100 IN
BC08R-03	3.00 FT.	3.00 FT. 1.5 IN. 100 IN
BC08R-04	4.00 FT.	4.00 FT. 1.5 IN. 100 IN
BC08R-05	5.00 FT.	5.00 FT. 1.5 IN. 100 IN
BC08R-06	6.00 FT.	6.00 FT. 1.5 IN. 200 IN
BC08R-07	7.00 FT.	7.00 FT. 1.5 IN. 200 IN
BC08R-08	8.00 FT.	8.00 FT. 1.5 IN. 200 IN
BC08R-09	9.00 FT.	9.00 FT. 1.5 IN. 200 IN
BC08R-10	10.00 FT.	10.00 FT. 1.5 IN. 200 IN
BC08R-11	11.00 FT.	11.00 FT. 1.5 IN. 300 IN
BC08R-12	12.00 FT.	12.00 FT. 1.5 IN. 300 IN
BC08R-13	13.00 FT.	13.00 FT. 1.5 IN. 300 IN
BC08R-14	14.00 FT.	14.00 FT. 1.5 IN. 300 IN
BC08R-15	15.00 FT.	15.00 FT. 1.5 IN. 300 IN
BC08R-16	16.00 FT.	16.00 FT. 1.5 IN. 300 IN
BC08R-17	17.00 FT.	17.00 FT. 1.5 IN. 300 IN
BC08R-18	18.00 FT.	18.00 FT. 1.5 IN. 300 IN
BC08R-19	19.00 FT.	19.00 FT. 1.5 IN. 300 IN
BC08R-20	20.00 FT.	20.00 FT. 1.5 IN. 300 IN
BC08R-25	25.00 FT.	25.00 FT. 1.5 IN. 300 IN
BC08R-30	30.00 FT.	30.00 FT. 1.5 IN. 300 IN
BC08R-35	35.00 FT.	35.00 FT. 1.5 IN. 300 IN
BC08R-50	50.00 FT.	50.00 FT. 1.5 IN. 100 FT
BC08R-60	60.00 FT.	60.00 FT. 1.5 IN. 120 FT
BC08R-75	75.00 FT.	75.00 FT. 1.5 IN. 150 FT
BC08R-A0	100.00 FT.	100.00 FT. 1.5 IN. 200 FT
BC08R-A3	130.00 FT.	130.00 FT. 1.5 IN. 200 FT
BC08R-A6	160.00 FT.	160.00 FT. 1.5 IN. 200 FT

1. ASSEMBLE THIS CABLE PER PROCESS SPEC #7606485-0-0.
2. CONNECTOR LEGEND IDENTIFICATION TO BE PLACED ON SHIELD SIDE OF CABLE IN THIS AREA AS SHOWN.
3. INSPECTION & TEST STAMPS TO BE PLACED AT EACH END OF THE CABLE ASSY.

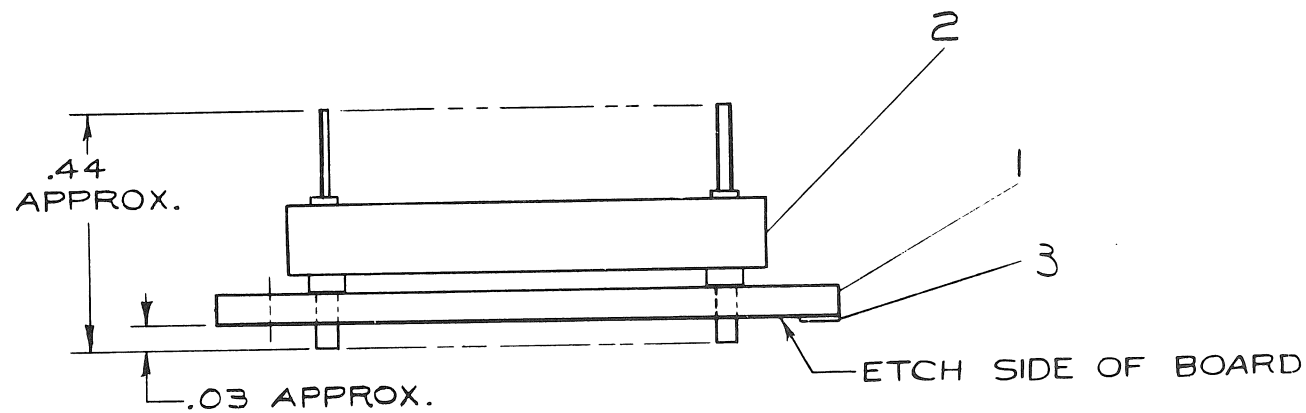
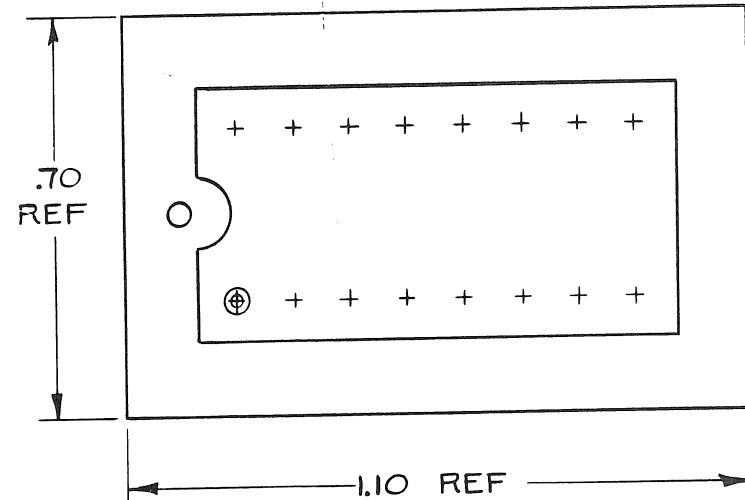


2	CONNECTOR, 40 SOCKET	12/12/06	2
1	BC08R-0-0	1700004	1
DESCRIPTION		DWG. PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES			
ANGLES ±0° 30'	CLASS OF ACCURACY (CHECK ONE)	NOMINAL DIMENSION RANGE INCHES	
SURFACE QUALITY IN	MEDIUM	OVER 0 TO 0.2	OVER 0.2 TO 1.2
MICROINCHES	PREFERRED	OVER 1.2 TO 4.0	OVER 4.0 TO 12.0
		OVER 12.0 TO 40.0	OVER 40.0 TO 80.0
		OVER 80.0 TO 160.0	OVER 160.0 TO 320.0
THIRD ANGLE PROJECTION	DRND. FONTAINE 8-28-70	FIRST USED ON	
REMOVE BURRS AND BREAK SHARP CORNERS	CHK'D J. FLEMING 8-28-70	TITLE	
DO NOT SCALE DWG	ENG'P GARDNER 9-3-70	BC08R	
MATERIAL	PROJ. ENG. GARDNER 9-3-70	10 CABLE	
FINISH	PROD. DONALD 9-4-70	NEXT HIGHER ASSY.	
		SIZE CODE	NUMBER
		D UA	BC08R-0-0
		SHEET	REV.
		1	L

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NOTES:

1. CUT OFF EXCESS LENGTH OF PRONGS BEFORE SOLDERING.



TOLERANCE
DECIMALS
xxx = $\pm .005$
xx = $\pm .02$
x = $\pm .1$

FIRST USED ON OPTION/MODEL

M78 ϕ

UNLESS OTHERWISE SPECIFIED

UNLESS OTHERWISE SPECIFIED

DIMENSION IN INCHES

TOLERANCES

DECIMALS FRACTIONS ANGLES

$\pm .005$ $\pm .001$ $\pm .001$ $\pm 0^{\circ}30'$

FINAL SURFACE QUALITY

REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL

SEE PARTS LIST

FINISH

DRN. *W. F. McCarthy* DATE *5/13/70*

CHK'D *J. Fleming* DATE *5-22-70*

ENG. *CRB* DATE *6/24/70*

PROJ. ENG. *CRB* DATE *6-26-70*

PROD. *Theresa Walsh* DATE *6/29/70*

NEXT HIGHER ASSY

M78 ϕ

SCALE *5/1*

SHEET *1* OF *1*

DIST. *G*

PARTS LIST

digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

TITLE

PRIORITY JUMPER

LEVEL # 4

SIZE CODE NUMBER REV.

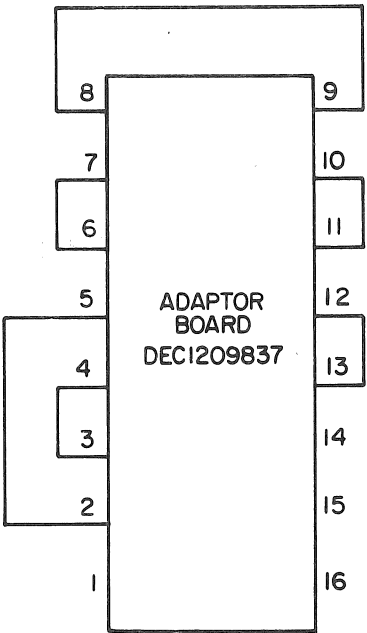
CIA5408776-0-0

DIST. *G*

REF	CIRCUIT SCHEMATIC	B-CS-5408776-0-1	
A/R	TAPE VINYL 3M TRANSPARENT .75 X 1.12	9008269	3
1	PLUG, 16 PRONG, AUGAT	1209837	2
1	ETCH BOARD	D-1A-5008775-0-0	1
QTY.	DESCRIPTION	PART NO.	ITEM NO.

REV.	CHANGE NO.	CHK

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REVISIONS	CHK	CHG	NO	REV	A		DRN.	DATE	TRANSISTOR & DIODE CONVERSION CHART				digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	TITLE PRIORITY JUMPER LEVEL			
	CHK	D					DATE	DEC	EIA	DEC	EIA	#4 5408776					
	ENG						DATE					SIZE		CODE	NUMBER	REV	
	PROD.						DATE					B		CS	5408776-0-1	A	
														PRINTED CIRCUIT REV. A B			

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS									
ENGINEERING SPECIFICATION									
TITLE PRELIMINARY DR11-K SPECIFICATION, CHECKOUT AND ACCEPTANCE PROCEDURE									
DATE 12/20/74									
REVISIONS									
REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE			
ENG <i>Tom Shriver</i> APPD <i>Bruce M. Shriver</i> SIZE CODE <i>A</i> SP <i>A</i> NUMBER DR11K-0-2 REV									
DEC 16-(382)-1079A-R873 DRA 107A SHEET 1 OF 5									

ENGINEERING SPECIFICATION																																										
TITLE PRELIMINARY DR11-K SPECIFICATION, CHECKOUT AND ACCEPTANCE PROCEDURE																																										
The DR11-K is a Digital I/O option consisting of a hex module M7843, and a BC08-R-1 cable. Various configurations are available which are selectable by solder jumper and PC micro switches. The device addresses, vector addresses and up to 16 input interrupts, are selected by the micro switches. The input register triggering and data paths are solder jumper selectable. Refer to A-SP-DR11-K-0-0 for detailed descriptions and operations.																																										
The M7843 will be assembled with a standard solder jumper configuration. The standard configuration is as follows:																																										
<table><tr><td>W1-A</td><td>W2-A</td><td>W3-A</td><td>W4-A</td><td>W5-A</td><td>W6-A</td><td>W7-A</td></tr><tr><td>W8-A</td><td>W9-A</td><td>W10-A</td><td>W11-A</td><td>W12-A</td><td>W13-A</td><td>W14-A</td></tr><tr><td>W15-A</td><td>W16-A</td><td>W17-A</td><td>W18-A</td><td>W19-A</td><td>W20-A</td><td>W21-A</td></tr><tr><td>W22-A</td><td>W23-A</td><td>W24</td><td></td><td></td><td></td><td></td></tr></table>										W1-A	W2-A	W3-A	W4-A	W5-A	W6-A	W7-A	W8-A	W9-A	W10-A	W11-A	W12-A	W13-A	W14-A	W15-A	W16-A	W17-A	W18-A	W19-A	W20-A	W21-A	W22-A	W23-A	W24									
W1-A	W2-A	W3-A	W4-A	W5-A	W6-A	W7-A																																				
W8-A	W9-A	W10-A	W11-A	W12-A	W13-A	W14-A																																				
W15-A	W16-A	W17-A	W18-A	W19-A	W20-A	W21-A																																				
W22-A	W23-A	W24																																								
The device address is selected by PC mounted micro switches. The standard addresses are 167770 for status register, 167772 for input register and 167774 for output register. The switch configuration is as follows (switch on = 0; switch off = 1):																																										
<table><tr><td></td><td>A12</td><td>A11</td><td>A10</td><td>A09</td><td>A08</td><td>A07</td><td>A06</td><td>A05</td><td>A04</td><td>A03</td></tr><tr><td>Switch</td><td>S3-2</td><td>S3-3</td><td>S4-3</td><td>S4-8</td><td>S4-4</td><td>S4-5</td><td>S4-7</td><td>S4-6</td><td>S4-2</td><td>S4-1</td></tr><tr><td>Position</td><td>On</td><td>Off</td><td>Off</td><td>Off</td><td>Off</td><td>Off</td><td>Off</td><td>Off</td><td>Off</td><td>Off</td></tr></table>											A12	A11	A10	A09	A08	A07	A06	A05	A04	A03	Switch	S3-2	S3-3	S4-3	S4-8	S4-4	S4-5	S4-7	S4-6	S4-2	S4-1	Position	On	Off	Off	Off	Off	Off	Off	Off	Off	Off
	A12	A11	A10	A09	A08	A07	A06	A05	A04	A03																																
Switch	S3-2	S3-3	S4-3	S4-8	S4-4	S4-5	S4-7	S4-6	S4-2	S4-1																																
Position	On	Off	Off	Off	Off	Off	Off	Off	Off	Off																																
DEC FORM NO DEC 16-(381)-1022-N370 DRA 108 SHEET 2 OF 5																																										

ENGINEERING SPECIFICATION																																											
TITLE PRELIMINARY DR11-K SPECIFICATION, CHECKOUT AND ACCEPTANCE PROCEDURE																																											
The vector address is selected by PC mounted micro switches. The standard vector addresses are 300 for input interrupt and 304 for output interrupt. The switch configuration is as follows (switch on = 1; switch off = 0):																																											
<table><tr><td></td><td>D8</td><td>D7</td><td>D6</td><td>D5</td><td>D4</td><td>D3</td></tr><tr><td>Position</td><td>Off</td><td>On</td><td>On</td><td>Off</td><td>Off</td><td>Off</td></tr><tr><td>Switch</td><td>S3-8</td><td>S3-7</td><td>S3-1</td><td>S3-5</td><td>S3-4</td><td>S3-6</td></tr></table>											D8	D7	D6	D5	D4	D3	Position	Off	On	On	Off	Off	Off	Switch	S3-8	S3-7	S3-1	S3-5	S3-4	S3-6													
	D8	D7	D6	D5	D4	D3																																					
Position	Off	On	On	Off	Off	Off																																					
Switch	S3-8	S3-7	S3-1	S3-5	S3-4	S3-6																																					
Each input bit can be selected by a PC mounted micro switch to generate an interrupt. The standard configuration is for all interrupt enable switches to be turned off. The following lists the interrupt enable switch for each input bit.																																											
<table><tr><td>INPUT REG BIT</td><td>SWITCH</td></tr><tr><td>15</td><td>S1-4</td></tr><tr><td>14</td><td>S1-1</td></tr><tr><td>13</td><td>S1-3</td></tr><tr><td>12</td><td>S1-2</td></tr><tr><td>11</td><td>S1-6</td></tr><tr><td>10</td><td>S1-5</td></tr><tr><td>9</td><td>S2-5</td></tr><tr><td>8</td><td>S2-6</td></tr><tr><td>7</td><td>S1-7</td></tr><tr><td>6</td><td>S1-8</td></tr><tr><td>5</td><td>S2-8</td></tr><tr><td>4</td><td>S2-7</td></tr><tr><td>3</td><td>S2-1</td></tr><tr><td>2</td><td>S2-2</td></tr><tr><td>1</td><td>S2-4</td></tr><tr><td>0</td><td>S2-3</td></tr></table>										INPUT REG BIT	SWITCH	15	S1-4	14	S1-1	13	S1-3	12	S1-2	11	S1-6	10	S1-5	9	S2-5	8	S2-6	7	S1-7	6	S1-8	5	S2-8	4	S2-7	3	S2-1	2	S2-2	1	S2-4	0	S2-3
INPUT REG BIT	SWITCH																																										
15	S1-4																																										
14	S1-1																																										
13	S1-3																																										
12	S1-2																																										
11	S1-6																																										
10	S1-5																																										
9	S2-5																																										
8	S2-6																																										
7	S1-7																																										
6	S1-8																																										
5	S2-8																																										
4	S2-7																																										
3	S2-1																																										
2	S2-2																																										
1	S2-4																																										
0	S2-3																																										
DEC FORM NO DEC 16-(381)-1022-N370 DRA 108 SHEET 3 OF 5																																											

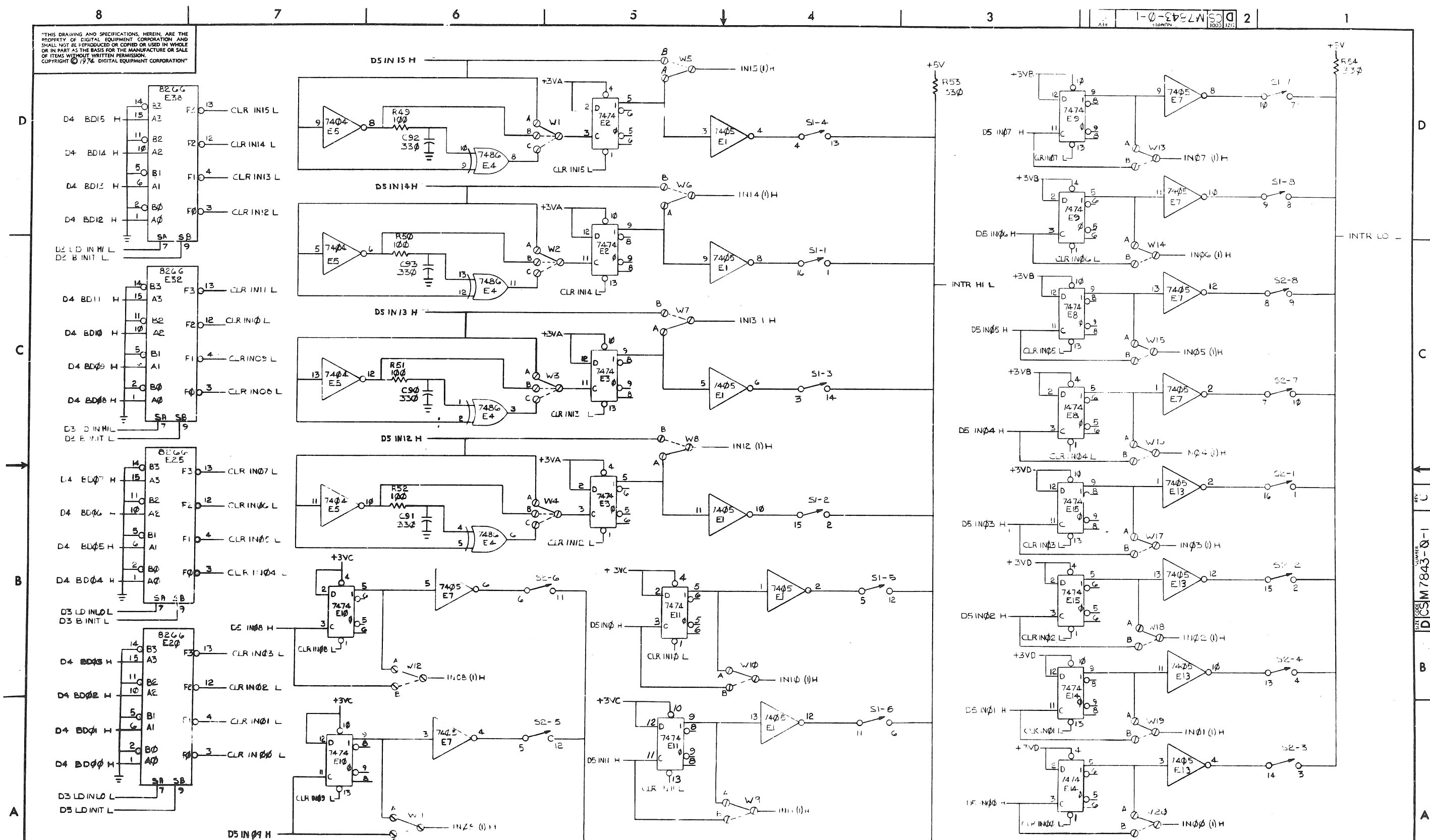
ENGINEERING SPECIFICATION									
TITLE PRELIMINARY DR11-K SPECIFICATION, CHECKOUT AND ACCEPTANCE PROCEDURE									
MAINTENANCE CABLE (BC08-R-1)									
This one foot cable is installed for running the diagnostic. It passes the output lines on J2 to the input lines on J1. J1 and J2 are H854 I/O connectors located at the top right of the M7843 module.									
TESTING (Standard Module Configuration)									
The diagnostic for the M7843 module is MAINDEC-11-DZDRG DR11-K Digital I/O test. Refer to the test write-up for loading procedure, starting procedure and test description. The procedure for checkout and acceptance is as follows:									
1. Set all the interrupt enable switches on.									
2. Start the diagnostic in the following test configuration (STANDARD SWITCH SETTING = 0000):									
a) No non-latching bits (000000)									
b) All input bits interrupting (177777)									
c) All inputs are negative (000000)									
d) All inputs are non-transition (000000)									
3. Run test for 5 minutes									
4. Set all the interrupt enable switches off									
5. Start the diagnostic in the following test configuration (STANDARD SWITCH SETTING = 0000):									
a) No non-latching bits (000000)									
b) All input bits non-interrupting (000000)									
c) All inputs are negative (000000)									
d) All inputs are non-transition (000000)									
6. Run test for 10 minutes									
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ENGINEERING SPECIFICATION		CONTINUATION SHEET																													
TITLE PRELIMINARY DR11-K SPECIFICATION, CHECKOUT AND ACCEPTANCE PROCEDURE																															
7. If the DR11-K is system installed run the DR11 module of DEC-X-11 as a system test in accordance with DEC-X-11 specifications. <u>COULTER MODEL "S" CONFIGURATION</u> When the DR11-K option is specifically ordered as a Coulter Model "S" interface, the following solder jumpers and switch configurations are performed. <u>Solder Jumper</u> <table><tr><td>W1-C</td><td>W2-C</td><td>W3-C</td><td>W4-C</td><td></td><td></td></tr><tr><td>W5-B</td><td>W6-B</td><td>W7-B</td><td>W8-B</td><td>W9-B</td><td>W10-B</td></tr><tr><td>W11-B</td><td>W12-B</td><td>W13-B</td><td>W14-B</td><td>W15-B</td><td>W16-B</td></tr><tr><td>W17-B</td><td>W18-B</td><td>W19-B</td><td>W20-B</td><td></td><td></td></tr></table> <u>Switches On</u> <table><tr><td>S1-4</td><td>S1-1</td><td>S1-3</td><td>S1-2</td></tr></table> <u>TESTING A COULTER CONFIGURED DR11-K</u> - Set up the M7847 for Coulter Configuration - Start the diagnostic in the following test configurations: - All input bits are non-latching (177777) - Bits 15 through 12 are interrupting (170000) - Bits 11 through 00 are negative inputs (000000) - Bits 15 through 12 are transition inputs (170000) - Run the diagnostic for 15 minutes.				W1-C	W2-C	W3-C	W4-C			W5-B	W6-B	W7-B	W8-B	W9-B	W10-B	W11-B	W12-B	W13-B	W14-B	W15-B	W16-B	W17-B	W18-B	W19-B	W20-B			S1-4	S1-1	S1-3	S1-2
W1-C	W2-C	W3-C	W4-C																												
W5-B	W6-B	W7-B	W8-B	W9-B	W10-B																										
W11-B	W12-B	W13-B	W14-B	W15-B	W16-B																										
W17-B	W18-B	W19-B	W20-B																												
S1-4	S1-1	S1-3	S1-2																												
SIZE		CODE	REV																												
A		Sp	DR11K-0-2																												
NUMBER		REV																													

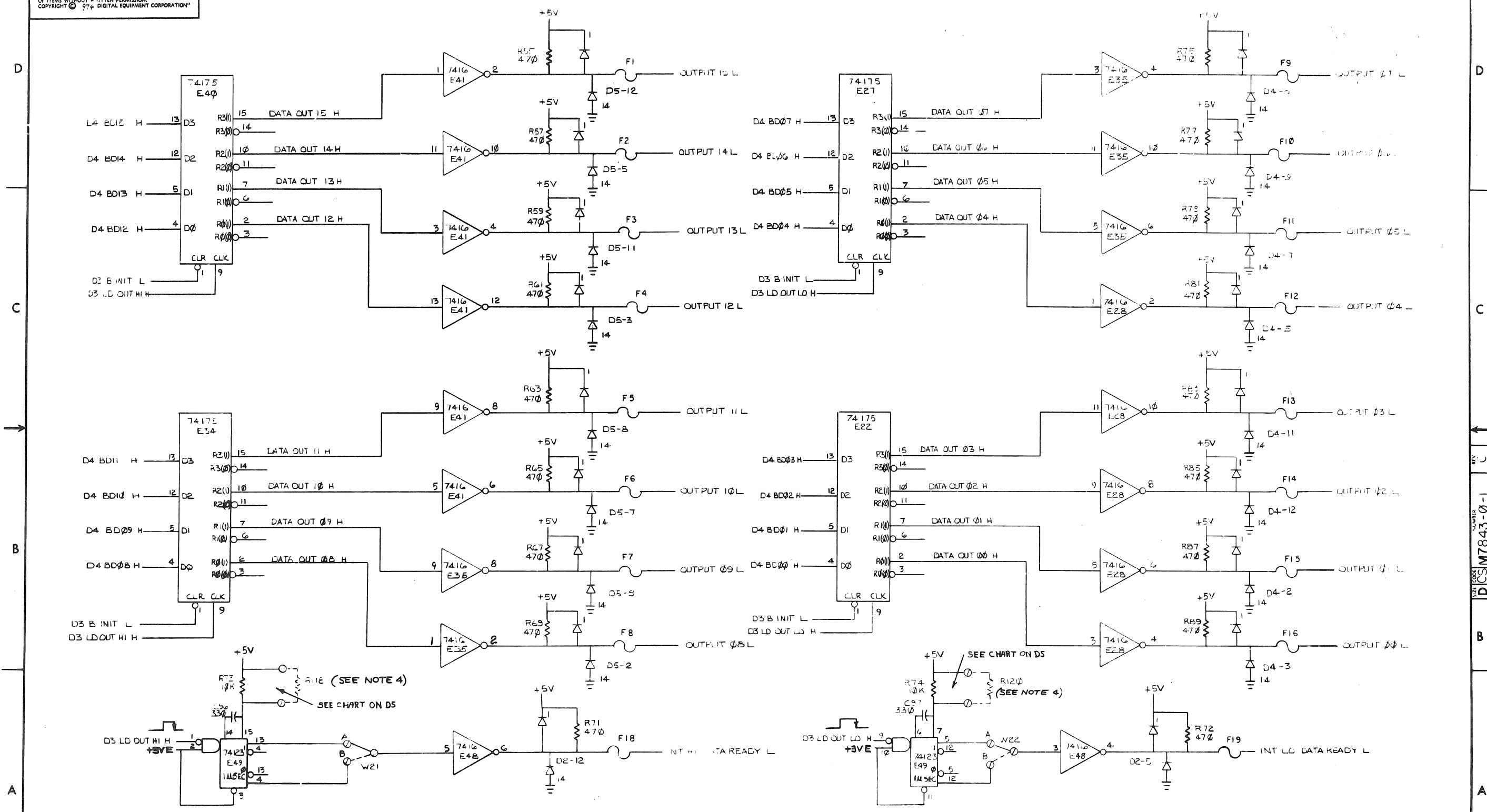
DEC FORM NO DEC 16-(381)-1022-N370
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SHEET 1 OF 1

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SCALE		SHEET 7	OF 8	DIST.

REV. C
D CS M7843-0-1

